



# SPEC® CINT2006 Result

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## Huawei

SPECint®\_rate2006 = 2390

Huawei 2288H V5 (Intel Xeon Platinum 8170)

SPECint\_rate\_base2006 = 2260

CPU2006 license: 3175

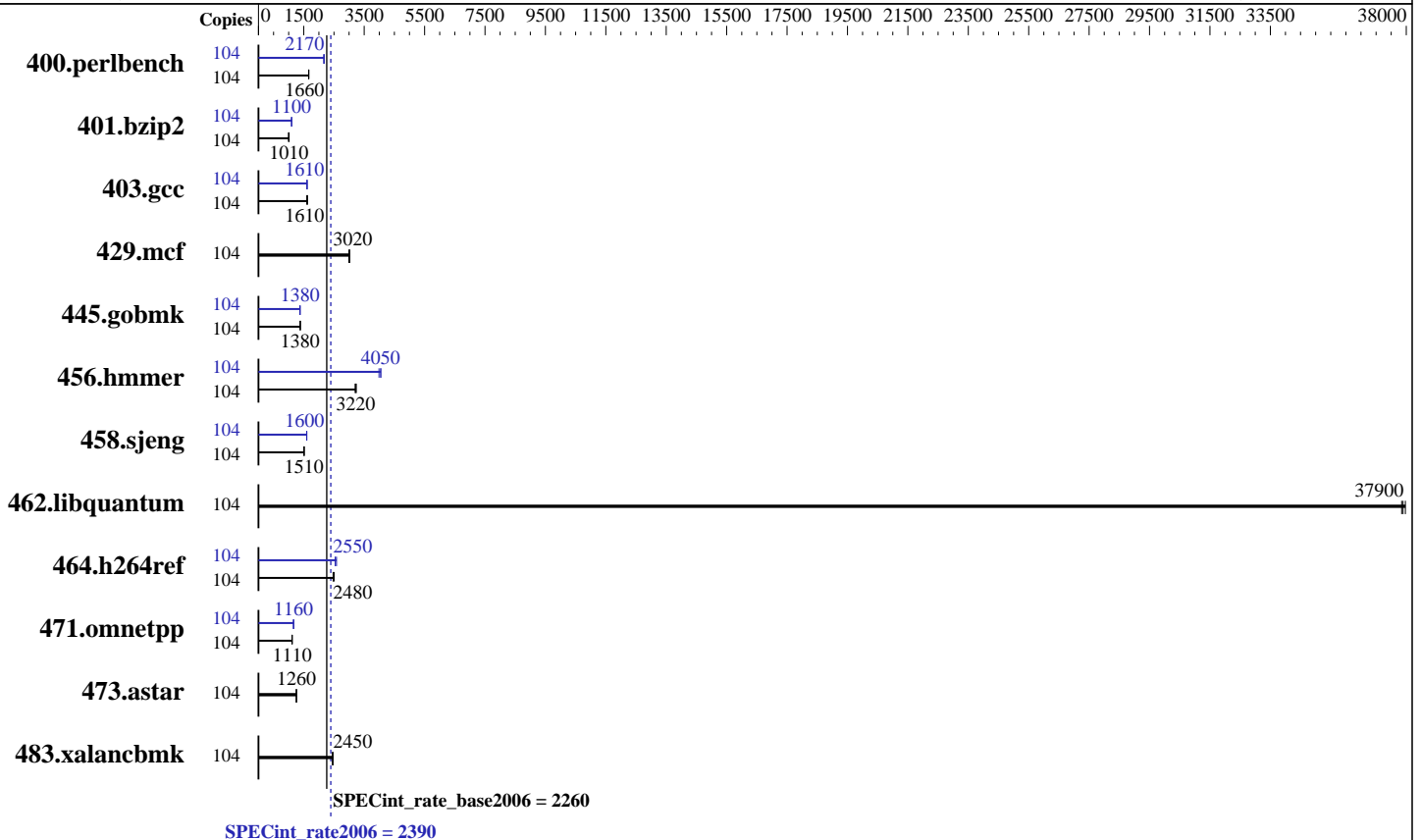
Test sponsor: Huawei

Tested by: Huawei

Test date: Jun-2017

Hardware Availability: Aug-2017

Software Availability: Nov-2016



### Hardware

CPU Name: Intel Xeon Platinum 8170  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 52 cores, 2 chips, 26 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 35.75 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)  
 Disk Subsystem: 1 x 1200 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 3.10.0-514.el7.x86\_64  
 Compiler: C/C++: Version 17.0.1.132 of Intel C/C++ Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	104	609	1670	<b><u>611</u></b>	<b><u>1660</u></b>	612	1660	104	<b><u>469</u></b>	<b><u>2170</u></b>	469	2160	467	2170
401.bzip2	104	1011	992	989	1010	<b><u>995</u></b>	<b><u>1010</u></b>	104	916	1100	<b><u>914</u></b>	<b><u>1100</u></b>	911	1100
403.gcc	104	515	1620	<b><u>520</u></b>	<b><u>1610</u></b>	522	1600	104	519	1610	522	1600	<b><u>520</u></b>	<b><u>1610</u></b>
429.mcf	104	317	2990	314	3020	<b><u>314</u></b>	<b><u>3020</u></b>	104	317	2990	314	3020	<b><u>314</u></b>	<b><u>3020</u></b>
445.gobmk	104	<b><u>789</u></b>	<b><u>1380</u></b>	788	1380	789	1380	104	792	1380	<b><u>793</u></b>	<b><u>1380</u></b>	794	1370
456.hammer	104	300	3240	303	3200	<b><u>301</u></b>	<b><u>3220</u></b>	104	243	3990	240	4050	<b><u>240</u></b>	<b><u>4050</u></b>
458.sjeng	104	833	1510	834	1510	<b><u>833</u></b>	<b><u>1510</u></b>	104	787	1600	789	1590	<b><u>787</u></b>	<b><u>1600</u></b>
462.libquantum	104	<b><u>56.9</u></b>	<b><u>37900</u></b>	56.8	38000	56.9	37800	104	<b><u>56.9</u></b>	<b><u>37900</u></b>	56.8	38000	56.9	37800
464.h264ref	104	<b><u>927</u></b>	<b><u>2480</u></b>	919	2510	931	2470	104	891	2580	<b><u>904</u></b>	<b><u>2550</u></b>	905	2540
471.omnetpp	104	584	1110	<b><u>583</u></b>	<b><u>1110</u></b>	583	1110	104	559	1160	560	1160	<b><u>559</u></b>	<b><u>1160</u></b>
473.astar	104	582	1250	<b><u>581</u></b>	<b><u>1260</u></b>	581	1260	104	582	1250	<b><u>581</u></b>	<b><u>1260</u></b>	581	1260
483.xalancbmk	104	291	2470	293	2450	<b><u>293</u></b>	<b><u>2450</u></b>	104	291	2470	293	2450	<b><u>293</u></b>	<b><u>2450</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Set SNC to Enable  
Set IMC Interleaving to 1 way  
Set Patrol Scrub to Disable  
Sysinfo program /spec17/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on localhost.localdomain Fri Jun 16 05:06:47 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Platinum 8170 CPU @ 2.10GHz  
2 "physical id"s (chips)  
104 "processors"

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## Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 26

siblings : 52

physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25  
26 27 28 29

physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25  
26 27 28 29

cache size : 36608 KB

From /proc/meminfo

MemTotal: 790481628 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.3 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.3"

PRETTY\_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.3:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.3:ga:server

uname -a:

Linux localhost.localdomain 3.10.0-514.el7.x86\_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Jun 15 18:29

SPEC is set to: /spec17

Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda2 xfs 898G 16G 883G 2% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.13 04/11/2017

Memory:

24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec17/lib/ia32:/spec17/lib/intel64:/spec17/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -D\_FILE\_OFFSET\_BITS=64

429.mcf: -D\_FILE\_OFFSET\_BITS=64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

464.h264ref: -D\_FILE\_OFFSET\_BITS=64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -D\_FILE\_OFFSET\_BITS=64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)

-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)

-no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

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## Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

403.gcc: -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-mem-layout-trans=3

456.hmmmer: -xCORE-AVX512 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto-ilp32  
-qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2)  
-qopt-ra-region-strategy=block  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs  
-L/sh10.2 -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.xml>

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