SPEC® CINT2006 Result
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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant ML30 Gen9
(3.90 GHz, Intel Xeon E3-1280 v6)

$SPECint^{®}\_rate2006 =$ Not Run

$SPECint\_rate\_base2006 =$ 272

CPU2006 license: 3	**Test date:** Mar-2017
Test sponsor: HPE	**Hardware Availability:** May-2017
Tested by: HPE	**Software Availability:** Nov-2016

Copies

Benchmark	Copies	Result
400.perlbench	8	215
401.bzip2	8	123
403.gcc	8	222
429.mcf	8	329
445.gobmk	8	180
456.hmmer	8	443
458.sjeng	8	189
462.libquantum	8	2860
464.h264ref	8	348
471.omnetpp	8	121
473.astar	8	144
483.xalancbmk	8	318

SPECint_rate_base2006 = 272

Hardware
CPU Name:	Intel Xeon E3-1280 v6
CPU Characteristics:	Intel Turbo Boost Technology up to 4.20 GHz
CPU MHz:	3900
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (4 x 16 GB 2Rx8 PC4-2400T-E)
Disk Subsystem:	1 x 1 TB SATA 7.2 K RPM, RAID 0
Other Hardware:	None

Software
Operating System:	Red Hat Enterprise Linux Server 7.3 Kernel 3.10.0-514
Compiler:	C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V10.2

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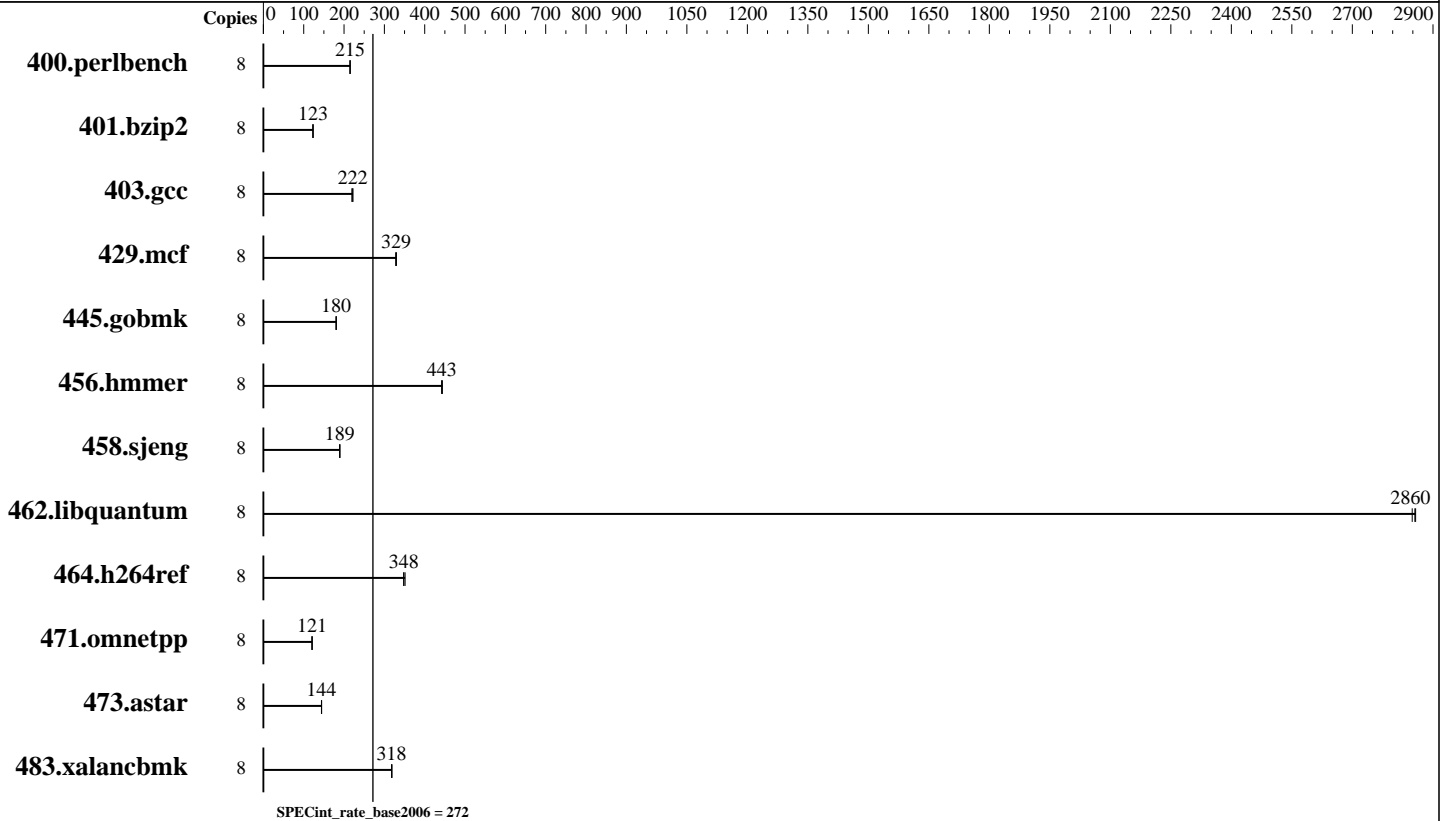**Test sponsor:** HPE
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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	364	215	**364**	**215**	363	215							
401.bzip2	8	**627**	**123**	630	123	626	123							
403.gcc	8	294	219	290	222	**291**	**222**							
429.mcf	8	221	330	**222**	**329**	222	328							
445.gobmk	8	465	180	**465**	**180**	464	181							
456.hmmer	8	168	444	**169**	**443**	169	442							
458.sjeng	8	**511**	**189**	511	189	511	189							
462.libquantum	8	58.0	2860	58.2	2850	**58.1**	**2860**							
464.h264ref	8	509	348	504	351	**508**	**348**							
471.omnetpp	8	**414**	**121**	415	121	414	121							
473.astar	8	389	144	**389**	**144**	390	144							
483.xalancbmk	8	173	319	174	318	**174**	**318**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
echo 1 >   /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Platform Notes

BIOS Configuration:
    Power Profile set to Custom
    Minimum Processor Idle Power Core C-State set to C3 State
    Minimum Processor Idle Power Package C-State set to No Package State
    Energy/Performance Bias set to Maximum Performance
    Thermal Configuration set to Maximum Cooling
    Processor Power and Utilization Monitoring set to Disabled
    Memory Double Refresh Rate set to 1x Refresh
    NUMA Group Size Optimization set to Flat

    Sysinfo program /hpe/cpu2006/config/sysinfo.rev6993
    Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

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Platform Notes (Continued)

```
running on ML30-G9-RHEL7.3 Mon Mar 27 01:40:29 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1280 v6 @ 3.90GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

From /proc/meminfo
   MemTotal:       65585572 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.3 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.3"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

uname -a:
   Linux ML30-G9-RHEL7.3 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT
   2016 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 25 11:22

SPEC is set to: /hpe/cpu2006
   Filesystem      Type   Size  Used Avail Use% Mounted on
   /dev/sda5       xfs    500G  126G  375G  26% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.
```

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Platform Notes (Continued)

```
   BIOS HP U23 01/17/2017
   Memory:
    4x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/hpe/cpu2006/libs/32:/hpe/cpu2006/libs/64:/hpe/cpu2006/sh10.2"

 Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
```

Base Compiler Invocation

C benchmarks:
```
    icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

C++ benchmarks:
```
    icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Base Portability Flags

```
    400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
      401.bzip2: -D_FILE_OFFSET_BITS=64
       403.gcc: -D_FILE_OFFSET_BITS=64
       429.mcf: -D_FILE_OFFSET_BITS=64
     445.gobmk: -D_FILE_OFFSET_BITS=64
     456.hmmer: -D_FILE_OFFSET_BITS=64
     458.sjeng: -D_FILE_OFFSET_BITS=64
   462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
    464.h264ref: -D_FILE_OFFSET_BITS=64
    471.omnetpp: -D_FILE_OFFSET_BITS=64
      473.astar: -D_FILE_OFFSET_BITS=64
   483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -no-prec-sqrt -static
    -qopt-prefetch -qopt-mem-layout-trans=3 -auto-ilp32
    -complex-limited-range -qopt-prefetch-issue-excl-hint -ansi-alias
    -unroll-aggressive -use-intel-optimized-headers -qopt-matmul
    -qopt-subscript-in-range -qopt-assume-safe-padding -qopt-calloc
```

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Base Optimization Flags (Continued)

C benchmarks (continued):
```
-inline-calloc -qopt-malloc-options=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -use-intel-optimized-headers
-unroll-aggressive -qopt-calloc -inline-calloc -qopt-malloc-options=3
-Wl,-z,muldefs -L/hpe/cpu2006/sh10.2 -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HPE-Compiler-Flags-Intel-V1.2-HSW-revI.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-HSW-revG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HPE-Compiler-Flags-Intel-V1.2-HSW-revI.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-HSW-revG.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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