SPEC® CINT2006 Result

Hewlett Packard Enterprise

(Test Sponsor: HPE)

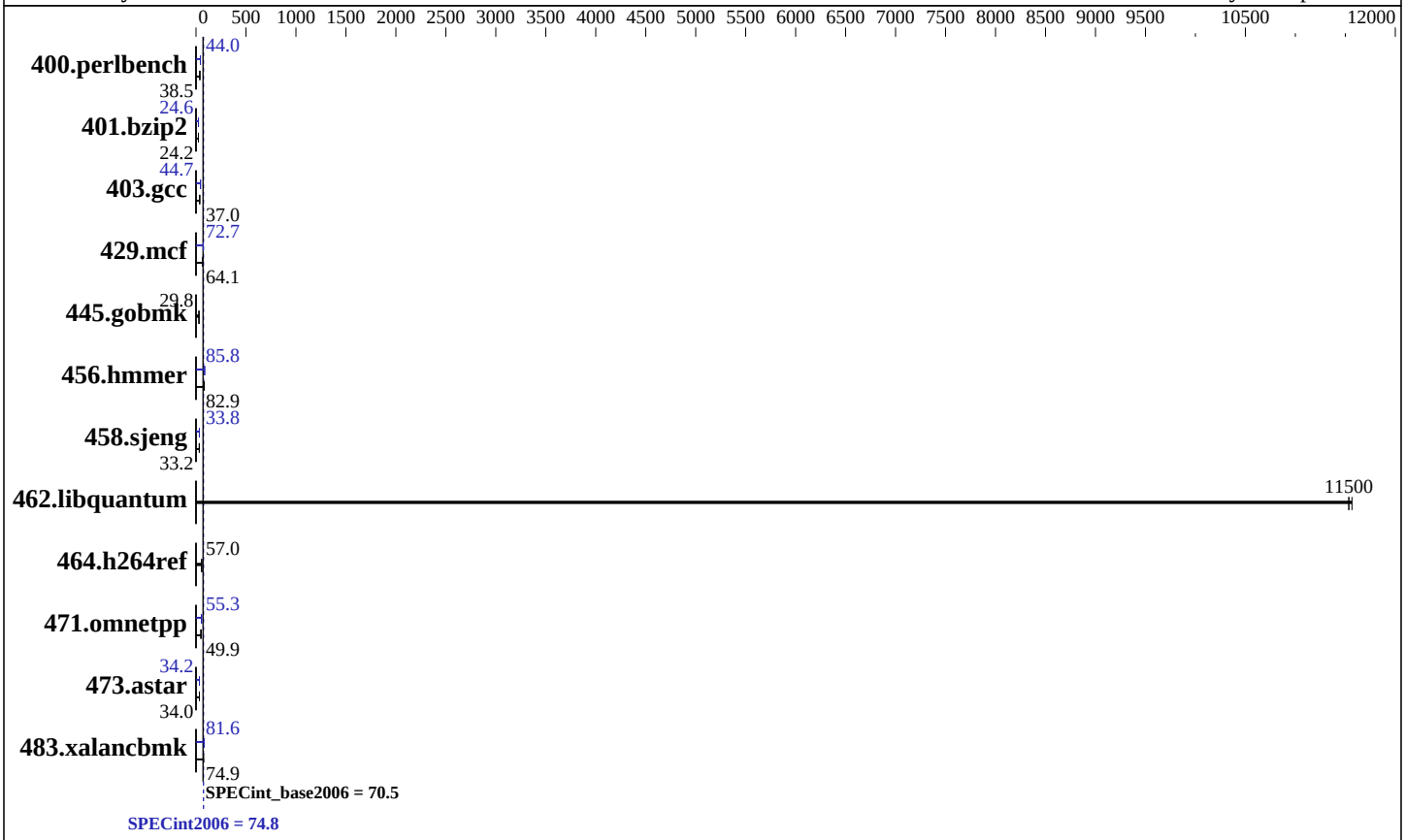
ProLiant DL580 Gen9
(2.40 GHz, Intel Xeon E7-8894 v4)

SPECint®2006 = 74.8

SPECint_base2006 = 70.5

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Jan-2017
Hardware Availability: Mar-2017
Software Availability: Sep-2016



Hardware

CPU Name:	Intel Xeon E7-8894 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	96 cores, 4 chips, 24 cores/chip
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	60 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (32 x 16 GB 2Rx4 PC4-2400T-R, running at 1600 MHz)
Disk Subsystem:	1 x 800 GB NVMe PCIe SSD, RAID 0
Other Hardware:	DL580 Gen9 NVMe SSD Express Bay Enablement Kit

Software

Operating System:	SUSE Linux Enterprise Server 12 (x86_64) SP1, Kernel 3.12.49-11-default
Compiler:	C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	254	38.5	**254**	**38.5**	255	38.3	**222**	**44.0**	221	44.1	222	43.9
401.bzip2	**398**	**24.2**	398	24.2	400	24.1	393	24.6	**392**	**24.6**	392	24.6
403.gcc	217	37.0	218	36.9	**218**	**37.0**	**180**	**44.7**	180	44.7	180	44.6
429.mcf	142	64.3	144	63.2	**142**	**64.1**	125	72.8	**125**	**72.7**	126	72.5
445.gobmk	352	29.8	**352**	**29.8**	351	29.9	352	29.8	**352**	**29.8**	351	29.9
456.hmmer	113	82.7	113	82.9	**113**	**82.9**	109	85.6	109	85.8	**109**	**85.8**
458.sjeng	364	33.3	**364**	**33.2**	366	33.0	358	33.8	358	33.8	**358**	**33.8**
462.libquantum	1.79	11600	1.80	11500	**1.80**	**11500**	1.79	11600	1.80	11500	**1.80**	**11500**
464.h264ref	388	57.0	**389**	**57.0**	392	56.5	388	57.0	**389**	**57.0**	392	56.5
471.omnetpp	127	49.3	**125**	**49.9**	125	50.1	**113**	**55.3**	113	55.1	113	55.5
473.astar	**206**	**34.0**	206	34.1	207	33.9	**205**	**34.2**	206	34.1	204	34.3
483.xalancbmk	**92.2**	**74.9**	92.1	74.9	93.1	74.1	84.8	81.4	**84.6**	**81.6**	84.4	81.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default.
```

Platform Notes

```
BIOS Configuration:
  HP Power Profile set to Custom
  HP Power Regulator to HP Static High Performance Mode
  Minimum Processor Idle Power Core C-State set to C3 State
  Minimum Processor Idle Power Package C-State set to No Package State
  QPI Snoop Configuration set to Home Snoop
  Collaborative Power Control set to Disabled
  Thermal Configuration set to Maximum Cooling
  Processor Power and Utilization Monitoring set to Disabled
  Intel Hyper Threading set to Disabled
  Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/ic17_latest/cpu2006_copy/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on dl580_manju Wed Jan 11 15:47:42 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
```

Continued on next page

Platform Notes (Continued)

```
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E7-8894 v4 @ 2.40GHz
          4 "physical id"s (chips)
          96 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 24
          siblings  : 24
          physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
          27 28 29
          physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
          27 28 29
          physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
          27 28 29
          physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
          27 28 29
       cache size : 61440 KB

    From /proc/meminfo
       MemTotal:       529314968 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    From /etc/*release* /etc/*version*
       SuSE-release:
          SUSE Linux Enterprise Server 12 (x86_64)
          VERSION = 12
          PATCHLEVEL = 1
          # This file is deprecated and will be removed in a future service pack or
          release.
          # Please check /etc/os-release for details about this release.
       os-release:
          NAME="SLES"
          VERSION="12-SP1"
          VERSION_ID="12.1"
          PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
          ID="sles"
          ANSI_COLOR="0;32"
          CPE_NAME="cpe:/o:suse:sles:12:sp1"

    uname -a:
       Linux dl580_manju 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
       (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

    run-level 3 Jan 11 10:18

    SPEC is set to: /home/ic17_latest/cpu2006_copy
       Filesystem     Type  Size  Used Avail Use% Mounted on
       /dev/nvme0n1p4 xfs   703G  311G  392G  45% /home
```

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Platform Notes (Continued)

```
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HP U17 12/08/2016
   Memory:
    64x UNKNOWN NOT AVAILABLE
    32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz

 (End of data from sysinfo program)
 Regarding the sysinfo display about the memory installed, the correct amount of
 memory is 512 GB and the dmidecode description should have one line reading as:
 32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,compact"
 LD_LIBRARY_PATH = "/home/ic17_latest/cpu2006_copy/libs/32:/home/ic17_latest/cpu2006_copy/libs/64:/home/ic17_latest/cpu2006_copy/sh10.2"
 OMP_NUM_THREADS = "96"

  Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM
  memory using Redhat Enterprise Linux 7.2
```

Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

```
 400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
   401.bzip2: -DSPEC_CPU_LP64
     403.gcc: -DSPEC_CPU_LP64
     429.mcf: -DSPEC_CPU_LP64
   445.gobmk: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
   464.h264ref: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -qopt-prefetch
-auto-ilp32 -complex-limited-range -qopt-prefetch-issue-excl-hint
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/home/ic17_latest/cpu2006_copy/sh10.2 -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

403.gcc: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

429.mcf: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen=threadsafe(pass 1) -prof-use(pass 2)
-xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -qopt-prefetch
-ansi-alias

401.bzip2: -prof-gen=threadsafe(pass 1) -prof-use(pass 2)
-xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -auto-ilp32 -qopt-prefetch
-ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-qopt-malloc-options=3 -auto-ilp32 -static

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-qopt-prefetch -auto-p32 -complex-limited-range -static

445.gobmk: basepeak = yes

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-qopt-prefetch -funroll-all-loops

458.sjeng: -prof-gen=threadsafe(pass 1) -prof-use(pass 2)
-xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen=threadsafe(pass 1) -prof-use(pass 2)
-xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)

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Peak Optimization Flags (Continued)

471.omnetpp (continued):
-qopt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs
-L/home/ic17_latest/cpu2006_copy/sh10.2 -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-auto-p32 -Wl,-z,muldefs
-L/home/ic17_latest/cpu2006_copy/sh10.2 -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ansi-alias -Wl,-z,muldefs
-L/home/ic17_latest/cpu2006_copy/sh10.2 -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html
http://www.spec.org/cpu2006/flags/HPE-Compiler-Flags-Intel-V1.2-HSW-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
http://www.spec.org/cpu2006/flags/HPE-Compiler-Flags-Intel-V1.2-HSW-revH.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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