



# SPEC® CFP2006 Result

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Huawei

**SPECfp\_rate2006 = 2070**

Kunlun 9008 (Intel Xeon E7-4820 v3)

**SPECfp\_rate\_base2006 = 2040**

CPU2006 license: 3175

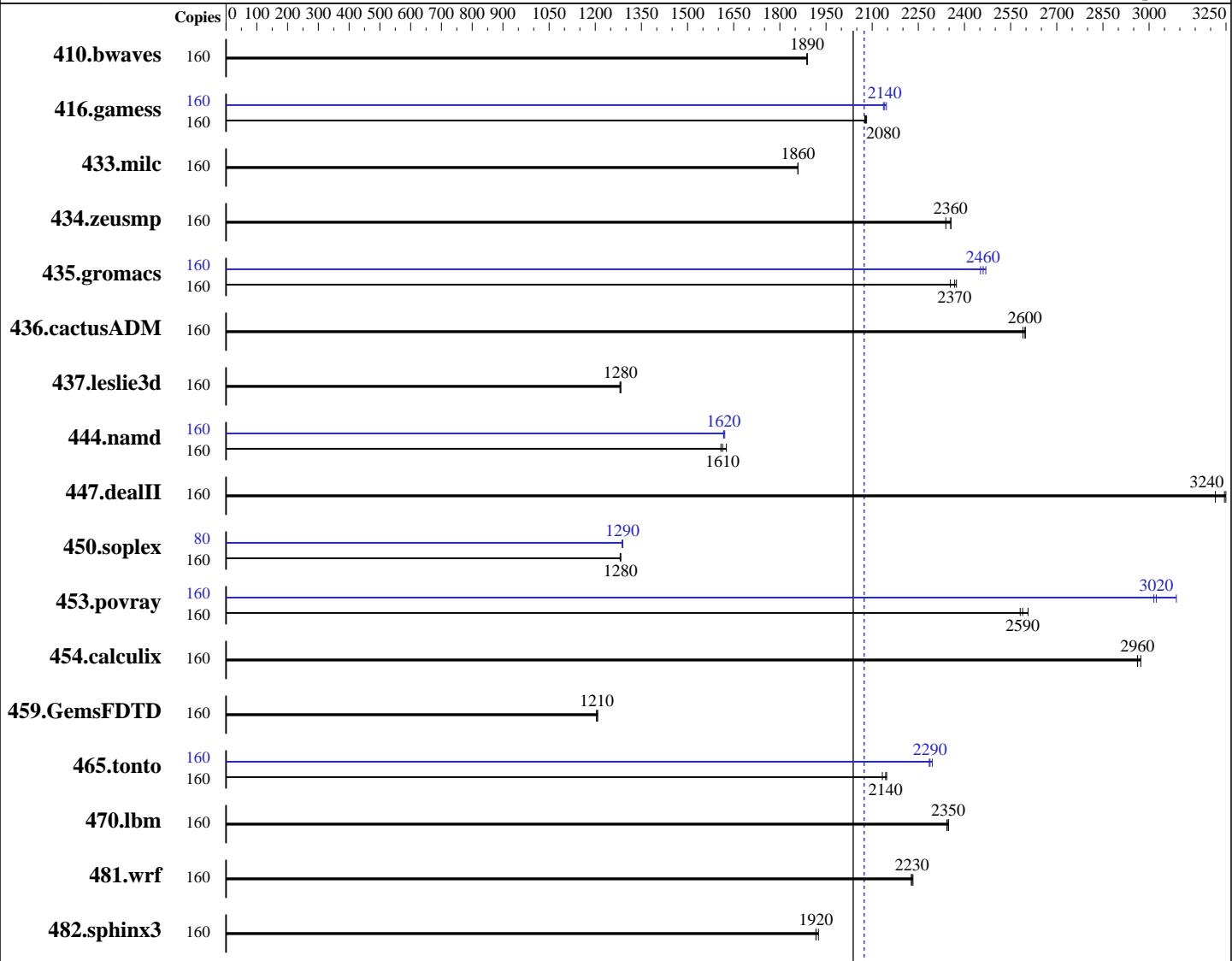
Test date: Dec-2016

Test sponsor: Huawei

Hardware Availability: Jan-2016

Tested by: Huawei

Software Availability: Sep-2014



**SPECfp\_rate\_base2006 = 2040**

**SPECfp\_rate2006 = 2070**

## Hardware

CPU Name: Intel Xeon E7-4820 v3  
 CPU Characteristics:  
 CPU MHz: 1900  
 FPU: Integrated  
 CPU(s) enabled: 80 cores, 8 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 4,8 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
 Compiler: 3.10.0-229.el7.x86\_64  
 Auto Parallel: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 File System: Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 No  
 xfs

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L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R,  
 running at 1333 MHz)  
 Disk Subsystem: 2 x 600 GB SAS, 10K RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	160	1152	1890	<b>1151</b>	<b>1890</b>	1151	1890	160	1152	1890	<b>1151</b>	<b>1890</b>	1151	1890
416.gamess	160	1509	2080	1505	2080	<b>1507</b>	<b>2080</b>	160	1466	2140	1460	2150	<b>1464</b>	<b>2140</b>
433.milc	160	790	1860	790	1860	<b>790</b>	<b>1860</b>	160	790	1860	790	1860	<b>790</b>	<b>1860</b>
434.zeusmp	160	<b>618</b>	<b>2360</b>	622	2340	618	2360	160	<b>618</b>	<b>2360</b>	622	2340	618	2360
435.gromacs	160	481	2370	<b>482</b>	<b>2370</b>	485	2350	160	463	2470	466	2450	<b>464</b>	<b>2460</b>
436.cactusADM	160	738	2590	<b>736</b>	<b>2600</b>	736	2600	160	738	2590	<b>736</b>	<b>2600</b>	736	2600
437.leslie3d	160	1175	1280	<b>1173</b>	<b>1280</b>	1172	1280	160	1175	1280	<b>1173</b>	<b>1280</b>	1172	1280
444.namd	160	789	1630	798	1610	<b>795</b>	<b>1610</b>	160	<b>793</b>	<b>1620</b>	794	1620	792	1620
447.dealII	160	569	3220	<b>564</b>	<b>3240</b>	563	3250	160	569	3220	<b>564</b>	<b>3240</b>	563	3250
450.soplex	160	1040	1280	<b>1041</b>	<b>1280</b>	1042	1280	80	518	1290	517	1290	<b>518</b>	<b>1290</b>
453.povray	160	330	2580	<b>329</b>	<b>2590</b>	327	2610	160	<b>282</b>	<b>3020</b>	282	3020	276	3090
454.calculix	160	444	2970	446	2960	<b>446</b>	<b>2960</b>	160	444	2970	446	2960	<b>446</b>	<b>2960</b>
459.GemsFDTD	160	1410	1200	1405	1210	<b>1408</b>	<b>1210</b>	160	1410	1200	1405	1210	<b>1408</b>	<b>1210</b>
465.tonto	160	733	2150	738	2130	<b>734</b>	<b>2140</b>	160	686	2300	<b>688</b>	<b>2290</b>	689	2290
470.lbm	160	936	2350	938	2340	<b>937</b>	<b>2350</b>	160	936	2350	938	2340	<b>937</b>	<b>2350</b>
481.wrf	160	801	2230	803	2230	<b>802</b>	<b>2230</b>	160	801	2230	803	2230	<b>802</b>	<b>2230</b>
482.sphinx3	160	1620	1930	1626	1920	<b>1626</b>	<b>1920</b>	160	1620	1930	1626	1920	<b>1626</b>	<b>1920</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Turbo mode set with:  
 cpupower -c all frequency-set -g performance



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## Platform Notes

BIOS configuration:

```
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Set C-State to C0/C1
Sysinfo program /home/spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Wed Dec 14 10:35:51 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4820 v3 @ 1.90GHz
        8 "physical id"s (chips)
        160 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 10
        siblings : 20
        physical 0: cores 0 1 2 3 4 8 9 10 11 12
        physical 1: cores 0 1 2 3 4 8 9 10 11 12
        physical 2: cores 0 1 2 3 4 8 9 10 11 12
        physical 3: cores 0 1 2 3 4 8 9 10 11 12
        physical 4: cores 0 1 2 3 4 8 9 10 11 12
        physical 5: cores 0 1 2 3 4 8 9 10 11 12
        physical 6: cores 0 1 2 3 4 8 9 10 11 12
        physical 7: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:      1055991488 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.1 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.1"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

uname -a:

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## Platform Notes (Continued)

```
Linux localhost.localdomain 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38
EST 2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 13 16:44
```

```
SPEC is set to: /home/spec
```

```
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   1.1T  52G  1010G   5% /home
```

```
Additional information from dmidecode:
```

```
Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.
```

```
BIOS American Megatrends Inc. BLXSV106 08/13/2016
```

```
Memory:
```

```
1x Hynix HMA42GR7MFR4N-TFTD 16 GB 2 rank 2133 MHz, configured at 1333 MHz
63x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1333 MHz
128x NO DIMM NO DIMM
```

```
(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/spec/libs/32:/home/spec/libs/64:/home/spec/sh"
```

```
Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1
```

```
Transparent Huge Pages enabled with:
```

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

```
Filesystem page cache cleared with:
```

```
echo 1> /proc/sys/vm/drop_caches
```

```
runspec command invoked through numactl i.e.:
```

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

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## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

icc -m64

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## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -fno-alias -auto-ilp32

```

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

```
450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -opt-malloc-options=3
```

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -unroll14 -ansi-alias
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep-
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto
             -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -opt-prefetch -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.xml>

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