



SPEC® CINT2006 Result

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Lenovo Group Limited

Lenovo System x3650 M5
(2.20 GHz, Intel Xeon E5-2699R v4)

SPECint®2006 = 75.2

SPECint_base2006 = 73.2

CPU2006 license: 9017

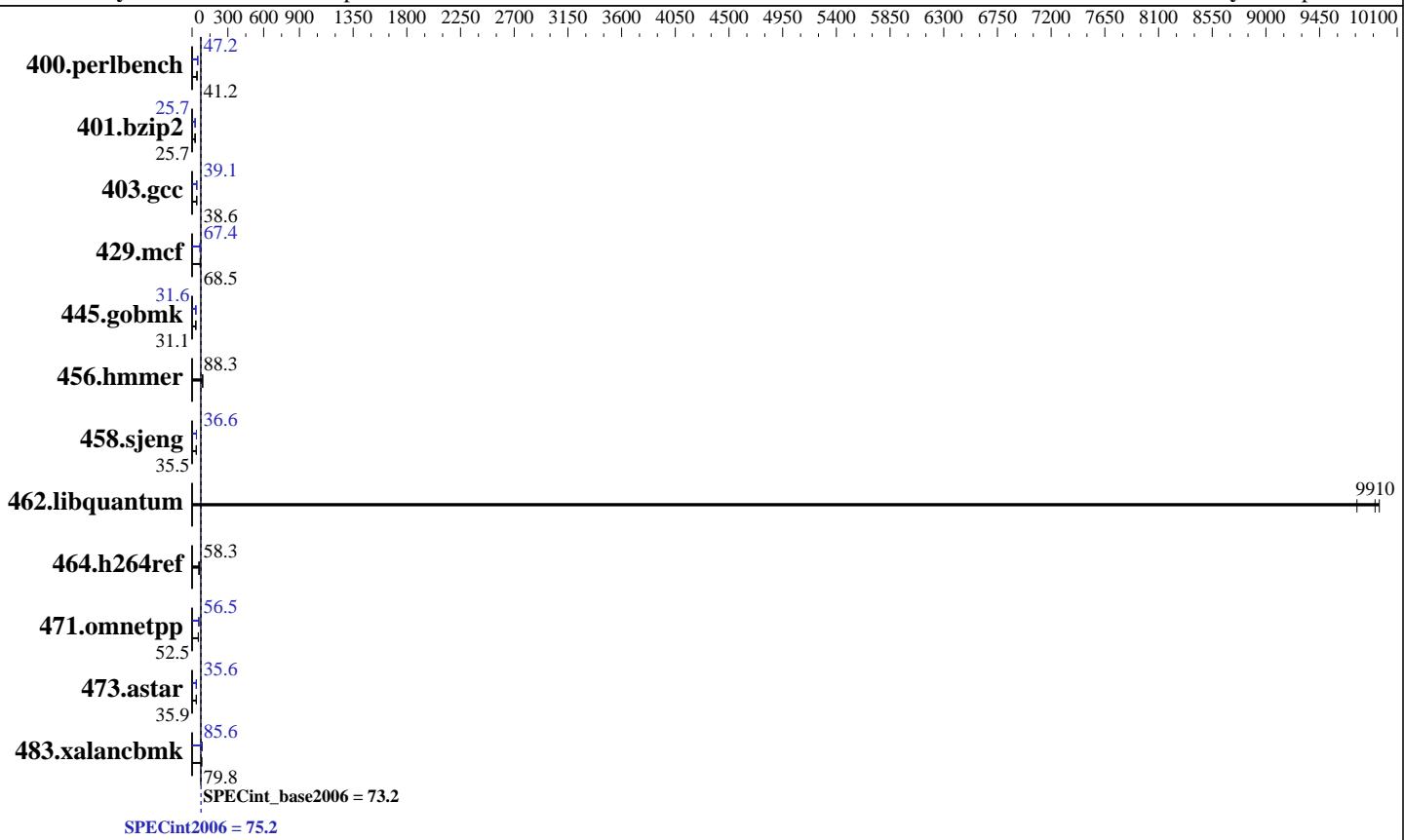
Test date: Dec-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2017

Tested by: Lenovo Group Limited

Software Availability: Sep-2016



Hardware

CPU Name: Intel Xeon E5-2699R v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 55 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
Compiler: Kernel 3.12.49-11-default
C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	237	41.2	<u>237</u>	<u>41.2</u>	238	41.1	<u>206</u>	<u>47.4</u>	<u>207</u>	<u>47.2</u>	<u>207</u>	<u>47.2</u>
401.bzip2	374	25.8	<u>376</u>	<u>25.7</u>	376	25.6	<u>375</u>	<u>25.7</u>	<u>376</u>	<u>25.7</u>	<u>376</u>	<u>25.7</u>
403.gcc	208	38.6	208	38.6	<u>208</u>	<u>38.6</u>	206	39.1	<u>206</u>	<u>39.1</u>	205	39.3
429.mcf	133	68.4	133	68.6	<u>133</u>	<u>68.5</u>	<u>135</u>	<u>67.4</u>	135	67.6	137	66.4
445.gobmk	<u>338</u>	<u>31.1</u>	338	31.1	338	31.1	<u>332</u>	<u>31.6</u>	332	31.6	335	31.3
456.hmmer	106	87.8	<u>106</u>	<u>88.3</u>	105	88.4	<u>106</u>	<u>87.8</u>	<u>106</u>	<u>88.3</u>	105	88.4
458.sjeng	<u>341</u>	<u>35.5</u>	341	35.5	343	35.3	<u>331</u>	<u>36.6</u>	332	36.5	331	36.6
462.libquantum	2.12	9760	2.08	9950	<u>2.09</u>	<u>9910</u>	2.12	9760	2.08	9950	<u>2.09</u>	<u>9910</u>
464.h264ref	<u>379</u>	<u>58.3</u>	381	58.1	379	58.3	<u>379</u>	<u>58.3</u>	381	58.1	379	58.3
471.omnetpp	119	52.4	<u>119</u>	<u>52.5</u>	118	52.8	<u>110</u>	<u>56.6</u>	<u>111</u>	<u>56.5</u>	111	56.5
473.astar	195	35.9	<u>196</u>	<u>35.9</u>	199	35.3	<u>197</u>	<u>35.7</u>	199	35.3	<u>197</u>	<u>35.6</u>
483.xalancbmk	86.2	80.0	<u>86.4</u>	<u>79.8</u>	86.5	79.8	<u>80.7</u>	<u>85.5</u>	<u>80.6</u>	<u>85.6</u>	80.6	85.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS configuration:

Operating Mode set to Maximum Performance

Hyper-Threading set to Disable

Sysinfo program /home/cpu2006-1.2-ic17.0/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on XinYi-MLK-06 Thu Dec 22 09:44:22 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2699R v4 @ 2.20GHz
  2 "physical id"s (chips)
  44 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with

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Platform Notes (Continued)

```

caution.)
cpu cores : 22
siblings : 22
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
cache size : 56320 KB

From /proc/meminfo
MemTotal:      263958180 kB
HugePages_Total:       0
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux XinYi-MLK-06 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 22 09:43

SPEC is set to: /home/cpu2006-1.2-ic17.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   690G  101G  590G  15%  /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[TCE1250-2.20]- 09/07/2016
Memory:
8x NO DIMM Unknown
16x Samsung M393A2G40DB1-CRC 16 GB 2 rank 2400 MHz

```

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Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic17.0/libs/32:/home/cpu2006-1.2-ic17.0/libs/64:/home/cpu2006-1.2-ic17.0/sh10.2"

OMP_NUM_THREADS = "44"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
 -auto-p32

C++ benchmarks:

 -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmr: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -D_FILE_OFFSET_BITS=64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -auto-ilp32 -qopt-prefetch

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Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-qopt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2)

456.hmmr: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-ra-region-strategy=block
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-C.xml>



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