



SPEC® CINT2006 Result

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NEC Corporation

SPECint®2006 = 74.4

Express5800/B120g-h (Intel Xeon E5-2699 v4)

SPECint_base2006 = 72.0

CPU2006 license: 9006

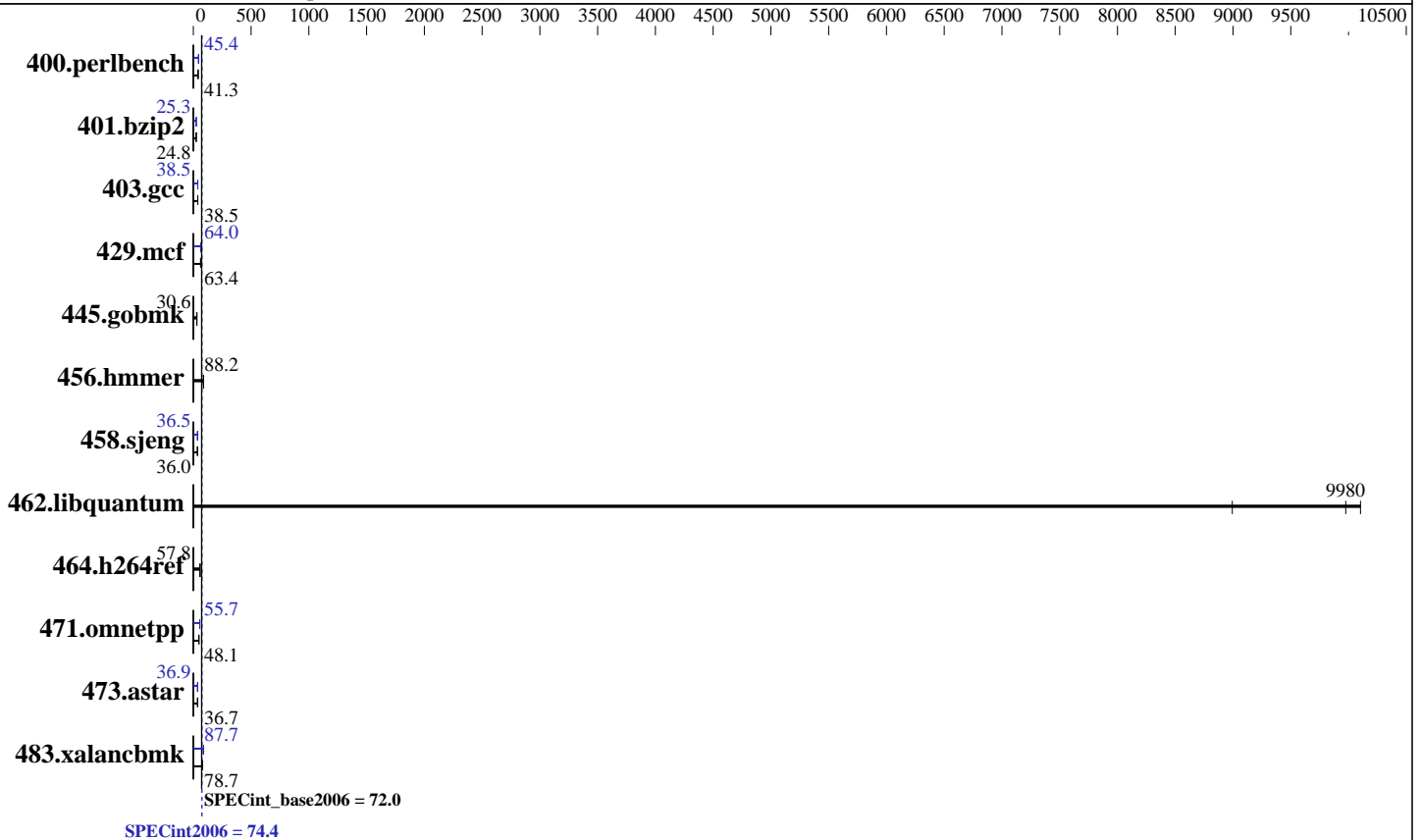
Test date: Mar-2016

Test sponsor: NEC Corporation

Hardware Availability: Dec-2016

Tested by: NEC Corporation

Software Availability: Jan-2016



Hardware

CPU Name: Intel Xeon E5-2699 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 55 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: NEC Storage M310 via Fibre Channel
 (See additional details below)
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 Kernel 3.10.0-327.4.5.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	236	41.3	<u>236</u>	<u>41.3</u>	236	41.4	215	45.4	<u>215</u>	<u>45.4</u>	216	45.2
401.bzip2	<u>390</u>	<u>24.8</u>	389	24.8	391	24.7	<u>382</u>	<u>25.3</u>	382	25.3	382	25.2
403.gcc	209	38.5	<u>209</u>	<u>38.5</u>	209	38.5	208	38.6	<u>209</u>	<u>38.5</u>	209	38.5
429.mcf	146	62.7	143	63.6	<u>144</u>	<u>63.4</u>	144	63.1	142	64.0	<u>143</u>	<u>64.0</u>
445.gobmk	344	30.5	343	30.6	<u>343</u>	<u>30.6</u>	344	30.5	343	30.6	<u>343</u>	<u>30.6</u>
456.hammer	106	88.0	106	88.2	<u>106</u>	<u>88.2</u>	106	88.0	106	88.2	<u>106</u>	<u>88.2</u>
458.sjeng	336	36.1	<u>336</u>	<u>36.0</u>	336	36.0	<u>331</u>	<u>36.5</u>	331	36.5	332	36.5
462.libquantum	<u>2.08</u>	<u>9980</u>	2.30	8990	2.05	10100	<u>2.08</u>	<u>9980</u>	2.30	8990	2.05	10100
464.h264ref	382	57.9	383	57.7	<u>383</u>	<u>57.8</u>	382	57.9	383	57.7	<u>383</u>	<u>57.8</u>
471.omnetpp	130	48.1	129	48.3	<u>130</u>	<u>48.1</u>	112	55.7	112	55.6	<u>112</u>	<u>55.7</u>
473.astar	<u>191</u>	<u>36.7</u>	191	36.7	191	36.8	<u>190</u>	<u>36.9</u>	191	36.8	190	36.9
483.xalancbmk	<u>87.7</u>	<u>78.7</u>	87.6	78.8	87.9	78.5	<u>78.7</u>	<u>87.7</u>	<u>78.6</u>	<u>87.7</u>	78.4	88.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Energy Performance: Performance
Patrol Scrub: Disabled
Snoop Mode: Home Snoop with Directory
Hyper-Threading: Disabled

Storage Configuration for Disk Subsystem:

NEC Storage M310 has 4 x 600 GB 10000 RPM SAS disks under RAID-10 configuration mounted over 8Gbps Fibre Channel interface with these options "defaults" in the /etc/fstab.

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "44"

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General Notes (Continued)

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -D_FILE_OFFSET_BITS=64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
 -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div
 -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
 -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
 -opt-prefetch -auto-p32

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Peak Optimization Flags (Continued)

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-B120g-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-B120g-RevB.xml>



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For other inquiries, please contact webmaster@spec.org.

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