



# SPEC<sup>®</sup> CINT2006 Result

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## Huawei

**SPECint<sup>®</sup>\_rate2006 = 6200**

Huawei RH8100 V3 (Intel Xeon E7-8870 v4)

**SPECint\_rate\_base2006 = 5960**

CPU2006 license: 3175

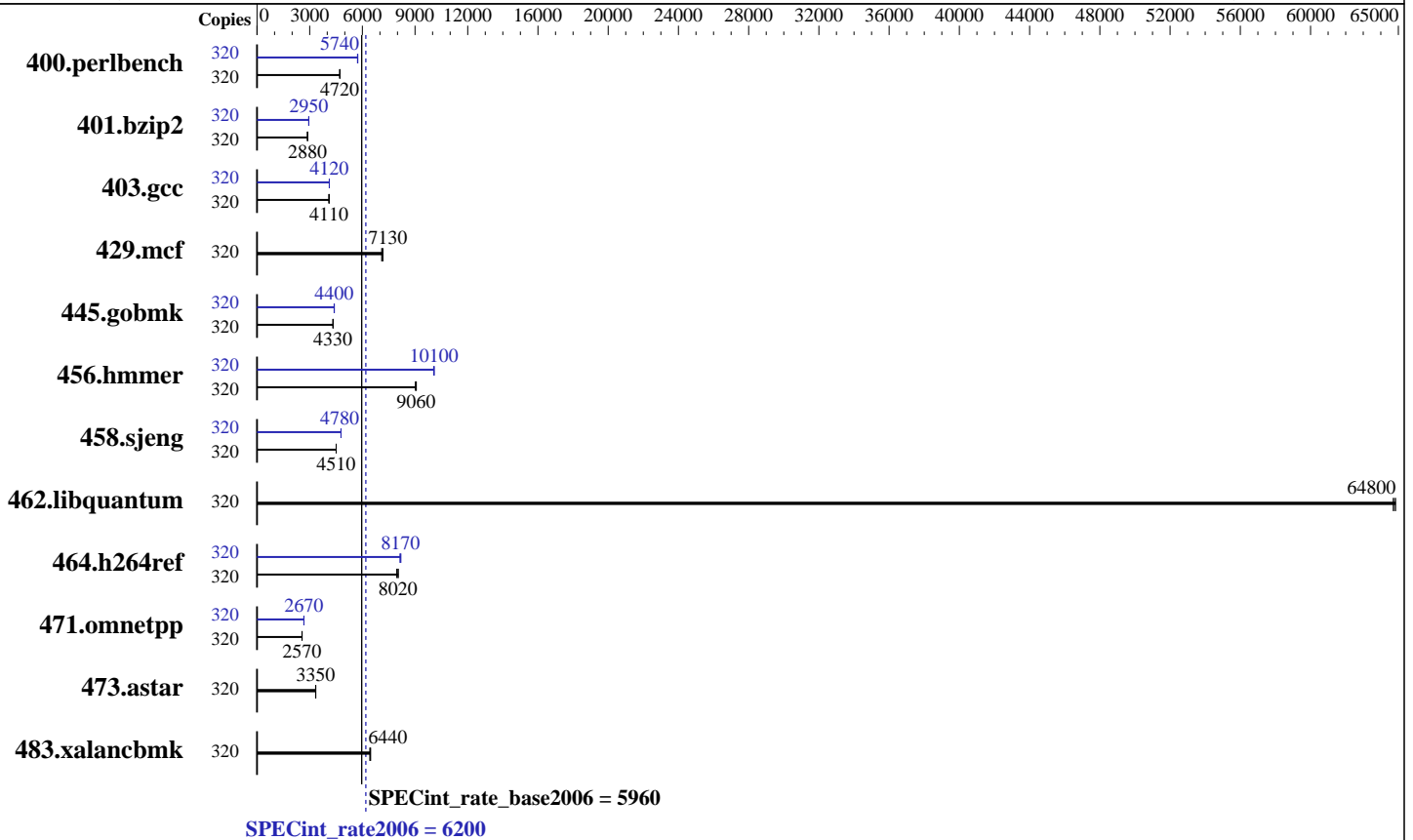
Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Jun-2016

Tested by: Huawei

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E7-8870 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 160 cores, 8 chips, 20 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4,6,8 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 50 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 1 TB (64 x 16 GB 2Rx8 PC4-2400T-R, running at 1600 MHz)  
 Disk Subsystem: 2 x 600 GB SAS, 10K RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP1  
 Kernel 3.12.49-11-default  
 Compiler: C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 5 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	320	661	4730	<b>662</b>	<b>4720</b>	665	4700	320	546	5720	<b>545</b>	<b>5740</b>	545	5740
401.bzip2	320	1077	2870	<b>1073</b>	<b>2880</b>	1069	2890	320	1046	2950	<b>1047</b>	<b>2950</b>	1048	2950
403.gcc	320	627	4110	<b>627</b>	<b>4110</b>	630	4090	320	626	4120	<b>626</b>	<b>4120</b>	626	4120
429.mcf	320	411	7100	<b>409</b>	<b>7130</b>	407	7170	320	411	7100	<b>409</b>	<b>7130</b>	407	7170
445.gobmk	320	776	4330	<b>776</b>	<b>4330</b>	774	4340	320	764	4390	<b>763</b>	<b>4400</b>	763	4400
456.hammer	320	329	9070	331	9020	<b>330</b>	<b>9060</b>	320	296	10100	<b>296</b>	<b>10100</b>	297	10100
458.sjeng	320	859	4510	<b>859</b>	<b>4510</b>	859	4510	320	809	4780	<b>809</b>	<b>4780</b>	809	4780
462.libquantum	320	102	64800	102	64700	<b>102</b>	<b>64800</b>	320	102	64800	102	64700	<b>102</b>	<b>64800</b>
464.h264ref	320	<b>883</b>	<b>8020</b>	880	8050	891	7950	320	<b>867</b>	<b>8170</b>	864	8200	870	8140
471.omnetpp	320	779	2570	782	2560	<b>779</b>	<b>2570</b>	320	<b>749</b>	<b>2670</b>	750	2670	747	2680
473.astar	320	671	3350	<b>672</b>	<b>3350</b>	673	3340	320	671	3350	<b>672</b>	<b>3350</b>	673	3340
483.xalancbmk	320	<b>343</b>	<b>6440</b>	341	6470	343	6430	320	<b>343</b>	<b>6440</b>	341	6470	343	6430

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Turbo mode set with:  
cpupower -c all frequency-set -g performance

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Set Lock\_step to disabled  
Baseboard Management Controller used to adjust the fan speed to 100%  
Set C-State to C0/C1  
Sysinfo program /home/spec/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-sick Wed Nov 30 04:29:13 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E7-8870 v4 @ 2.10GHz  
Continued on next page



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## Platform Notes (Continued)

8 "physical id"s (chips)  
320 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 20
siblings  : 40
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 2: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 3: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 4: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 5: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 6: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 7: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
cache size : 51200 KB
```

From /proc/meminfo

```
MemTotal:      1058817340 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

/usr/bin/lsb\_release -d

```
SUSE Linux Enterprise Server 12 SP1
```

From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
```

os-release:

```
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

```
Linux linux-sick 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 5 Nov 30 04:22

SPEC is set to: /home/spec

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        xfs   1.1T  9.5G  1.1T   1% /home
```

Additional information from dmidecode:

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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 5.11 02/05/2016

Memory:

64x Micron 18ASF2G72PDZ-2G3B1 16 GB 2 rank 2400 MHz, configured at 1600 MHz  
128x NO DIMM NO DIMM

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/spec/libs/32:/home/spec/libs/64:/home/spec/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64

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## Base Portability Flags (Continued)

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.aster: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64

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## Peak Portability Flags (Continued)

429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
  
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias  
  
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div  
  
429.mcf: basepeak = yes  
  
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias  
-opt-mem-layout-trans=3  
  
456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
  
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-auto-ilp32  
  
462.libquantum: basepeak = yes  
  
464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

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## Peak Optimization Flags (Continued)

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.xml>

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Tested with SPEC CPU2006 v1.2.

Report generated on Wed Jan 11 10:34:52 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 January 2017.