



# SPEC<sup>®</sup> CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Huawei

**SPECint<sup>®</sup>2006 = 61.9**

Huawei CH222 V3(Intel Xeon E5-2620 v4)

**SPECint\_base2006 = 59.0**

CPU2006 license: 3175

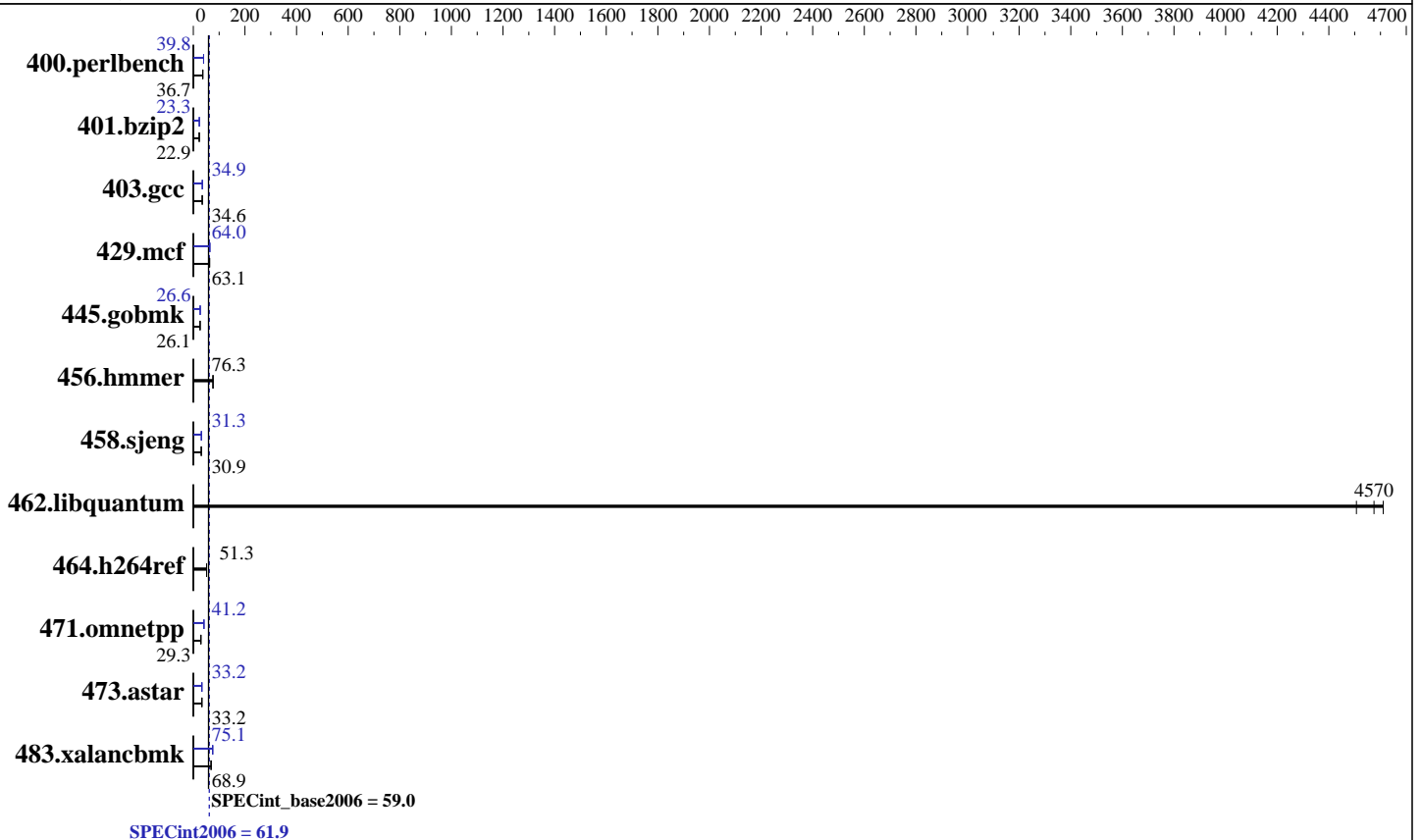
Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Nov-2015



### Hardware

CPU Name: Intel Xeon E5-2620 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 61.9

Huawei CH222 V3(Intel Xeon E5-2620 v4)

SPECint\_base2006 = 59.0

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Nov-2016  
Hardware Availability: Mar-2016  
Software Availability: Nov-2015

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>266</u></b>	<b><u>36.7</u></b>	267	36.6	266	36.7	<b><u>245</u></b>	<b><u>39.8</u></b>	246	39.8	245	39.9
401.bzip2	422	22.9	420	23.0	<b><u>421</u></b>	<b><u>22.9</u></b>	415	23.2	415	23.3	<b><u>415</u></b>	<b><u>23.3</u></b>
403.gcc	<b><u>233</u></b>	<b><u>34.6</u></b>	233	34.6	233	34.6	230	34.9	<b><u>231</u></b>	<b><u>34.9</u></b>	231	34.9
429.mcf	<b><u>145</u></b>	<b><u>63.1</u></b>	144	63.2	145	63.0	143	63.7	<b><u>143</u></b>	<b><u>64.0</u></b>	142	64.1
445.gobmk	402	26.1	<b><u>402</u></b>	<b><u>26.1</u></b>	400	26.2	<b><u>394</u></b>	<b><u>26.6</u></b>	394	26.6	394	26.6
456.hammer	122	76.5	122	76.2	<b><u>122</u></b>	<b><u>76.3</u></b>	122	76.5	122	76.2	<b><u>122</u></b>	<b><u>76.3</u></b>
458.sjeng	392	30.9	<b><u>392</u></b>	<b><u>30.9</u></b>	392	30.9	<b><u>387</u></b>	<b><u>31.3</u></b>	387	31.3	387	31.3
462.libquantum	4.49	4610	<b><u>4.53</u></b>	<b><u>4570</u></b>	4.60	4510	4.49	4610	<b><u>4.53</u></b>	<b><u>4570</u></b>	4.60	4510
464.h264ref	432	51.3	429	51.6	<b><u>431</u></b>	<b><u>51.3</u></b>	432	51.3	429	51.6	<b><u>431</u></b>	<b><u>51.3</u></b>
471.omnetpp	214	29.2	<b><u>214</u></b>	<b><u>29.3</u></b>	212	29.4	<b><u>152</u></b>	<b><u>41.2</u></b>	152	41.2	152	41.1
473.astar	211	33.3	<b><u>212</u></b>	<b><u>33.2</u></b>	212	33.1	<b><u>212</u></b>	<b><u>33.2</u></b>	212	33.0	211	33.2
483.xalancbmk	100	68.9	100	69.0	<b><u>100</u></b>	<b><u>68.9</u></b>	<b><u>91.9</u></b>	<b><u>75.1</u></b>	91.7	75.2	92.1	74.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Set Snoop Mode to ES mode  
Set Patrol Scrub to Disable  
Set Hyper-Threading to Disable  
Sysinfo program /spec16/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Sun Nov 27 10:00:25 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz  
2 "physical id"s (chips)  
16 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with  
Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 61.9

Huawei CH222 V3(Intel Xeon E5-2620 v4)

SPECint\_base2006 = 59.0

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Platform Notes (Continued)

```

caution.)
  cpu cores : 8
  siblings  : 8
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      527794600 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 27 09:50

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        xfs   254G   32G  223G  13% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.32 09/14/2016

Memory:

```

16x Micron 36ASF4G72PZ-2G3A1 32 GB 2 rank 2400 MHz, configured at 2133 MHz
8x NO DIMM NO DIMM

```

(End of data from sysinfo program)



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 61.9

Huawei CH222 V3(Intel Xeon E5-2620 v4)

SPECint\_base2006 = 59.0

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei CH121 V3 and Huawei CH222 V3 are electronically equivalent.

The results have been measured on a Huawei CH121 V3 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmarthheap64



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 61.9

Huawei CH222 V3(Intel Xeon E5-2620 v4)

SPECint\_base2006 = 59.0

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Nov-2016  
Hardware Availability: Mar-2016  
Software Availability: Nov-2015

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 61.9

Huawei CH222 V3(Intel Xeon E5-2620 v4)

SPECint\_base2006 = 59.0

CPU2006 license: 3175

Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Nov-2015

## Peak Optimization Flags (Continued)

401.bzip2 (continued):

-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc

-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel

-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2)

-opt-ra-region-strategy=block

-ansi-alias

-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 61.9

Huawei CH222 V3(Intel Xeon E5-2620 v4)

SPECint\_base2006 = 59.0

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Dec 15 11:17:03 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 13 December 2016.