



SPEC[®] CFP2006 Result

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Lenovo Group Limited

Lenovo ThinkServer SD350
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECfp[®]_rate2006 = 989

SPECfp_rate_base2006 = 963

CPU2006 license: 9017

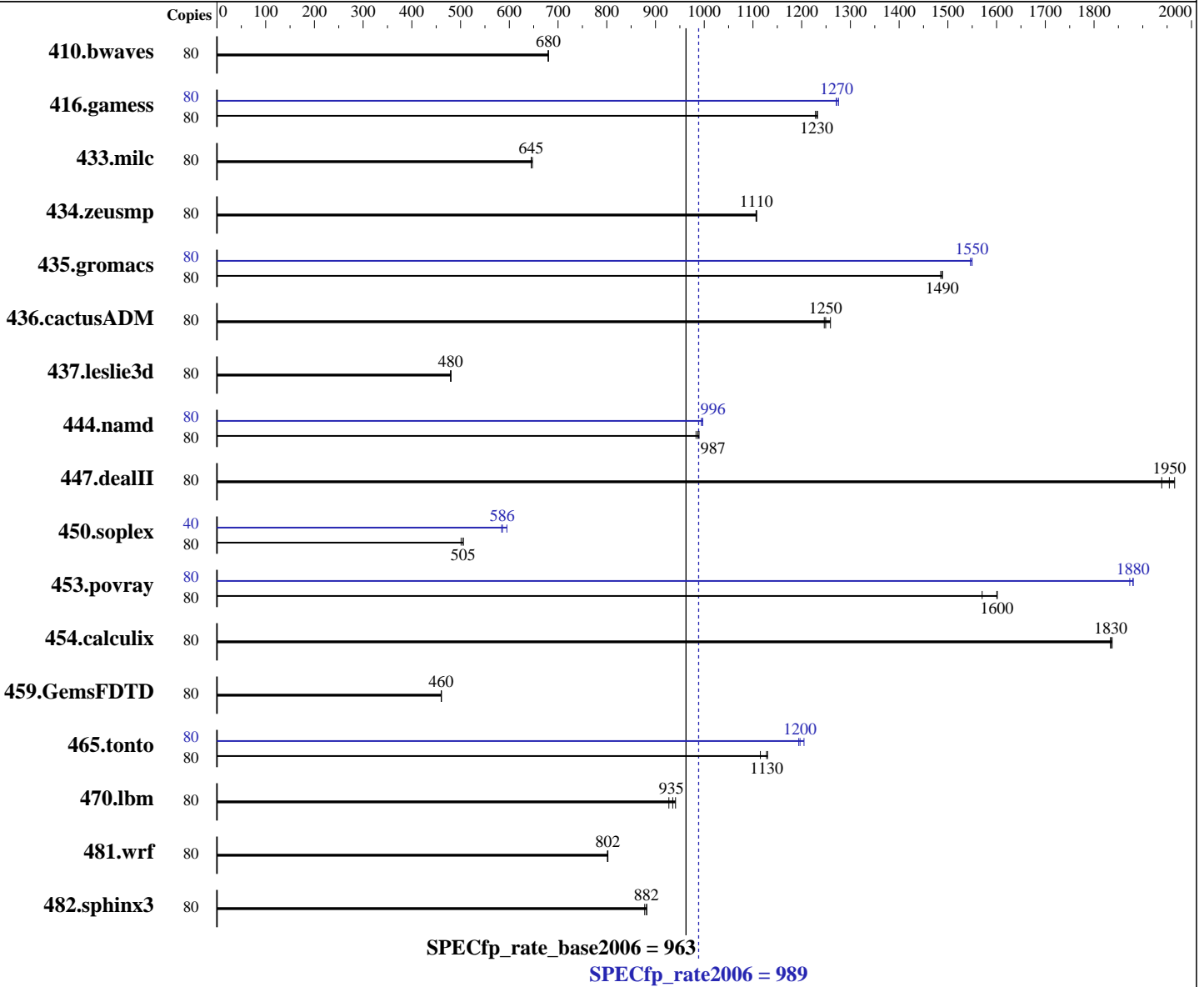
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Aug-2016

Hardware Availability: Sep-2016

Software Availability: Mar-2016



Hardware

CPU Name: Intel Xeon E5-2698 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 40 cores, 2 chips, 20 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 50 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	80	1599	680	1599	680	<u>1599</u>	<u>680</u>	80	1599	680	1599	680	<u>1599</u>	<u>680</u>
416.gamess	80	1275	1230	<u>1273</u>	<u>1230</u>	1271	1230	80	1232	1270	1228	1280	<u>1232</u>	<u>1270</u>
433.milc	80	1134	647	<u>1139</u>	<u>645</u>	1139	645	80	1134	647	<u>1139</u>	<u>645</u>	1139	645
434.zeusmp	80	<u>658</u>	<u>1110</u>	657	1110	658	1110	80	<u>658</u>	<u>1110</u>	657	1110	658	1110
435.gromacs	80	384	1490	385	1490	<u>384</u>	<u>1490</u>	80	369	1550	<u>369</u>	<u>1550</u>	369	1550
436.cactusADM	80	<u>765</u>	<u>1250</u>	759	1260	767	1250	80	<u>765</u>	<u>1250</u>	759	1260	767	1250
437.leslie3d	80	1568	480	1566	480	<u>1568</u>	<u>480</u>	80	1568	480	1566	480	<u>1568</u>	<u>480</u>
444.namd	80	649	989	653	983	<u>650</u>	<u>987</u>	80	<u>644</u>	<u>996</u>	644	997	645	994
447.dealII	80	466	1970	472	1940	<u>468</u>	<u>1950</u>	80	466	1970	472	1940	<u>468</u>	<u>1950</u>
450.soplex	80	1320	506	<u>1321</u>	<u>505</u>	1332	501	40	561	595	<u>569</u>	<u>586</u>	571	584
453.povray	80	266	1600	<u>266</u>	<u>1600</u>	271	1570	80	<u>226</u>	<u>1880</u>	227	1870	226	1880
454.calculix	80	360	1830	<u>360</u>	<u>1830</u>	359	1840	80	360	1830	<u>360</u>	<u>1830</u>	359	1840
459.GemsFDTD	80	<u>1844</u>	<u>460</u>	1842	461	1845	460	80	<u>1844</u>	<u>460</u>	1842	461	1845	460
465.tonto	80	706	1120	697	1130	<u>698</u>	<u>1130</u>	80	<u>658</u>	<u>1200</u>	659	1190	653	1200
470.lbm	80	<u>1175</u>	<u>935</u>	1168	941	1185	927	80	<u>1175</u>	<u>935</u>	1168	941	1185	927
481.wrf	80	<u>1114</u>	<u>802</u>	1114	802	1115	801	80	<u>1114</u>	<u>802</u>	1114	802	1115	801
482.sphinx3	80	1768	882	1776	878	<u>1768</u>	<u>882</u>	80	1768	882	1776	878	<u>1768</u>	<u>882</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches



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Platform Notes

BIOS Configuration:

Hyper-Threading set to Enabled
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on ip10-245-28-45 Wed Aug 24 05:22:52 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2698 v4 @ 2.20GHz
 2 "physical id"s (chips)
 80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores      : 20
  siblings       : 40
  physical 0:    cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 1:    cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
cache size      : 51200 KB
```

From /proc/meminfo

```
MemTotal:        264561064 kB
HugePages_Total: 0
Hugepagesize:    2048 kB
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

```
Linux ip10-245-28-45 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Aug 23 15:32

SPEC is set to: /home/cpu2006-1.2-ic16.0

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Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xfs	690G	7.0G	683G	2%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 3.45 05/31/2016

Memory:

8x Hynix Semiconductor HMA84GR7MFR4N-UH 32 GB 2 rank 2400 MHz
8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
         -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
         -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
         -prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -unroll4 -ansi-alias

```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml>



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