



SPEC® CFP2006 Result

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Fujitsu

PRIMERGY BX2580 M2, Intel Xeon E5-2699 v4, 2.20 GHz

SPECfp_rate2006 = 1120
SPECfp_rate_base2006 = 1090

CPU2006 license: 19

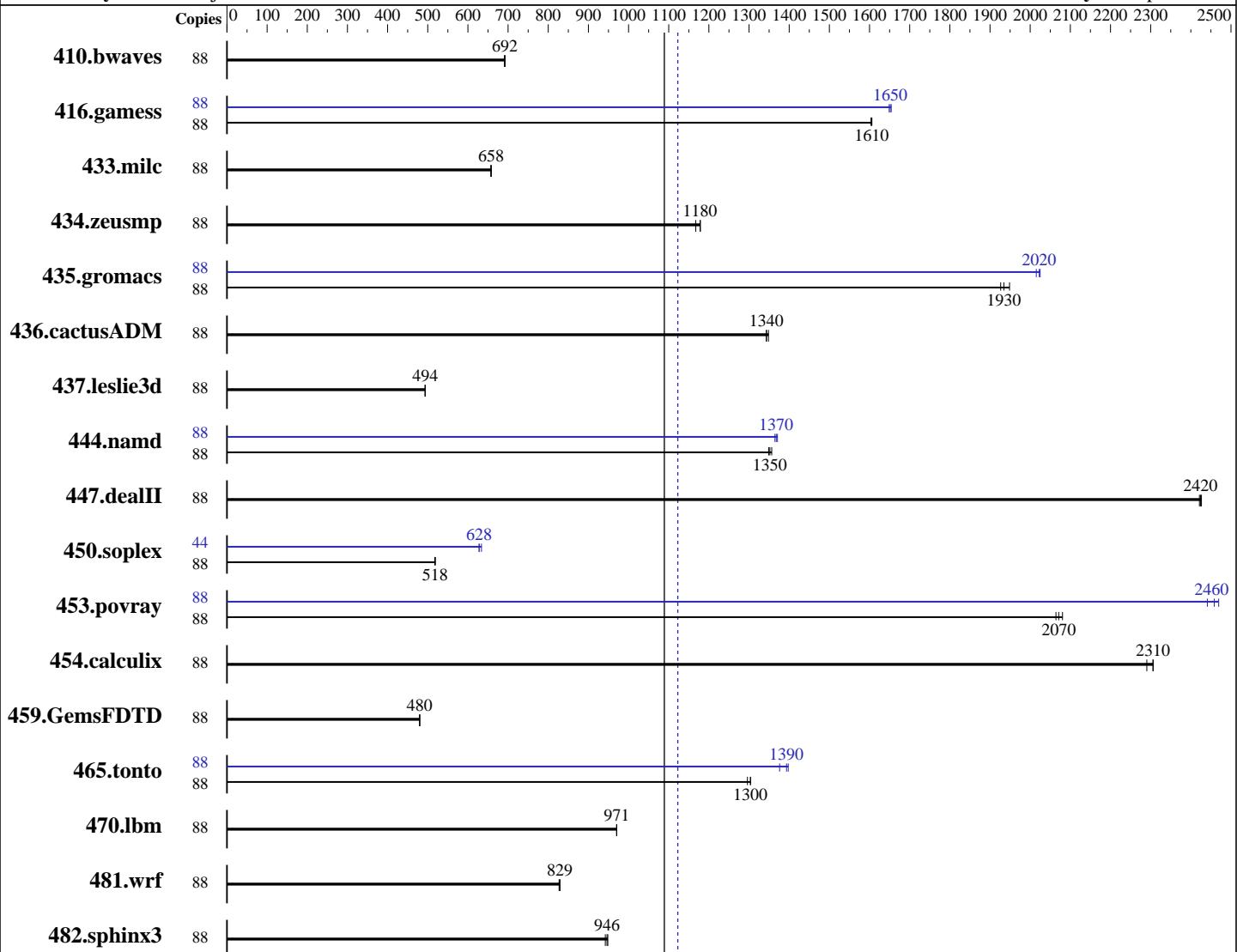
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2016

Hardware Availability: Apr-2016

Software Availability: Sep-2015



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Hardware

CPU Name: Intel Xeon E5-2699 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 55 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 1 x SATA, 1000 GB, 7200 RPM
 Other Hardware: None

Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 88 | 1731 | 691 | 1728 | 692 | 1727 | 692 | 88 | 1731 | 691 | 1728 | 692 | 1727 | 692 |
| 416.gamess | 88 | 1073 | 1610 | 1073 | 1610 | 1074 | 1600 | 88 | 1045 | 1650 | 1042 | 1650 | 1043 | 1650 |
| 433.milc | 88 | 1227 | 658 | 1228 | 658 | 1230 | 657 | 88 | 1227 | 658 | 1228 | 658 | 1230 | 657 |
| 434.zeusmp | 88 | 680 | 1180 | 679 | 1180 | 686 | 1170 | 88 | 680 | 1180 | 679 | 1180 | 686 | 1170 |
| 435.gromacs | 88 | 322 | 1950 | 325 | 1930 | 326 | 1930 | 88 | 312 | 2020 | 310 | 2020 | 311 | 2020 |
| 436.cactusADM | 88 | 783 | 1340 | 780 | 1350 | 783 | 1340 | 88 | 783 | 1340 | 780 | 1350 | 783 | 1340 |
| 437.leslie3d | 88 | 1678 | 493 | 1675 | 494 | 1675 | 494 | 88 | 1678 | 493 | 1675 | 494 | 1675 | 494 |
| 444.namd | 88 | 520 | 1360 | 522 | 1350 | 523 | 1350 | 88 | 515 | 1370 | 517 | 1360 | 516 | 1370 |
| 447.dealII | 88 | 415 | 2430 | 416 | 2420 | 415 | 2420 | 88 | 415 | 2430 | 416 | 2420 | 415 | 2420 |
| 450.soplex | 88 | 1414 | 519 | 1418 | 517 | 1416 | 518 | 44 | 579 | 634 | 584 | 628 | 584 | 628 |
| 453.povray | 88 | 227 | 2060 | 226 | 2070 | 225 | 2080 | 88 | 190 | 2460 | 192 | 2440 | 190 | 2470 |
| 454.calculix | 88 | 315 | 2310 | 315 | 2310 | 317 | 2290 | 88 | 315 | 2310 | 315 | 2310 | 317 | 2290 |
| 459.GemsFDTD | 88 | 1946 | 480 | 1945 | 480 | 1946 | 480 | 88 | 1946 | 480 | 1945 | 480 | 1946 | 480 |
| 465.tonto | 88 | 668 | 1300 | 664 | 1300 | 664 | 1300 | 88 | 620 | 1400 | 629 | 1380 | 621 | 1390 |
| 470.lbm | 88 | 1246 | 970 | 1246 | 971 | 1246 | 971 | 88 | 1246 | 970 | 1246 | 971 | 1246 | 971 |
| 481.wrf | 88 | 1189 | 827 | 1186 | 829 | 1186 | 829 | 88 | 1189 | 827 | 1186 | 829 | 1186 | 829 |
| 482.sphinx3 | 88 | 1809 | 948 | 1821 | 942 | 1813 | 946 | 88 | 1809 | 948 | 1821 | 942 | 1813 | 946 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Energy Performance = Performance
 Utilization Profile = Unbalanced

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Platform Notes (Continued)

```
QPI snoop mode: Cluster on Die
    COD Enable = Enabled, Early Snoop = Disabled, Home Snoop Dir OSB = Disabled
CPU C1E Support = Disabled
Sysinfo program /home/SPECcpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date::: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-rzz5 Tue May 24 23:40:26 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
        2 "physical id"s (chips)
        88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 22
        siblings : 44
        physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
        28
        physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
        28
cache size : 28160 KB
```

```
From /proc/meminfo
MemTotal:      264510700 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
        NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
```

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Platform Notes (Continued)

```
Linux linux-rzz5 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015  
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 24 11:46 last=5
```

```
SPEC is set to: /home/SPECcpu2006  
Filesystem      Type  Size  Used Avail Use% Mounted on  
/dev/sda3        xfs   331G   79G  253G  24% /home  
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.4.0 for D3321-B1x  
03/17/2016
```

Memory:

```
16x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
  447.dealII: -DSPEC_CPU_LP64
  450.soplex: -DSPEC_CPU_LP64
  453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
  470.lbm: -DSPEC_CPU_LP64
  481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevB.xml>

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