



# SPEC<sup>®</sup> CFP2006 Result

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## Lenovo Group Limited

SPECfp<sup>®</sup>\_rate2006 = 4050

Lenovo System x3950 X6  
(2.40 GHz, Intel Xeon E7-8867 v4)

SPECfp\_rate\_base2006 = 3950

CPU2006 license: 9017

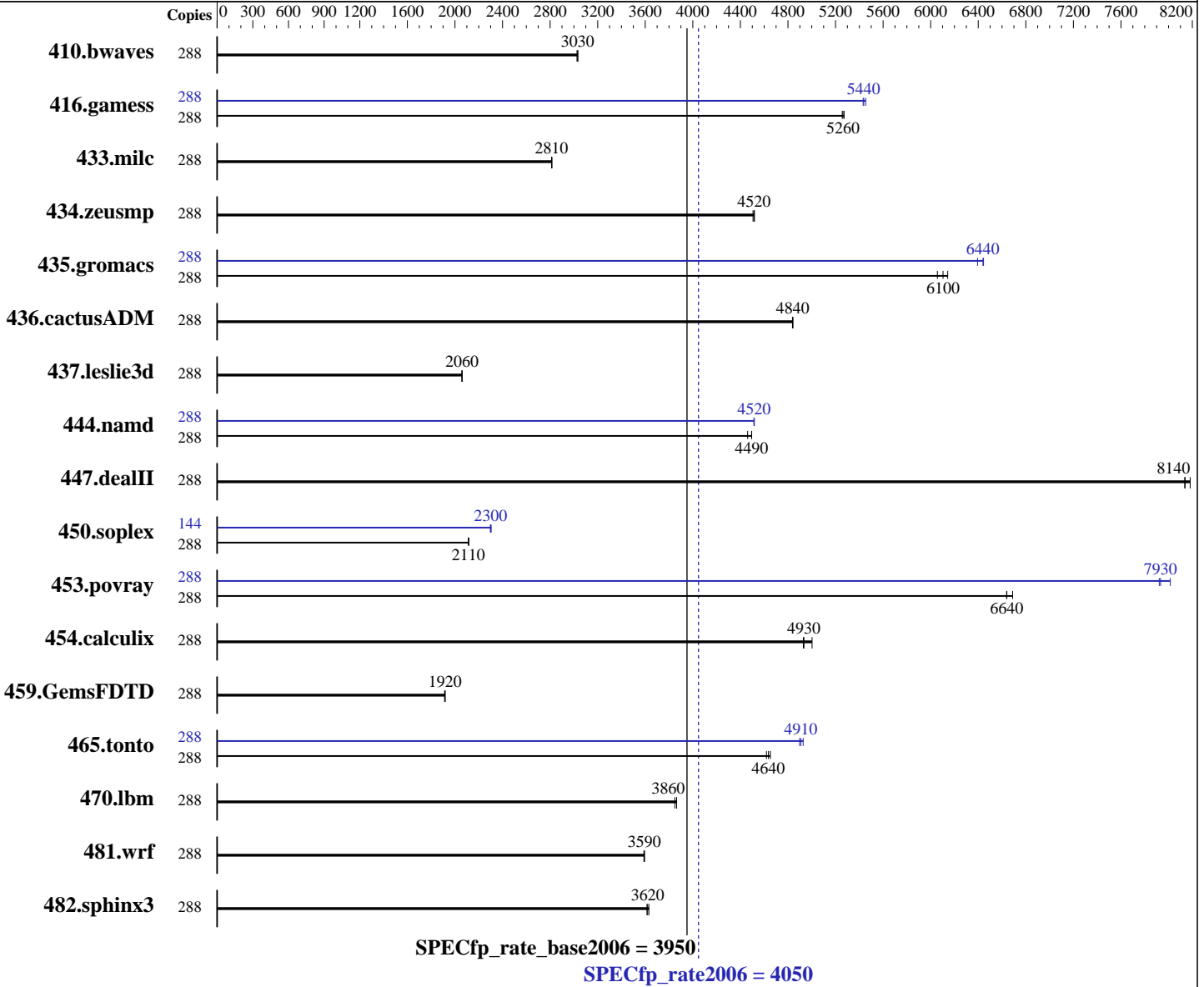
Test date: Jul-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Jun-2016

Tested by: Lenovo Group Limited

Software Availability: Mar-2016



### Hardware

CPU Name: Intel Xeon E7-8867 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 144 cores, 8 chips, 18 cores/chip, 2 threads/core  
 CPU(s) orderable: 4,8 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 45 MB I+D on chip per chip  
Other Cache: None  
Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
Disk Subsystem: 2 x 600 GB 15000 RPM SAS, RAID 1  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	288	1294	3030	<u>1291</u>	<u>3030</u>	1290	3030	288	1294	3030	<u>1291</u>	<u>3030</u>	1290	3030		
416.gamess	288	1073	5260	1070	5270	<u>1071</u>	<u>5260</u>	288	1034	5450	1038	5430	<u>1037</u>	<u>5440</u>		
433.milc	288	940	2810	939	2820	<u>940</u>	<u>2810</u>	288	940	2810	939	2820	<u>940</u>	<u>2810</u>		
434.zeusmp	288	582	4510	<u>580</u>	<u>4520</u>	580	4520	288	582	4510	<u>580</u>	<u>4520</u>	580	4520		
435.gromacs	288	335	6140	<u>337</u>	<u>6100</u>	340	6060	288	319	6440	<u>319</u>	<u>6440</u>	322	6390		
436.cactusADM	288	711	4840	<u>711</u>	<u>4840</u>	711	4840	288	711	4840	<u>711</u>	<u>4840</u>	711	4840		
437.leslie3d	288	1315	2060	1314	2060	<u>1314</u>	<u>2060</u>	288	1315	2060	1314	2060	<u>1314</u>	<u>2060</u>		
444.namd	288	<u>514</u>	<u>4490</u>	514	4500	518	4460	288	511	4520	512	4510	<u>511</u>	<u>4520</u>		
447.dealII	288	405	8140	403	8180	<u>405</u>	<u>8140</u>	288	405	8140	403	8180	<u>405</u>	<u>8140</u>		
450.soplex	288	1135	2120	1137	2110	<u>1136</u>	<u>2110</u>	144	523	2300	<u>522</u>	<u>2300</u>	521	2300		
453.povray	288	<u>231</u>	<u>6640</u>	231	6640	229	6690	288	193	7920	191	8010	<u>193</u>	<u>7930</u>		
454.calculix	288	475	5000	<u>482</u>	<u>4930</u>	482	4930	288	475	5000	<u>482</u>	<u>4930</u>	482	4930		
459.GemsFDTD	288	1597	1910	<u>1595</u>	<u>1920</u>	1594	1920	288	1597	1910	<u>1595</u>	<u>1920</u>	1594	1920		
465.tonto	288	614	4620	609	4650	<u>611</u>	<u>4640</u>	288	575	4930	579	4900	<u>577</u>	<u>4910</u>		
470.lbm	288	1028	3850	<u>1024</u>	<u>3860</u>	1024	3860	288	1028	3850	<u>1024</u>	<u>3860</u>	1024	3860		
481.wrf	288	896	3590	895	3590	<u>896</u>	<u>3590</u>	288	896	3590	895	3590	<u>896</u>	<u>3590</u>		
482.sphinx3	288	1553	3610	<u>1551</u>	<u>3620</u>	1546	3630	288	1553	3610	<u>1551</u>	<u>3620</u>	1546	3630		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches



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CPU2006 license: 9017

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Hardware Availability: Jun-2016

Tested by: Lenovo Group Limited

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## Platform Notes

### BIOS Configuration:

Operating Mode set to "Maximum Performance"  
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on X3950-01-SLES12SP1 Fri Jul 15 03:43:56 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

### From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E7-8867 v4 @ 2.40GHz
 8 "physical id"s (chips)
288 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores      : 18
siblings       : 36
physical 0:    cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1:    cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 2:    cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 3:    cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 4:    cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 5:    cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 6:    cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 7:    cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size     : 46080 KB
```

### From /proc/meminfo

```
MemTotal:      1058539016 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

### From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

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## Platform Notes (Continued)

Linux X3950-01-SLES12SP1 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015 (8d714a0) x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Jul 14 14:58

SPEC is set to: /home/cpu2006-1.2-ic16.0

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	512G	6.7G	505G	2%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[A9E135CUS-3.10]- 06/16/2016

Memory:

128x NO DIMM Unknown

64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB memory using RedHat EL 7.2 glibc 2.17

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

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**Software Availability:** Mar-2016

## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

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## Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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