



# SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL360 Gen9**

(2.40 GHz, Intel Xeon E5-2640 v4)

**SPECint®\_rate2006 = 887**

**SPECint\_rate\_base2006 = 849**

**CPU2006 license:** 3

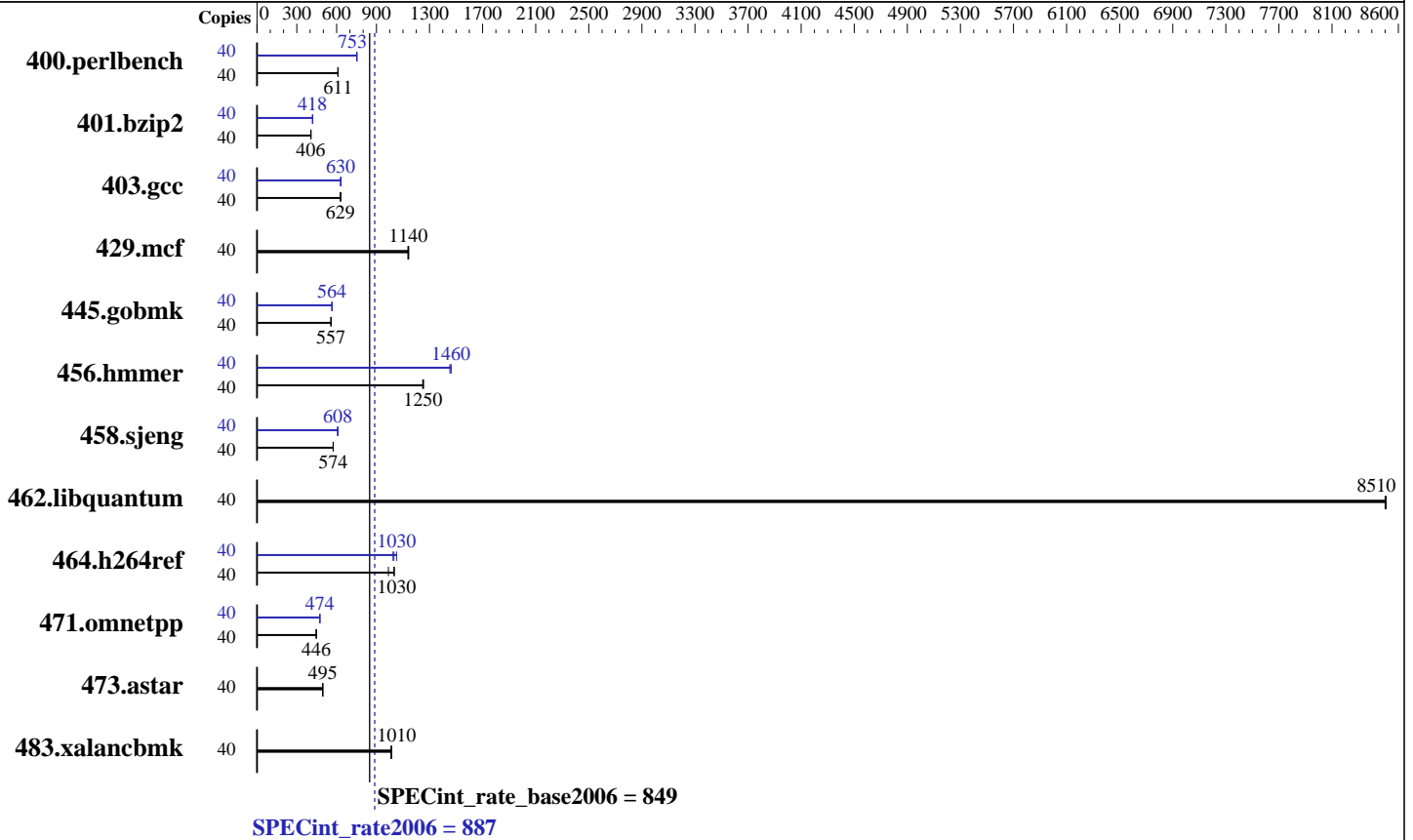
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Jul-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Nov-2015



## Hardware

**CPU Name:** Intel Xeon E5-2640 v4  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.40 GHz  
**CPU MHz:** 2400  
**FPU:** Integrated  
**CPU(s) enabled:** 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
**CPU(s) orderable:** 1,2 chip  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 25 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
**Disk Subsystem:** 2 x 400 GB SAS SSD, RAID 1  
**Other Hardware:** None

## Software

**Operating System:** Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Kernel 3.10.0-327.el7.x86\_64  
**Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
**Auto Parallel:** No  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V10.2



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.40 GHz, Intel Xeon E5-2640 v4)

SPECint\_rate2006 = 887

SPECint\_rate\_base2006 = 849

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jul-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	642	609	640	611	<b>640</b>	<b>611</b>	40	519	753	520	752	<b>519</b>	<b>753</b>
401.bzip2	40	<b>950</b>	<b>406</b>	949	407	951	406	40	919	420	<b>923</b>	<b>418</b>	923	418
403.gcc	40	<b>512</b>	<b>629</b>	513	628	510	631	40	<b>511</b>	<b>630</b>	511	630	508	634
429.mcf	40	321	1140	319	1140	<b>320</b>	<b>1140</b>	40	321	1140	319	1140	<b>320</b>	<b>1140</b>
445.gobmk	40	753	557	752	558	<b>753</b>	<b>557</b>	40	<b>743</b>	<b>564</b>	742	566	743	564
456.hammer	40	297	1260	<b>299</b>	<b>1250</b>	299	1250	40	255	1460	<b>255</b>	<b>1460</b>	257	1450
458.sjeng	40	844	574	842	575	<b>843</b>	<b>574</b>	40	<b>796</b>	<b>608</b>	797	607	795	609
462.libquantum	40	97.4	8510	97.5	8500	<b>97.4</b>	<b>8510</b>	40	97.4	8510	97.5	8500	<b>97.4</b>	<b>8510</b>
464.h264ref	40	855	1040	<b>858</b>	<b>1030</b>	894	990	40	864	1020	842	1050	<b>858</b>	<b>1030</b>
471.omnetpp	40	560	446	560	447	<b>560</b>	<b>446</b>	40	<b>528</b>	<b>474</b>	528	473	527	474
473.astar	40	<b>567</b>	<b>495</b>	565	497	568	494	40	<b>567</b>	<b>495</b>	565	497	568	494
483.xalanbmk	40	273	1010	273	1010	<b>273</b>	<b>1010</b>	40	273	1010	273	1010	<b>273</b>	<b>1010</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Platform Notes

BIOS Configuration:  
HP Power Profile set to Custom  
HP Power Regulator to HP Static High Performance Mode  
Minimum Processor Idle Power Core C-State set to C1E State  
Minimum Processor Idle Power Package C-State set to No Package State  
QPI Snoop Configuration set to Cluster on Die  
Collaborative Power Control set to Disabled  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh  
Intel Hyperthreading set to Enabled

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL360 Gen9**

(2.40 GHz, Intel Xeon E5-2640 v4)

**SPECint\_rate2006 = 887**

**SPECint\_rate\_base2006 = 849**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Jul-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Nov-2015

## Platform Notes (Continued)

Sysinfo program /cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Fri Jul 8 16:19:06 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2640 v4 @ 2.40GHz
 2 "physical id"s (chips)
 40 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 10
siblings   : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

From /proc/meminfo

```
MemTotal:      528065724 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

uname -a:

```
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jul 8 16:18

SPEC is set to: /cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   368G  207G  161G  57% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program  
Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.40 GHz, Intel Xeon E5-2640 v4)

SPECint\_rate2006 = 887

SPECint\_rate\_base2006 = 849

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jul-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 04/12/2016

Memory:

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as: 16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.40 GHz, Intel Xeon E5-2640 v4)

SPECint\_rate2006 = 887

SPECint\_rate\_base2006 = 849

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jul-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64

403.gcc: -D\_FILE\_OFFSET\_BITS=64

429.mcf: -D\_FILE\_OFFSET\_BITS=64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64

458.sjeng: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64

462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

464.h264ref: -D\_FILE\_OFFSET\_BITS=64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -D\_FILE\_OFFSET\_BITS=64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 5



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.40 GHz, Intel Xeon E5-2640 v4)

SPECint\_rate2006 = 887

SPECint\_rate\_base2006 = 849

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jul-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Peak Portability Flags (Continued)

483.xalanbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias  
-opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.40 GHz, Intel Xeon E5-2640 v4)

SPECint\_rate2006 = 887

SPECint\_rate\_base2006 = 849

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jul-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Aug 9 17:03:27 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 9 August 2016.