SPEC® CFP2006 Result

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Lenovo Group Limited

$SPECfp^{®}\_rate2006 = 361$

Lenovo System x3550 M5
(1.70 GHz, Intel Xeon E5-2603 v4)

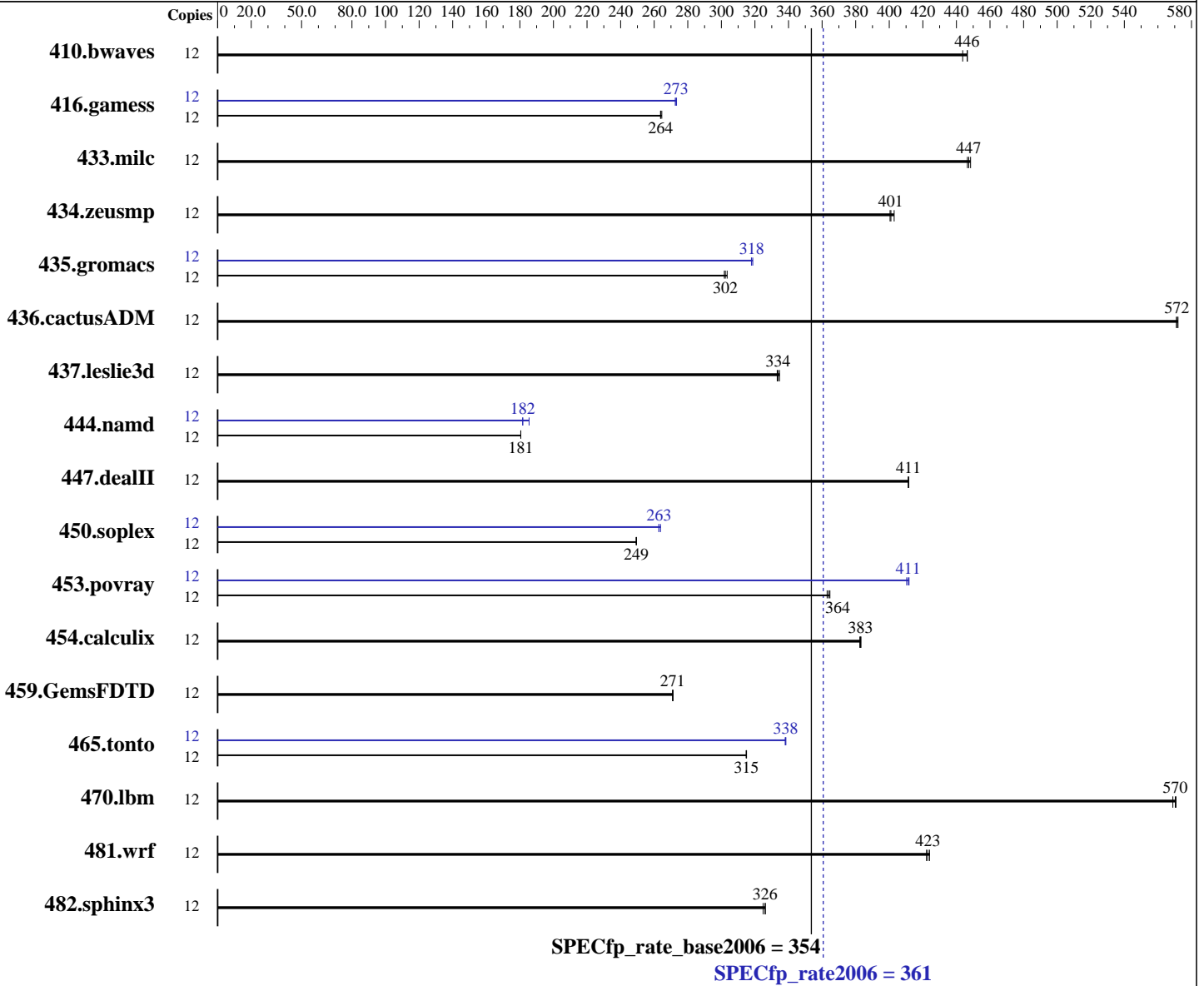$SPECfp\_rate\_base2006 = 354$

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Jun-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

Benchmark	Copies	Result
410.bwaves	12	446
416.gamess	12	273
416.gamess	12	264
433.milc	12	447
434.zeusmp	12	401
435.gromacs	12	318
435.gromacs	12	302
436.cactusADM	12	572
437.leslie3d	12	334
444.namd	12	182
444.namd	12	181
447.dealII	12	411
450.soplex	12	263
450.soplex	12	249
453.povray	12	411
453.povray	12	364
454.calculix	12	383
459.GemsFDTD	12	271
465.tonto	12	338
465.tonto	12	315
470.lbm	12	570
481.wrf	12	423
482.sphinx3	12	326

SPECfp_rate_base2006 = 354

SPECfp_rate2006 = 361

Hardware

CPU Name: Intel Xeon E5-2603 v4
CPU Characteristics:
CPU MHz: 1700
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64) Kernel 3.12.49-11-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)

Continued on next page

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L3 Cache:	15 MB I+D on chip per chip	Base Pointers:	32/64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 1866 MHz)	Other Software:	None
Disk Subsystem:	1 x 800 GB SATA SSD		
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	**365**	**446**	365	447	368	444	12	**365**	**446**	365	447	368	444
416.gamess	12	891	264	**890**	**264**	888	265	12	860	273	**861**	**273**	862	273
433.milc	12	**246**	**447**	246	448	247	447	12	**246**	**447**	246	448	247	447
434.zeusmp	12	271	403	**272**	**401**	273	400	12	271	403	**272**	**401**	273	400
435.gromacs	12	282	303	284	302	**283**	**302**	12	270	318	**269**	**318**	269	319
436.cactusADM	12	**251**	**572**	251	571	251	572	12	**251**	**572**	251	571	251	572
437.leslie3d	12	338	333	337	335	**338**	**334**	12	338	333	337	335	**338**	**334**
444.namd	12	**533**	**181**	533	181	533	181	12	519	185	**530**	**182**	530	182
447.dealII	12	334	412	334	411	**334**	**411**	12	334	412	334	411	**334**	**411**
450.soplex	12	**401**	**249**	401	249	402	249	12	379	264	381	263	**381**	**263**
453.povray	12	**175**	**364**	175	365	176	363	12	155	412	**155**	**411**	156	410
454.calculix	12	**259**	**383**	259	382	258	383	12	**259**	**383**	259	382	258	383
459.GemsFDTD	12	**470**	**271**	470	271	469	271	12	**470**	**271**	470	271	469	271
465.tonto	12	375	315	**375**	**315**	375	315	12	349	338	**349**	**338**	349	338
470.lbm	12	289	571	290	569	**289**	**570**	12	289	571	290	569	**289**	**570**
481.wrf	12	316	424	**317**	**423**	318	422	12	316	424	**317**	**423**	318	422
482.sphinx3	12	717	326	**717**	**326**	720	325	12	717	326	**717**	**326**	720	325

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"
    Transparent Huge Pages enabled with:
     echo always > /sys/kernel/mm/transparent_hugepage/enabled
    Filesystem page cache cleared with:
     echo 1 > /proc/sys/vm/drop_caches

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Platform Notes

```
BIOS Configuration:
 Operating Mode set to "Maximum Performance"
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on DaAn-02 Sat Jun 11 12:42:00 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2603 v4@ 1.70GHz
      2 "physical id"s (chips)
      12 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 6
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       263830720 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
   Linux DaAn-02 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
   (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 11 03:19

SPEC is set to: /home/cpu2006-1.2-ic16.0
```

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Platform Notes (Continued)

```
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sda4       xfs   688G   28G  661G   5% /home
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS LENOVO -[TBE124K-2.10]- 05/10/2016
  Memory:
  16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at 1867 MHz
  8x NO DIMM Unknown

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
    icc    -m64
```

C++ benchmarks:
```
    icpc   -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc    -m64 ifort -m64
```

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Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
       433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
      447.dealII: -DSPEC_CPU_LP64
     450.soplex: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
    icc    -m64
```

C++ benchmarks (except as noted below):
```
    icpc   -m64
```

```
    450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

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Peak Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -D_FILE_OFFSET_BITS=64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
    433.milc: basepeak = yes
```

```
    470.lbm: basepeak = yes
```

```
  482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
    444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -fno-alias -auto-ilp32
```

```
    447.dealII: basepeak = yes
```

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Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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