



# SPEC® CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9  
(2.60 GHz, Intel Xeon E5-2623 v4)

SPECfp®\_rate2006 = 362

SPECfp\_rate\_base2006 = 351

CPU2006 license: 3

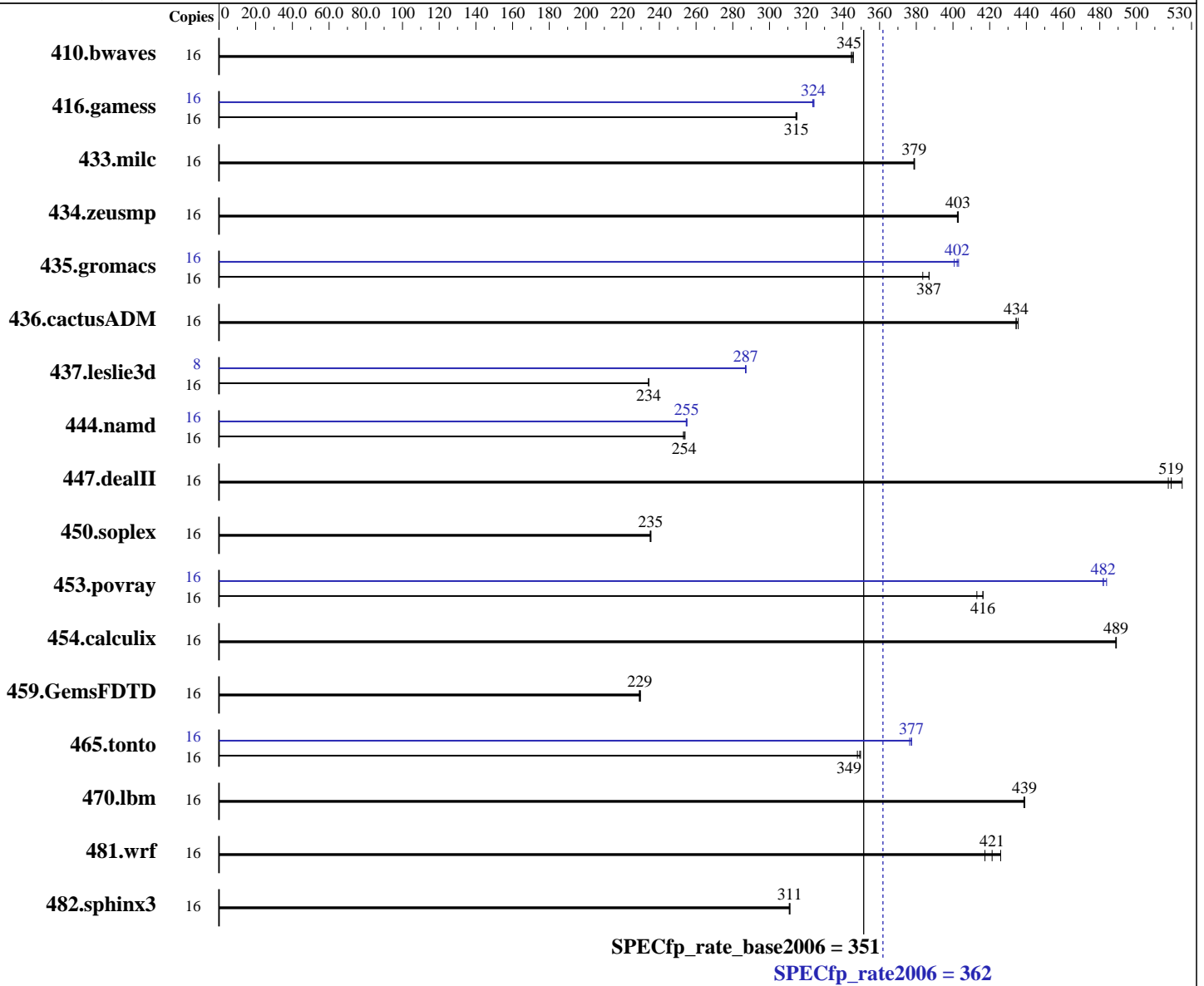
Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



## Hardware

CPU Name: Intel Xeon E5-2623 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

## Software

Operating System: Red Hat Enterprise Linux Server release 7.2, (Maipo)  
 Kernel 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs

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L3 Cache: 10 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	16	631	345	<b>630</b>	<b>345</b>	629	346	16	631	345	<b>630</b>	<b>345</b>	629	346		
416.gamess	16	995	315	<b>995</b>	<b>315</b>	997	314	16	966	324	<b>967</b>	<b>324</b>	968	324		
433.milc	16	388	379	387	379	<b>388</b>	<b>379</b>	16	388	379	387	379	<b>388</b>	<b>379</b>		
434.zeusmp	16	361	403	362	402	<b>361</b>	<b>403</b>	16	361	403	362	402	<b>361</b>	<b>403</b>		
435.gromacs	16	<b>295</b>	<b>387</b>	295	387	298	384	16	285	401	<b>284</b>	<b>402</b>	283	403		
436.cactusADM	16	439	436	<b>440</b>	<b>434</b>	440	434	16	439	436	<b>440</b>	<b>434</b>	440	434		
437.leslie3d	16	642	234	642	234	<b>642</b>	<b>234</b>	8	262	287	262	287	<b>262</b>	<b>287</b>		
444.namd	16	<b>506</b>	<b>254</b>	505	254	507	253	16	<b>503</b>	<b>255</b>	503	255	504	255		
447.dealII	16	<b>353</b>	<b>519</b>	354	517	349	525	16	<b>353</b>	<b>519</b>	354	517	349	525		
450.soplex	16	567	235	568	235	<b>567</b>	<b>235</b>	16	567	235	568	235	<b>567</b>	<b>235</b>		
453.povray	16	204	416	206	413	<b>204</b>	<b>416</b>	16	<b>177</b>	<b>482</b>	176	484	177	482		
454.calculix	16	270	489	<b>270</b>	<b>489</b>	270	489	16	270	489	<b>270</b>	<b>489</b>	270	489		
459.GemsFDTD	16	741	229	<b>740</b>	<b>229</b>	739	230	16	741	229	<b>740</b>	<b>229</b>	739	230		
465.tonto	16	453	348	450	350	<b>451</b>	<b>349</b>	16	418	376	<b>417</b>	<b>377</b>	417	377		
470.lbm	16	501	439	501	439	<b>501</b>	<b>439</b>	16	501	439	501	439	<b>501</b>	<b>439</b>		
481.wrf	16	420	426	428	417	<b>424</b>	<b>421</b>	16	420	426	428	417	<b>424</b>	<b>421</b>		
482.sphinx3	16	1003	311	<b>1002</b>	<b>311</b>	1002	311	16	1003	311	<b>1002</b>	<b>311</b>	1002	311		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>



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## Platform Notes

### BIOS Configuration:

Intel Hyperthreading Option set to Enabled  
 Power Profile set to Custom  
 Power Regulator set to Static High Performance Mode  
 Minimum Processor Idle Power Core C-State set to C1E State  
 Minimum Processor Idle Power Package C-State set to No Package State  
 Collaborative Power Control set to Disabled  
 QPI Snoop Configuration set to Home Snoop  
 Thermal Configuration set to Maximum Cooling  
 Processor Power and Utilization Monitoring set to Disabled  
 Memory Double Refresh Rate set to 1x Refresh  
 Energy Performance Bias set to Maximum Performance

### Sysinfo program

/home/specuser/specsuite/HP\_build\_ic16\_suite\_corrected\_int\_bins/cpu2006/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
 running on ml350bdwspec Sun May 29 15:07:12 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

### From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2623 v4@ 2.60GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores     : 4
  siblings      : 8
  physical 0:   cores 0 1 2 3
  physical 1:   cores 0 1 2 3
cache size     : 10240 KB
```

### From /proc/meminfo

```
MemTotal:      528068680 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

### From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

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## Platform Notes (Continued)

uname -a:

```
Linux ml350bdwspec 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 29 15:06

SPEC is set to:

/home/specuser/specsuite/HP\_build\_ic16\_suite\_corrected\_int\_bins/cpu2006

```
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda5        xfs       318G     163G   156G   52% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P92 04/12/2016

Memory:

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as: 16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

## General Notes

Environment variables set by runspec before the start of the run:

ID\_LIBRARY\_PATH = \*/home/specuser/specsuite/HP\_build\_ic16\_suite\_corrected\_int\_bins/cpu2006/libs/32:/home/specuser/specsuite/HP\_build\_ic16\_suite\_corrected\_int\_bins/cpu2006/libs/64:/home/specuser/specsuite/HP\_build\_ic16\_suite\_corrected\_int\_bins/cpu2006/sh\*

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

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## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -static(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -static(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -static(pass 2) -prof-use(pass 2)  
-unroll2 -inline-level=0 -scalar-rep-

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## Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -static(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revF.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revF.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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