



# SPEC® CINT2006 Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL580 Gen9  
(2.80 GHz, Intel Xeon E7-8891 v4)

**SPECint®2006 = Not Run**

**SPECint\_base2006 = 71.2**

**CPU2006 license:** 3

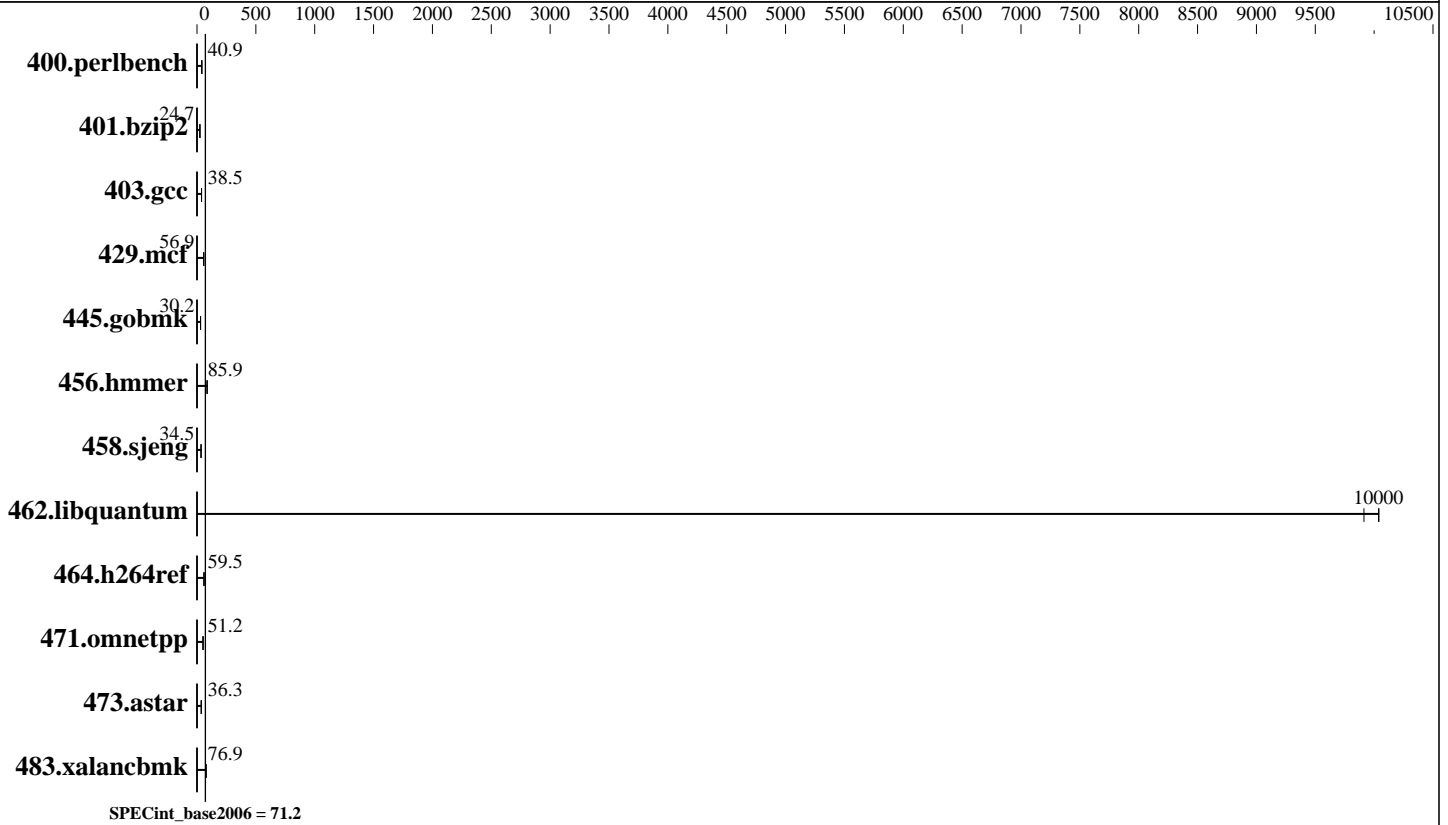
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** May-2016

**Hardware Availability:** Jun-2016

**Software Availability:** Dec-2015



## Hardware

**CPU Name:** Intel Xeon E7-8891 v4  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.50 GHz  
**CPU MHz:** 2800  
**FPU:** Integrated  
**CPU(s) enabled:** 40 cores, 4 chips, 10 cores/chip  
**CPU(s) orderable:** 2,4 chip  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 60 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 512 GB (32 x 16 GB 2Rx4 PC4-2400T-R, running at 1600 MHz)  
**Disk Subsystem:** 1 x 800 GB NVMe PCIe SSD, RAID 0  
**Other Hardware:** DL580 Gen9 NVMe SSD Express Bay Enablement Kit

## Software

**Operating System:** SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
Kernel 3.12.49-11-default  
**Compiler:** C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux  
**Auto Parallel:** Yes  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	239	40.9	<u>239</u>	<u>40.9</u>	239	40.9						
401.bzip2	391	24.7	<u>391</u>	<u>24.7</u>	390	24.7						
403.gcc	<u>209</u>	<u>38.5</u>	209	38.5	209	38.5						
429.mcf	160	56.9	160	56.9	<u>160</u>	<u>56.9</u>						
445.gobmk	347	30.3	347	30.2	<u>347</u>	<u>30.2</u>						
456.hmmer	108	86.0	109	85.7	<u>109</u>	<u>85.9</u>						
458.sjeng	351	34.5	<u>350</u>	<u>34.5</u>	350	34.6						
462.libquantum	2.09	9910	<u>2.06</u>	<u>10000</u>	2.06	10000						
464.h264ref	372	59.5	<u>372</u>	<u>59.5</u>	372	59.4						
471.omnetpp	<u>122</u>	<u>51.2</u>	120	52.1	123	50.8						
473.astar	<u>193</u>	<u>36.3</u>	192	36.5	194	36.2						
483.xalancbmk	89.8	76.9	<u>89.8</u>	<u>76.9</u>	89.3	77.3						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C6 State

Minimum Processor Idle Power Package C-State set to Package C6 (retention) State

Energy/Performance Bias set to Maximum Performance

QPI Snoop Configuration set to Home Snoop

Collaborative Power Control set to Disabled

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Intel Hyperthreading set to Disabled

Sysinfo program /home/intel\_binary/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on linux-vi0i Sun May 15 14:15:51 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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<http://www.spec.org/>

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## Platform Notes (Continued)

model name : Intel(R) Xeon(R) CPU E7-8891 v4 @ 2.80GHz

4 "physical id"s (chips)

40 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 10

siblings : 10

physical 0: cores 5 9 10 11 13 18 24 26 28 29

physical 1: cores 5 9 10 11 13 18 24 26 28 29

physical 2: cores 5 9 10 11 13 18 24 26 28 29

physical 3: cores 5 9 10 11 13 18 24 26 28 29

cache size : 61440 KB

From /proc/meminfo

MemTotal: 529321304 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 1

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP1"

VERSION\_ID="12.1"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP1"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:

Linux linux-vi0i 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015  
(8d714a0) x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 May 15 13:56

SPEC is set to: /home/intel\_binary/cpu2006

Filesystem Type Size Used Avail Use% Mounted on  
/dev/nvme0nlp4 xfs 703G 23G 681G 4% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS HP U17 04/26/2016

Memory:

64x UNKNOWN NOT AVAILABLE

32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:  
32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/intel\_binary/cpu2006/libs/32:/home/intel\_binary/cpu2006/libs/64:/home/intel\_binary/cpu2006/sh"

OMP\_NUM\_THREADS = "40"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalanbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jun 30 13:53:38 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 6 June 2016.

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