SPEC® CINT2006 Result

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HITACHI

Compute Blade 520X (Intel Xeon E7-8890 v4)

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SPECint_rate_base2006 = 6770

CPU2006 license: 35
Test sponsor: HITACHI
Tested by: HITACHI

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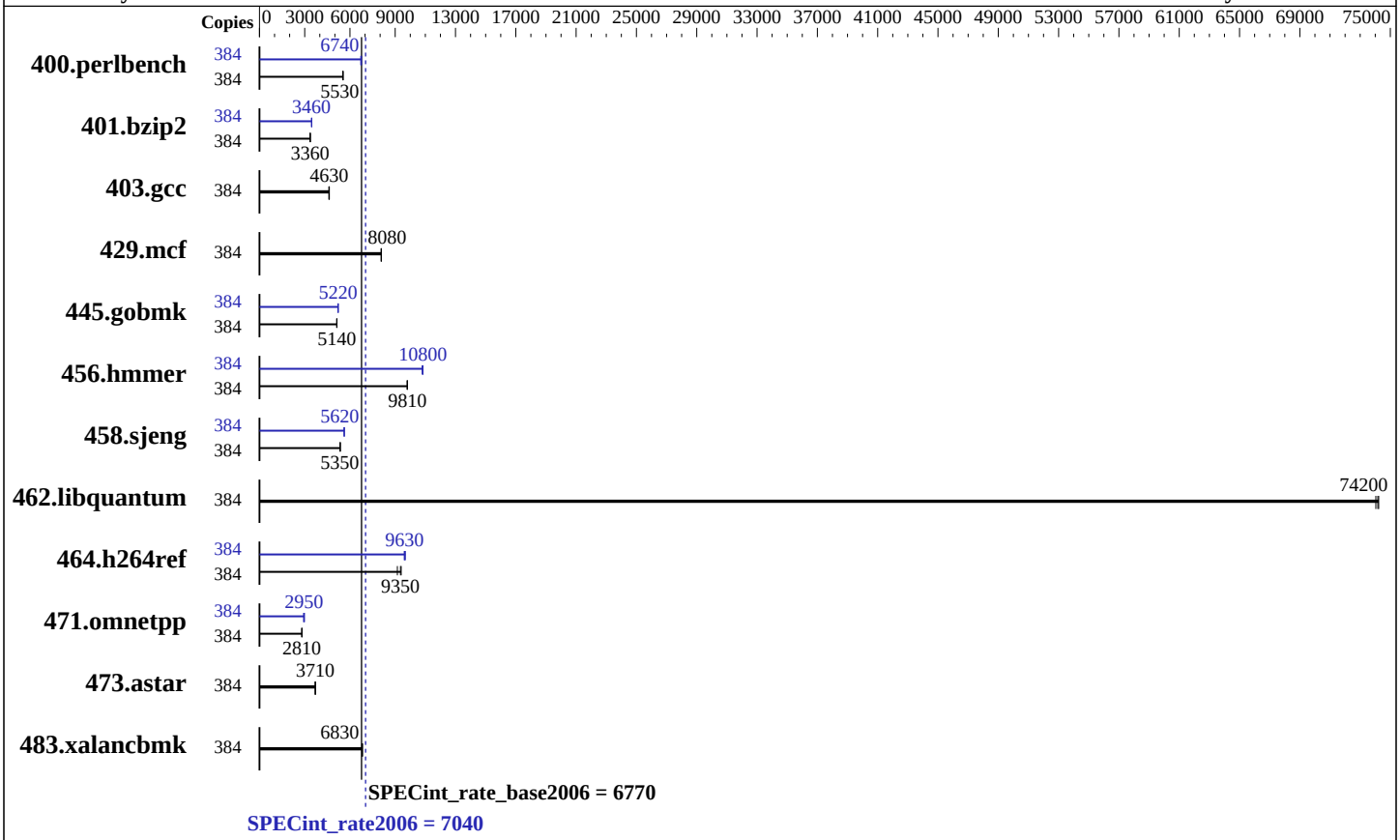
Hardware Availability: Jun-2016
Software Availability: Mar-2016



Hardware

CPU Name:	Intel Xeon E7-8890 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	192 cores, 8 chips, 24 cores/chip, 2 threads/core
CPU(s) orderable:	2,4,8 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	60 MB I+D on chip per chip
Other Cache:	None
Memory:	3 TB (192 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem:	2 x 600 GB SAS, 15000 RPM, RAID1
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 3.12.28-4-default
Compiler:	C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	384	680	5520	**679**	**5530**	675	5560	384	558	6720	554	6770	**557**	**6740**
401.bzip2	384	**1102**	**3360**	1104	3360	1098	3370	384	1073	3450	**1071**	**3460**	1069	3470
403.gcc	384	672	4600	667	4630	**668**	**4630**	384	672	4600	667	4630	**668**	**4630**
429.mcf	384	433	8080	434	8070	**434**	**8080**	384	433	8080	434	8070	**434**	**8080**
445.gobmk	384	**784**	**5140**	787	5120	784	5140	384	775	5200	771	5230	**772**	**5220**
456.hmmer	384	365	9810	**365**	**9810**	366	9790	384	332	10800	331	10800	**331**	**10800**
458.sjeng	384	**868**	**5350**	869	5350	868	5360	384	826	5620	**827**	**5620**	827	5620
462.libquantum	384	107	74000	**107**	**74200**	107	74200	384	107	74000	**107**	**74200**	107	74200
464.h264ref	384	**909**	**9350**	904	9400	929	9140	384	878	9680	885	9600	**882**	**9630**
471.omnetpp	384	852	2820	855	2810	**854**	**2810**	384	820	2930	804	2980	**814**	**2950**
473.astar	384	727	3710	730	3690	**727**	**3710**	384	727	3710	730	3690	**727**	**3710**
483.xalancbmk	384	390	6800	**388**	**6830**	388	6830	384	390	6800	**388**	**6830**	388	6830

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
 Memory Power Management = Automatic
 Active Energy Manager = "Capping Disabled"
 Platform Controlled Type = "Maximum Performance"
 C1 Enhanced Mode = Disable
 Sysinfo program /home/cpu2006/config/sysinfo.rev6914
 $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
 running on SLES12 Sun May 15 00:27:45 2016

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E7-8890 v4 @ 2.20GHz
       8 "physical id"s (chips)
       384 "processors"
```

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Platform Notes (Continued)

```
      cores, siblings (Caution: counting these is hw and system dependent.  The
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 24
         siblings  : 48
         physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
         physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
         physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
         physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
         physical 4: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
         physical 5: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
         physical 6: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
         physical 7: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
      cache size : 61440 KB

   From /proc/meminfo
      MemTotal:       3176250832 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      SUSE Linux Enterprise Server 12

   From /etc/*release* /etc/*version*
      SuSE-release:
         SUSE Linux Enterprise Server 12 (x86_64)
         VERSION = 12
         PATCHLEVEL = 0
         # This file is deprecated and will be removed in a future service pack or
         release.
         # Please check /etc/os-release for details about this release.
      os-release:
         NAME="SLES"
         VERSION="12"
         VERSION_ID="12"
         PRETTY_NAME="SUSE Linux Enterprise Server 12"
         ID="sles"
         ANSI_COLOR="0;32"
         CPE_NAME="cpe:/o:suse:sles:12"

   uname -a:
      Linux SLES12 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014 (9879bd4)
      x86_64 x86_64 x86_64 GNU/Linux
```

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Platform Notes (Continued)

```
 run-level 3 May 11 17:50

 SPEC is set to: /home/cpu2006
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda4      xfs   516G   19G  498G   4% /home
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HITACHI 11-00 04/21/2016
   Memory:
    76x 0x0000 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
    2x 0x0001 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
    2x 0x0002 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
    4x 0x0003 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
    2x 0x0300 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
    2x 0x0400 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
    2x 0x0402 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
    2x 0x0603 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
    2x 0x0E0A M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
    2x 0x5C00 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
    96x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

 Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB
 memory using RedHat EL 7.2 glibc 2.17
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Base Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-D_FILE_OFFSET_BITS=64`
403.gcc: `-D_FILE_OFFSET_BITS=64`
429.mcf: `-D_FILE_OFFSET_BITS=64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-D_FILE_OFFSET_BITS=64`
458.sjeng: `-D_FILE_OFFSET_BITS=64`
462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
464.h264ref: `-D_FILE_OFFSET_BITS=64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-D_FILE_OFFSET_BITS=64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
-opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-ansi-alias

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-ansi-alias` `-opt-ra-region-strategy=block -Wl,-z,muldefs` `-L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.xml`

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