SPEC® CFP2006 Result
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Supermicro

SuperServer 6038R-TXR
(X10DRX , Intel Xeon E5-2630 v4)

$SPECfp^{®}\_rate2006 = 665$

$SPECfp\_rate\_base2006 = 650$
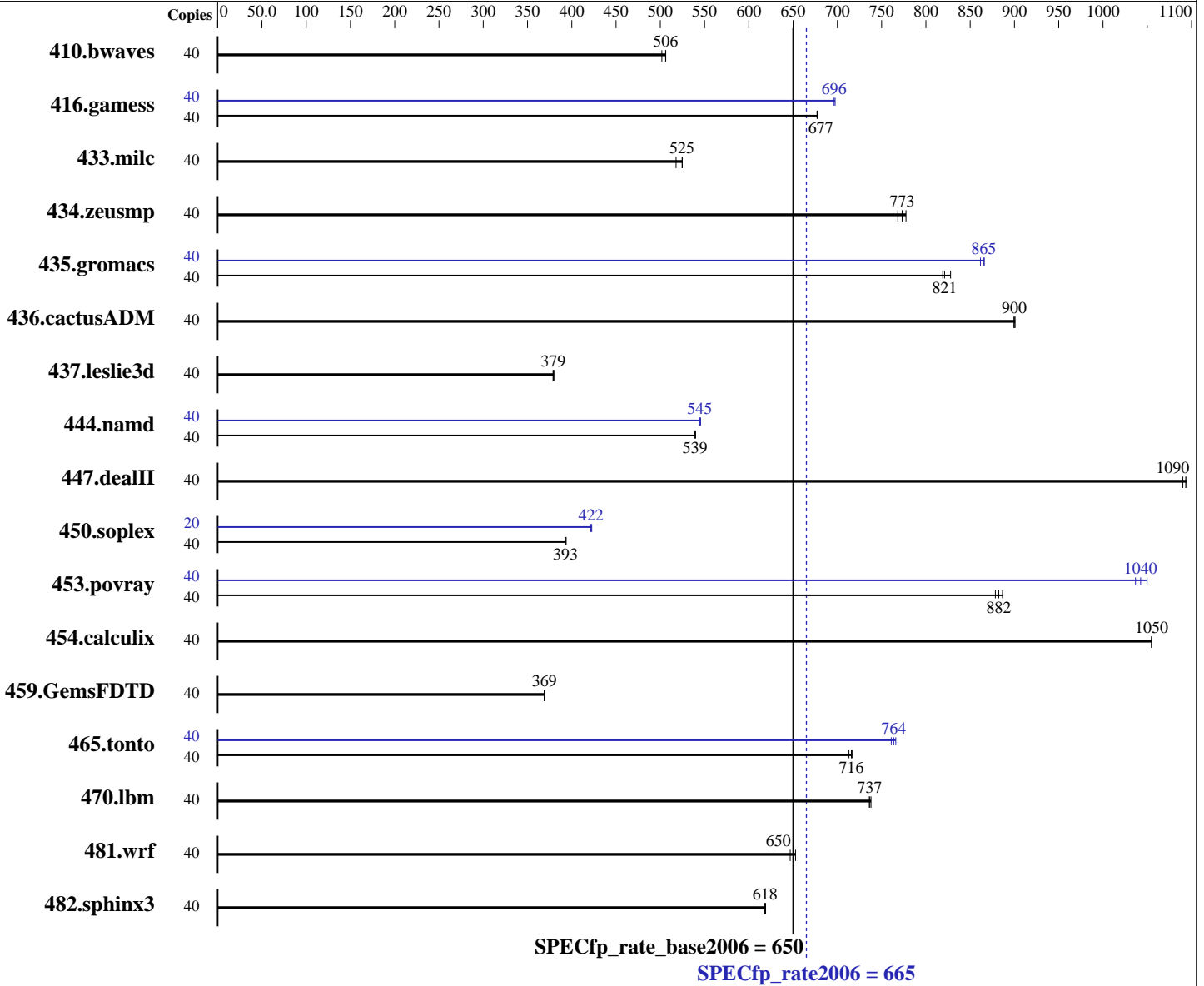
CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: May-2016
Hardware Availability: Mar-2016
Software Availability: Sep-2015

Copies	
410.bwaves	40 — 506
416.gamess	40 — 696 / 40 — 677
433.milc	40 — 525
434.zeusmp	40 — 773
435.gromacs	40 — 865 / 40 — 821
436.cactusADM	40 — 900
437.leslie3d	40 — 379
444.namd	40 — 545 / 40 — 539
447.dealII	40 — 1090
450.soplex	20 — 422 / 40 — 393
453.povray	40 — 1040 / 40 — 882
454.calculix	40 — 1050
459.GemsFDTD	40 — 369
465.tonto	40 — 764 / 40 — 716
470.lbm	40 — 737
481.wrf	40 — 650
482.sphinx3	40 — 618

SPECfp_rate_base2006 = 650

SPECfp_rate2006 = 665

Hardware

CPU Name: Intel Xeon E5-2630 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.2, Kernel 3.10.0-327.el7.x86_64
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)

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L3 Cache:	25 MB I+D on chip per chip	Base Pointers:	32/64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)	Other Software:	None
Disk Subsystem:	1 x 1000 GB SATA III, 7200 RPM		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	40	1083	502	1074	506	**1074**	**506**	40	1083	502	1074	506	**1074**	**506**
416.gamess	40	**1157**	**677**	1156	677	1157	677	40	**1125**	**696**	1126	695	1123	697
433.milc	40	709	518	700	525	**700**	**525**	40	709	518	700	525	**700**	**525**
434.zeusmp	40	**471**	**773**	468	778	474	768	40	**471**	**773**	468	778	474	768
435.gromacs	40	345	828	349	819	**348**	**821**	40	**330**	**865**	331	862	330	866
436.cactusADM	40	531	899	531	901	**531**	**900**	40	531	899	531	901	**531**	**900**
437.leslie3d	40	992	379	990	380	**992**	**379**	40	992	379	990	380	**992**	**379**
444.namd	40	595	539	594	540	**595**	**539**	40	588	546	589	544	**589**	**545**
447.dealII	40	418	1090	420	1090	**419**	**1090**	40	418	1090	420	1090	**419**	**1090**
450.soplex	40	**849**	**393**	847	394	850	393	20	396	422	**395**	**422**	395	423
453.povray	40	**241**	**882**	242	878	240	886	40	205	1040	203	1050	**204**	**1040**
454.calculix	40	**313**	**1050**	313	1050	313	1060	40	**313**	**1050**	313	1050	313	1060
459.GemsFDTD	40	1148	370	1151	369	**1150**	**369**	40	1148	370	1151	369	**1150**	**369**
465.tonto	40	549	717	**550**	**716**	552	713	40	**515**	**764**	517	761	514	766
470.lbm	40	748	735	745	738	**746**	**737**	40	748	735	745	738	**746**	**737**
481.wrf	40	691	647	**687**	**650**	685	653	40	691	647	**687**	**650**	685	653
482.sphinx3	40	**1261**	**618**	1262	618	1260	619	40	**1261**	**618**	1262	618	1260	619

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Early Snoop = Disable
Sysinfo program /home/cpu2006_ic16/config/sysinfo.rev6914

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Platform Notes (Continued)

```
 $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
 running on localhost.localdomain Fri May 13 04:46:02 2016

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2630 v4 @ 2.20GHz
       2 "physical id"s (chips)
       40 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 10
       siblings  : 20
       physical 0: cores 0 1 2 3 4 8 9 10 11 12
       physical 1: cores 0 1 2 3 4 8 9 10 11 12
    cache size : 25600 KB

 From /proc/meminfo
    MemTotal:       528083380 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux Server"
       VERSION="7.2 (Maipo)"
       ID="rhel"
       ID_LIKE="fedora"
       VERSION_ID="7.2"
       PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
       ANSI_COLOR="0;31"
       CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
    redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
    system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

 uname -a:
    Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
    EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

 run-level 3 May 12 16:37

 SPEC is set to: /home/cpu2006_ic16
    Filesystem            Type  Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs   216G   39G  178G  18% /home
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
```

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Platform Notes (Continued)

```
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS American Megatrends Inc. 2.0a 03/17/2016
  Memory:
   16x Micron 36ASF4G72PZ-2G3A1 32 GB 2 rank 2400 MHz, configured at 2133 MHz

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006_ic16/libs/32:/home/cpu2006_ic16/libs/64:/home/cpu2006_ic16/sh"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
    icc   -m64
```

C++ benchmarks:
```
    icpc  -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
```

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Test sponsor:	Supermicro	**Hardware Availability:**	Mar-2016
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Base Portability Flags (Continued)

```
   444.namd: -DSPEC_CPU_LP64
  447.dealII: -DSPEC_CPU_LP64
  450.soplex: -DSPEC_CPU_LP64
  453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
   465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc   -m64
```

C++ benchmarks (except as noted below):

```
icpc  -m64
```

```
  450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc   -m64 ifort -m64
```

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Peak Portability Flags

```
   410.bwaves: -DSPEC_CPU_LP64
   416.gamess: -DSPEC_CPU_LP64
     433.milc: -DSPEC_CPU_LP64
   434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -D_FILE_OFFSET_BITS=64
    453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
   433.milc: basepeak = yes

   470.lbm: basepeak = yes

   482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
   444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -fno-alias -auto-ilp32

   447.dealII: basepeak = yes

   450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -opt-malloc-options=3

   453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -unroll4 -ansi-alias
```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

```
410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
           -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml

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(X10DRX , Intel Xeon E5-2630 v4)

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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