



# SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(2.60 GHz, Intel Xeon E5-2623 v4)

**SPECint®2006 = 54.5**

**54.5**

**SPECint\_base2006 = 52.5**

**52.5**

CPU2006 license: 3

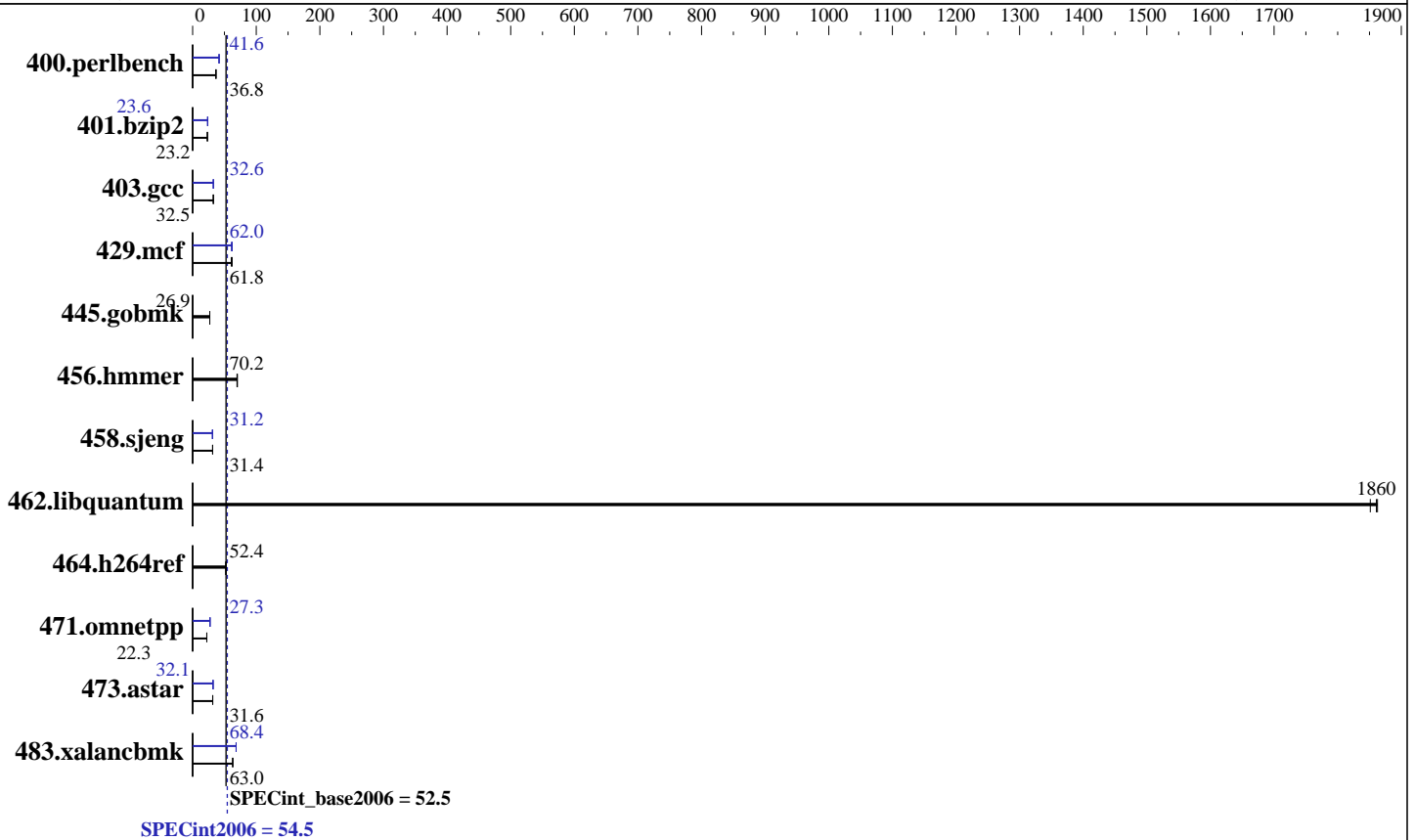
Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



## Hardware

CPU Name: Intel Xeon E5-2623 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 7.2, (Maipo)  
 Kernel 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(2.60 GHz, Intel Xeon E5-2623 v4)

SPECint2006 = 54.5

SPECint\_base2006 = 52.5

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	270	36.1	265	36.9	<b>266</b>	<b>36.8</b>	<b>235</b>	<b>41.6</b>	236	41.5	235	41.6
401.bzip2	418	23.1	<b>416</b>	<b>23.2</b>	415	23.2	408	23.6	<b>409</b>	<b>23.6</b>	410	23.5
403.gcc	248	32.5	<b>248</b>	<b>32.5</b>	247	32.6	247	32.6	<b>247</b>	<b>32.6</b>	247	32.5
429.mcf	<b>148</b>	<b>61.8</b>	150	60.7	147	61.9	150	61.0	<b>147</b>	<b>62.0</b>	147	62.1
445.gobmk	389	26.9	<b>390</b>	<b>26.9</b>	390	26.9	389	26.9	<b>390</b>	<b>26.9</b>	390	26.9
456.hammer	133	70.2	<b>133</b>	<b>70.2</b>	134	69.7	133	70.2	<b>133</b>	<b>70.2</b>	134	69.7
458.sjeng	385	31.4	<b>385</b>	<b>31.4</b>	384	31.5	388	31.2	<b>388</b>	<b>31.2</b>	388	31.2
462.libquantum	<b>11.1</b>	<b>1860</b>	11.2	1850	11.1	1860	<b>11.1</b>	<b>1860</b>	11.2	1850	11.1	1860
464.h264ref	<b>423</b>	<b>52.4</b>	422	52.4	423	52.3	<b>423</b>	<b>52.4</b>	422	52.4	423	52.3
471.omnetpp	<b>280</b>	<b>22.3</b>	281	22.2	280	22.3	<b>229</b>	<b>27.3</b>	229	27.3	228	27.4
473.astar	222	31.6	<b>222</b>	<b>31.6</b>	223	31.5	220	32.0	<b>218</b>	<b>32.1</b>	218	32.2
483.xalancbmk	109	63.1	<b>109</b>	<b>63.0</b>	110	62.9	<b>101</b>	<b>68.4</b>	101	68.3	101	68.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C1E State

Minimum Processor Idle Power Package C-State set to No Package State

QPI Snoop Configuration set to Home Snoop

Collaborative Power Control set to Disabled

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x Refresh

Intel Hyperthreading set to Disabled

Sysinfo program /home/intel\_binary/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on DL380Gen9allbin Mon Mar 28 22:13:19 2016

This section contains SUT (System Under Test) info as seen by

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 2



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380 Gen9**

(2.60 GHz, Intel Xeon E5-2623 v4)

**SPECint2006 =**

**54.5**

**SPECint\_base2006 =**

**52.5**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Nov-2015

## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2623 v4@ 2.60GHz

2 "physical id"s (chips)

8 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4

siblings : 4

physical 0: cores 0 1 2 3

physical 1: cores 0 1 2 3

cache size : 10240 KB

From /proc/meminfo

MemTotal: 528069676 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.2 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.2"

PRETTY\_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.2:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.2:ga:server

uname -a:

Linux DL380Gen9allbin 3.10.0-327.el7.x86\_64 #1 SMP Thu Oct 29 17:29:29 EDT

2015 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Mar 28 21:35

SPEC is set to: /home/intel\_binary/cpu2006

Filesystem Type Size Used Avail Use% Mounted on

/dev/sda5 xfs 318G 78G 241G 25% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 03/10/2016

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

<http://www.spec.org/>

Page 3



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9  
(2.60 GHz, Intel Xeon E5-2623 v4)

SPECint2006 = 54.5

SPECint\_base2006 = 52.5

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Platform Notes (Continued)

Memory:

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:  
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "8"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(2.60 GHz, Intel Xeon E5-2623 v4)

SPECint2006 =

54.5

SPECint\_base2006 =

52.5

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(2.60 GHz, Intel Xeon E5-2623 v4)

SPECint2006 =

54.5

SPECint\_base2006 =

52.5

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias

401.bzp2: -xAVX(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -par-num-threads=1(pass 1)  
 -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch  
 -auto-p32

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: -xAVX(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)  
 -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)  
 -prof-use(pass 2) -opt-ra-region-strategy=block  
 -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

473.aster: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
 -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380 Gen9**

(2.60 GHz, Intel Xeon E5-2623 v4)

**SPECint2006 =**

**54.5**

**SPECint\_base2006 =**

**52.5**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Nov-2015

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Tue May 3 18:00:24 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 3 May 2016.