SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX2540 M1, Intel Xeon E5-2630L v3, 1.8 GHz

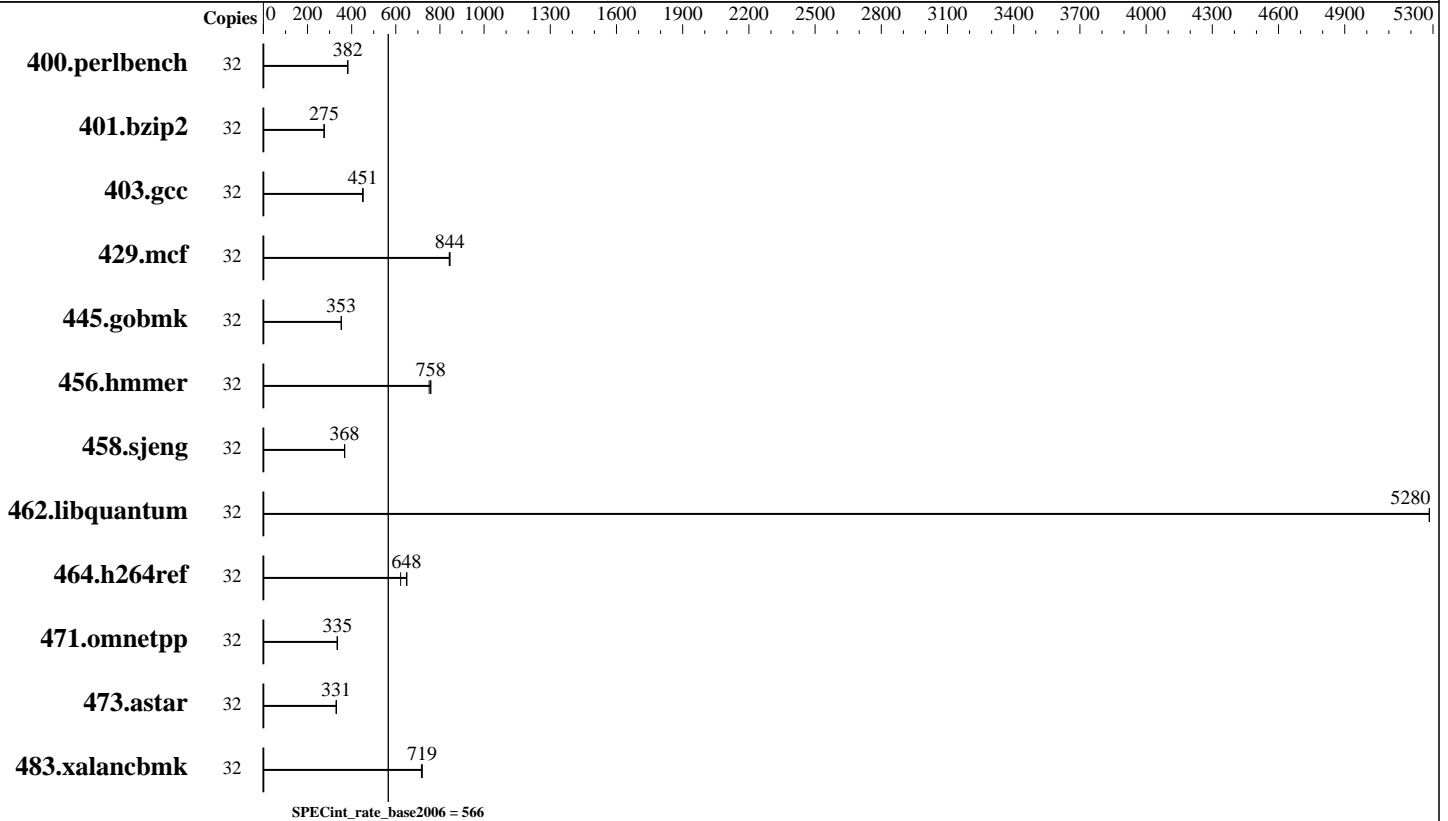SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 566

CPU2006 license: 19	**Test date:**	Mar-2016
Test sponsor: Fujitsu	**Hardware Availability:**	Sep-2014
Tested by: Fujitsu	**Software Availability:**	Sep-2015

Copies 0 200 400 600 800 1000 1300 1600 1900 2200 2500 2800 3100 3400 3700 4000 4300 4600 4900 5300

```
400.perlbench   32   382
401.bzip2       32   275
403.gcc         32   451
429.mcf         32   844
445.gobmk       32   353
456.hmmer       32   758
458.sjeng       32   368
462.libquantum  32   5280
464.h264ref     32   648
471.omnetpp     32   335
473.astar       32   331
483.xalancbmk   32   719
```

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Hardware

CPU Name:	Intel Xeon E5-2630L v3
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (8 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 (x86_64) Kernel 3.12.49-11-default
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	**818**	**382**	817	383	819	382							
401.bzip2	32	1120	276	**1121**	**275**	1121	275							
403.gcc	32	570	452	**571**	**451**	573	449							
429.mcf	32	345	846	347	842	**346**	**844**							
445.gobmk	32	951	353	**950**	**353**	950	353							
456.hmmer	32	394	759	397	751	**394**	**758**							
458.sjeng	32	1051	368	1053	368	**1052**	**368**							
462.libquantum	32	125	5280	**125**	**5280**	125	5280							
464.h264ref	32	**1092**	**648**	1138	622	1089	650							
471.omnetpp	32	598	335	596	335	**597**	**335**							
473.astar	32	**680**	**331**	679	331	681	330							
483.xalancbmk	32	307	719	**307**	**719**	308	716							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
BIOS configuration:
Energy Performance = Performance
Utilization Profile = Unbalanced
QPI snoop mode: Early Snoop
    COD Enable = Disabled, Early Snoop = Enabled
CPU C1E Support = Disabled
QPI Link Frequency Select = 6.4 GT/s
Sysinfo program /home/SPECcpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on RX2540M1 Mon Mar 21 13:39:16 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2630L v3 @ 1.80GHz
```

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Platform Notes (Continued)

```
    2 "physical id"s (chips)
    32 "processors"
 cores, siblings (Caution: counting these is hw and system dependent.  The
 following excerpts from /proc/cpuinfo might not be reliable.  Use with
 caution.)
    cpu cores : 8
    siblings  : 16
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
 cache size : 20480 KB

From /proc/meminfo
 MemTotal:       131805368 kB
 HugePages_Total:        0
 Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
 SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
 SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 1
    # This file is deprecated and will be removed in a future service pack or
    release.
    # Please check /etc/os-release for details about this release.
 os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
 Linux RX2540M1 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 21 13:38 last=5

SPEC is set to: /home/SPECcpu2006
 Filesystem      Type  Size  Used Avail Use% Mounted on
 /dev/sda3       xfs   890G  3.3G  886G   1% /home
Additional information from dmidecode:

 Warning: Use caution when you interpret this section. The 'dmidecode' program
 reads system data which is "intended to allow hardware to be accurately
 determined", but the intent may not be met, as there are frequent changes to
 hardware, firmware, and the "DMTF SMBIOS" standard.
```

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Platform Notes (Continued)

```
   BIOS FUJITSU // American Megatrends Inc. V5.0.0.9 R1.24.0 for D3289-A1x
   05/12/2015
   Memory:
    16x NO DIMM NO DIMM
    8x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1866 MHz

  (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

  Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
  memory using RedHat EL 7.1
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled
  Filesystem page cache cleared with:
  echo 1>        /proc/sys/vm/drop_caches
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>

  For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
     icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks:
```
     icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Base Portability Flags

```
    400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
        401.bzip2: -D_FILE_OFFSET_BITS=64
          403.gcc: -D_FILE_OFFSET_BITS=64
          429.mcf: -D_FILE_OFFSET_BITS=64
        445.gobmk: -D_FILE_OFFSET_BITS=64
       456.hmmer: -D_FILE_OFFSET_BITS=64
        458.sjeng: -D_FILE_OFFSET_BITS=64
    462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
      464.h264ref: -D_FILE_OFFSET_BITS=64
      471.omnetpp: -D_FILE_OFFSET_BITS=64
        473.astar: -D_FILE_OFFSET_BITS=64
    483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

        403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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