



# SPEC® CINT2006 Result

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## Supermicro

Supermicro X11SAE-M motherboard  
(X11SAE-M, Intel Xeon E3-1280 v5)

**SPECint®2006 = 74.8**

**SPECint\_base2006 = 72.7**

CPU2006 license: 001176

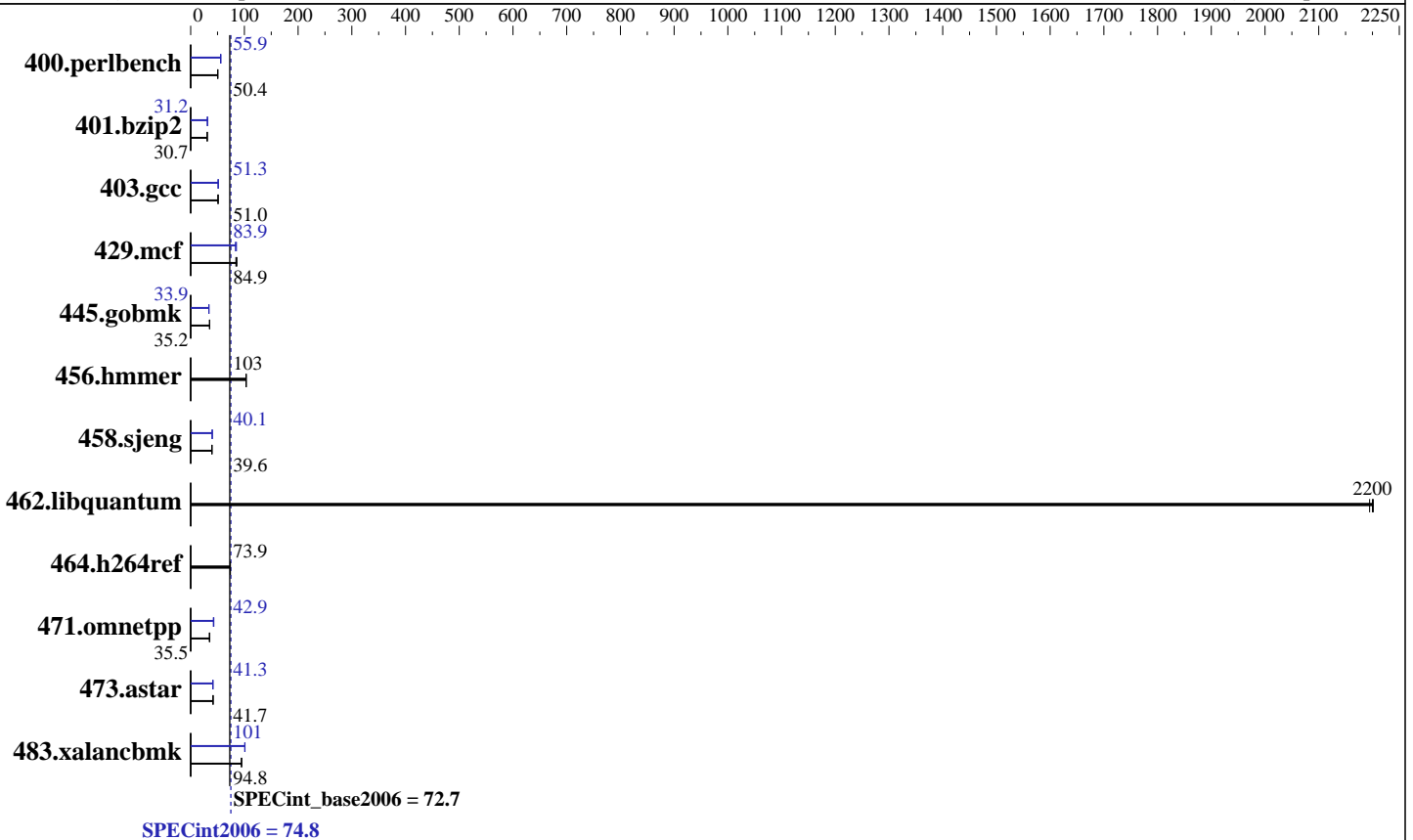
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Oct-2015

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Xeon E3-1280 v5  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
 CPU MHz: 3700  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (2 x 16 GB 2Rx8 PC4-2133P-E)  
 Disk Subsystem: 1 x 400 GB SATA III SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86\_64  
 Compiler: C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	194	50.3	193	50.5	<b>194</b>	<b>50.4</b>	175	55.9	174	56.0	<b>175</b>	<b>55.9</b>
401.bzip2	<b>314</b>	<b>30.7</b>	312	30.9	314	30.7	309	31.2	309	31.2	<b>309</b>	<b>31.2</b>
403.gcc	158	51.1	<b>158</b>	<b>51.0</b>	158	50.9	157	51.3	<b>157</b>	<b>51.3</b>	157	51.2
429.mcf	108	84.1	106	86.1	<b>107</b>	<b>84.9</b>	107	85.3	109	83.4	<b>109</b>	<b>83.9</b>
445.gobmk	298	35.2	298	35.2	<b>298</b>	<b>35.2</b>	309	33.9	309	33.9	<b>309</b>	<b>33.9</b>
456.hammer	90.3	103	90.4	103	<b>90.4</b>	<b>103</b>	90.3	103	90.4	103	<b>90.4</b>	<b>103</b>
458.sjeng	306	39.6	<b>306</b>	<b>39.6</b>	306	39.6	<b>302</b>	<b>40.1</b>	302	40.1	302	40.1
462.libquantum	9.44	2190	<b>9.42</b>	<b>2200</b>	9.41	2200	9.44	2190	<b>9.42</b>	<b>2200</b>	9.41	2200
464.h264ref	300	73.8	299	73.9	<b>300</b>	<b>73.9</b>	300	73.8	299	73.9	<b>300</b>	<b>73.9</b>
471.omnetpp	181	34.6	176	35.6	<b>176</b>	<b>35.5</b>	<b>146</b>	<b>42.9</b>	148	42.2	145	43.0
473.astar	<b>168</b>	<b>41.7</b>	168	41.7	169	41.6	171	41.1	169	41.6	<b>170</b>	<b>41.3</b>
483.xalancbmk	72.8	94.8	73.0	94.5	<b>72.8</b>	<b>94.8</b>	68.2	101	68.5	101	<b>68.4</b>	<b>101</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

As tested, the system used a Supermicro CSE-731i-300B chassis. The chassis is configured with 2 PWS-305-PQ redundant power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0108L4 rear cooling fan.

BIOS Settings:

Hyper-threading = Disabled

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1

running on X10SRA-01 Wed Jan 27 18:24:45 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E3-1280 v5 @ 3.70GHz

1 "physical id"s (chips)

4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with

Continued on next page



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### Platform Notes (Continued)

```
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
  cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:      32760744 kB
HugePages_Total:    0
Hugepagesize:   2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.1 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.1"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

```
uname -a:
Linux X10SRA-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 27 18:19
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   183G  5.3G 178G   3% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS American Megatrends Inc. 1.0a 12/21/2015
Memory:
  2x Not Specified Not Specified
  2x Samsung M391A2K43BB1-CPB 16 GB 2 rank 2133 MHz
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmarthheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

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## Peak Optimization Flags (Continued)

401.bzip2 (continued):

`-opt-prefetch -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc`

`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel`

`-opt-prefetch -auto-p32`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`

`-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias`

456.hmmcr: `basepeak = yes`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`

`-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)`

`-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`

`-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)`

`-par-num-threads=1(pass 1) -prof-use(pass 2)`

`-opt-ra-region-strategy=block`

`-ansi-alias`

`-Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch`

`-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64`

483.xalancbmk: `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch`

`-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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