



SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

Supermicro X11SAE-M motherboard
(X11SAE-M, Intel Core i5-6500T)

SPECint®_rate2006 = 172

SPECint_rate_base2006 = 167

CPU2006 license: 001176

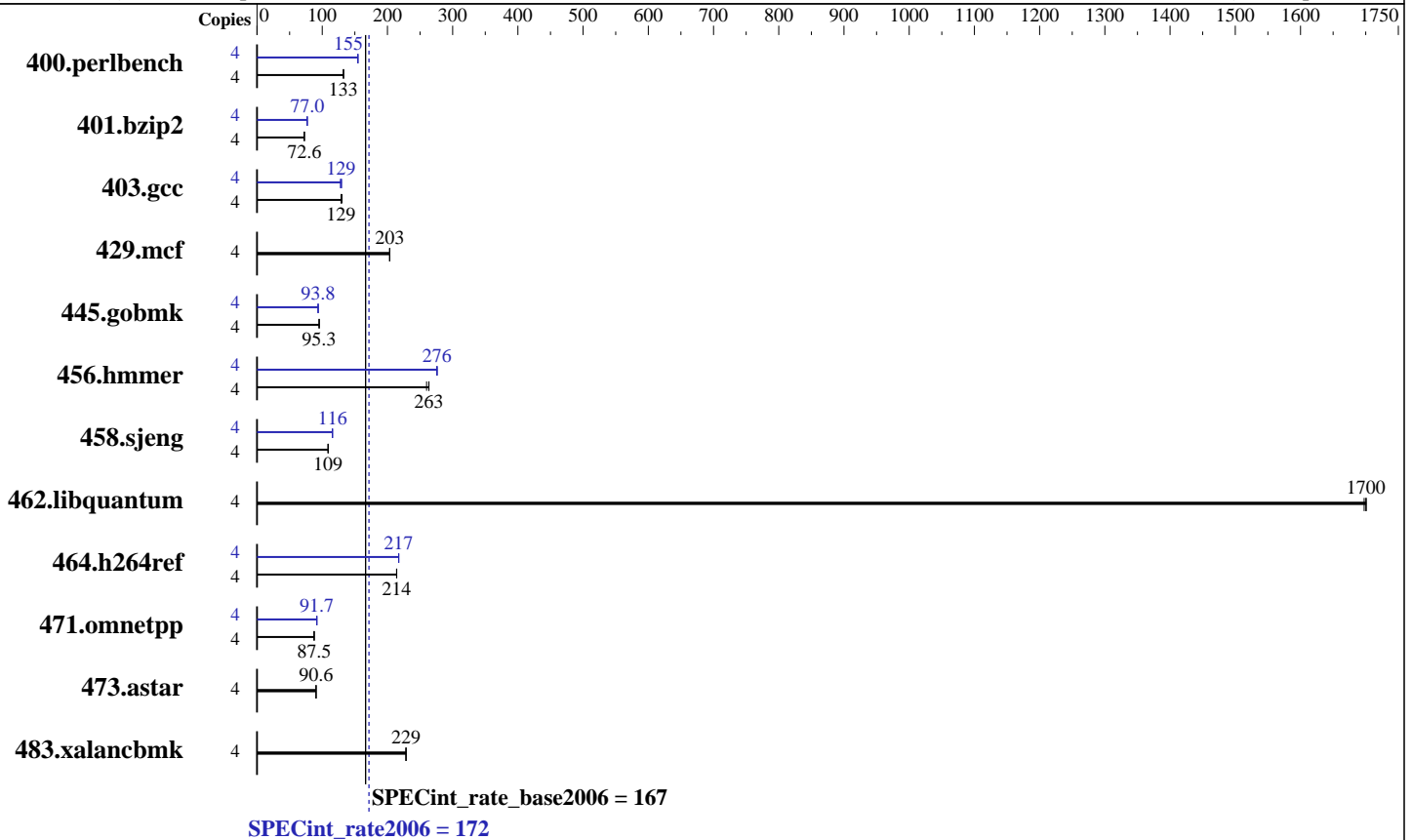
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Sep-2015

Software Availability: Sep-2015



Hardware

CPU Name: Intel Core i5-6500T
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 6 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 1Rx8 PC4-2133P-U)
 Disk Subsystem: 1 x 400 GB SATA III SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.e17.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

Supermicro X11SAE-M motherboard
(X11SAE-M, Intel Core i5-6500T)

SPECint_rate2006 = 172

SPECint_rate_base2006 = 167

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jan-2016
Hardware Availability: Sep-2015
Software Availability: Sep-2015

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	295	133	<u>295</u>	<u>133</u>	295	133	4	252	155	253	155	<u>253</u>	<u>155</u>
401.bzip2	4	<u>532</u>	<u>72.6</u>	532	72.5	532	72.6	4	502	76.9	<u>501</u>	<u>77.0</u>	501	77.1
403.gcc	4	247	130	<u>250</u>	<u>129</u>	250	129	4	252	128	<u>250</u>	<u>129</u>	248	130
429.mcf	4	179	203	179	204	<u>179</u>	<u>203</u>	4	179	203	179	204	<u>179</u>	<u>203</u>
445.gobmk	4	441	95.1	<u>440</u>	<u>95.3</u>	440	95.4	4	447	93.9	<u>447</u>	<u>93.8</u>	449	93.5
456.hammer	4	<u>142</u>	<u>263</u>	144	260	142	264	4	135	276	<u>135</u>	<u>276</u>	135	275
458.sjeng	4	444	109	444	109	<u>444</u>	<u>109</u>	4	418	116	418	116	<u>418</u>	<u>116</u>
462.libquantum	4	<u>48.7</u>	<u>1700</u>	48.7	1700	48.8	1700	4	<u>48.7</u>	<u>1700</u>	48.7	1700	48.8	1700
464.h264ref	4	414	214	413	214	<u>414</u>	<u>214</u>	4	407	217	408	217	<u>407</u>	<u>217</u>
471.omnetpp	4	285	87.8	286	87.5	<u>286</u>	<u>87.5</u>	4	273	91.5	272	91.9	<u>273</u>	<u>91.7</u>
473.astar	4	<u>310</u>	<u>90.6</u>	310	90.6	310	90.5	4	<u>310</u>	<u>90.6</u>	310	90.6	310	90.5
483.xalancbmk	4	<u>121</u>	<u>229</u>	121	229	121	228	4	<u>121</u>	<u>229</u>	121	229	121	228

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

As tested, the system used a Supermicro CSE-731i-300B chassis. The chassis is configured with 2 PWS-305-PQ redundant power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0108L4 rear cooling fan.

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Fri Jan 29 17:42:31 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Core(TM) i5-6500T CPU @ 2.50GHz
1 "physical id"s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with
Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

Supermicro X11SAE-M motherboard
(X11SAE-M, Intel Core i5-6500T)

SPECint_rate2006 = 172

SPECint_rate_base2006 = 167

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Sep-2015

Software Availability: Sep-2015

Platform Notes (Continued)

```

caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
  cache size : 6144 KB

```

```

From /proc/meminfo
MemTotal:      16039968 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.1 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.1"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38
EST 2015 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jan 29 12:32

```

SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   369G  173G  197G  47% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

BIOS American Megatrends Inc. 1.0a 12/21/2015
Memory:
  4x Micron 8ATF51264AZ-2G1A2 4 GB 1 rank 2133 MHz

```

(End of data from sysinfo program)



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

Supermicro X11SAE-M motherboard
(X11SAE-M, Intel Core i5-6500T)

SPECint_rate2006 = 172

SPECint_rate_base2006 = 167

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jan-2016
Hardware Availability: Sep-2015
Software Availability: Sep-2015

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalanbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/sh -lsmartheap

Base Other Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

Supermicro X11SAE-M motherboard
(X11SAE-M, Intel Core i5-6500T)

SPECint_rate2006 = 172

SPECint_rate_base2006 = 167

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Sep-2015

Software Availability: Sep-2015

Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

Supermicro X11SAE-M motherboard
(X11SAE-M, Intel Core i5-6500T)

SPECint_rate2006 = 172

SPECint_rate_base2006 = 167

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Sep-2015

Software Availability: Sep-2015

Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

Standard Performance Evaluation Corporation

info@spec.org

<http://www.spec.org/>

Page 6



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

Supermicro X11SAE-M motherboard
(X11SAE-M, Intel Core i5-6500T)

SPECint_rate2006 = 172

SPECint_rate_base2006 = 167

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Sep-2015

Software Availability: Sep-2015

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Feb 23 17:36:47 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 February 2016.