



# SPEC<sup>®</sup> CFP2006 Result

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## Supermicro

SuperServer 5029S-TN2  
(X11SSV-Q , Intel Pentium G4400)

SPECfp<sup>®</sup>\_rate2006 = 107

SPECfp\_rate\_base2006 = 105

CPU2006 license: 001176

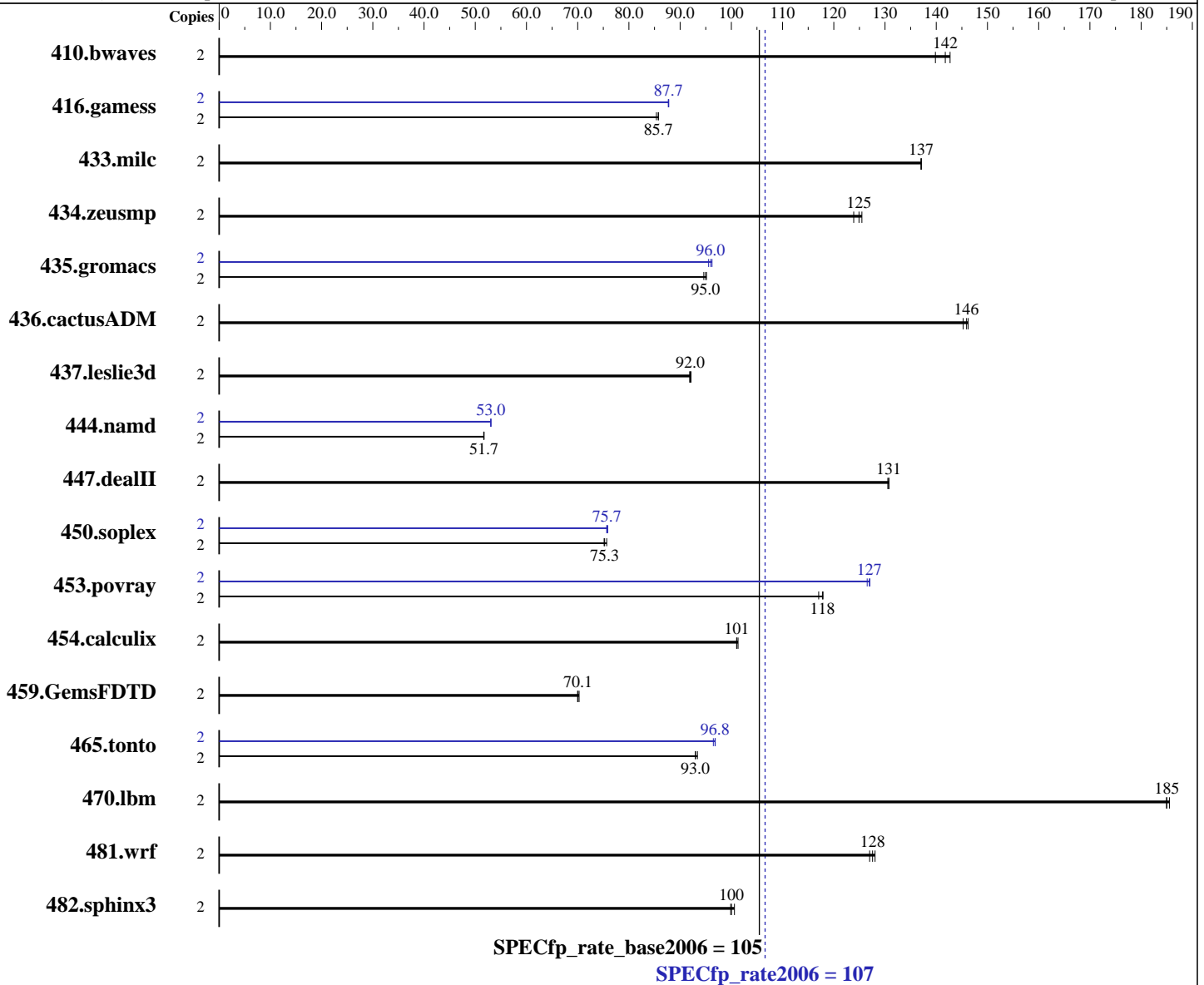
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Pentium G4400  
 CPU Characteristics:  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 3 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-U)  
Disk Subsystem: 1 x 750 GB SATA III, 7200 RPM  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	190	143	194	140	<u>192</u>	<u>142</u>	2	190	143	194	140	<u>192</u>	<u>142</u>
416.gamess	2	457	85.8	<u>457</u>	<u>85.7</u>	459	85.4	2	<u>446</u>	<u>87.7</u>	447	87.7	446	87.8
433.milc	2	<u>134</u>	<u>137</u>	134	137	134	137	2	<u>134</u>	<u>137</u>	134	137	134	137
434.zeusmp	2	147	124	<u>146</u>	<u>125</u>	145	125	2	147	124	<u>146</u>	<u>125</u>	145	125
435.gromacs	2	151	94.6	150	95.1	<u>150</u>	<u>95.0</u>	2	148	96.2	<u>149</u>	<u>96.0</u>	149	95.6
436.cactusADM	2	163	146	165	145	<u>164</u>	<u>146</u>	2	163	146	165	145	<u>164</u>	<u>146</u>
437.leslie3d	2	204	92.1	205	91.9	<u>204</u>	<u>92.0</u>	2	204	92.1	205	91.9	<u>204</u>	<u>92.0</u>
444.namd	2	311	51.6	<u>310</u>	<u>51.7</u>	310	51.7	2	302	53.1	<u>302</u>	<u>53.0</u>	302	53.0
447.dealII	2	<u>175</u>	<u>131</u>	175	131	175	131	2	<u>175</u>	<u>131</u>	175	131	175	131
450.soplex	2	222	75.2	220	75.7	<u>222</u>	<u>75.3</u>	2	220	75.9	220	75.7	<u>220</u>	<u>75.7</u>
453.povray	2	90.9	117	<u>90.3</u>	<u>118</u>	90.2	118	2	83.7	127	<u>83.8</u>	<u>127</u>	84.1	127
454.calculix	2	163	101	163	101	<u>163</u>	<u>101</u>	2	163	101	163	101	<u>163</u>	<u>101</u>
459.GemsFDTD	2	<u>303</u>	<u>70.1</u>	303	70.0	302	70.3	2	<u>303</u>	<u>70.1</u>	303	70.0	302	70.3
465.tonto	2	212	93.0	211	93.4	<u>212</u>	<u>93.0</u>	2	<u>203</u>	<u>96.8</u>	204	96.5	203	96.9
470.lbm	2	149	185	148	186	<u>149</u>	<u>185</u>	2	149	185	148	186	<u>149</u>	<u>185</u>
481.wrf	2	176	127	<u>175</u>	<u>128</u>	174	128	2	176	127	<u>175</u>	<u>128</u>	174	128
482.sphinx3	2	<u>390</u>	<u>100</u>	390	99.9	387	101	2	<u>390</u>	<u>100</u>	390	99.9	387	101

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /home/cpu2006\_ic16/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Fri Dec 18 12:08:49 2015

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### Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Pentium(R) CPU G4400 @ 3.30GHz
 1 "physical id"s (chips)
 2 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 2
  siblings  : 2
  physical 0: cores 0 1
cache size : 3072 KB
```

```
From /proc/meminfo
MemTotal:      16164312 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.1 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.1"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38
EST 2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 17 20:35
```

```
SPEC is set to: /home/cpu2006_ic16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   216G  185G  32G  86% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS American Megatrends Inc. 1.0a 11/03/2015

Memory:

2x Samsung M471A1G43DB0-CPB 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006\_ic16/libs/32:/home/cpu2006\_ic16/libs/64:/home/cpu2006\_ic16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB  
memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks (except as noted below):  
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -D\_FILE\_OFFSET\_BITS=64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes  
470.lbm: basepeak = yes  
482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)  
-prof-use(pass 2) -fno-alias -auto-ilp32  
447.dealII: basepeak = yes  
450.soplex: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)  
-prof-use(pass 2) -opt-malloc-options=3  
453.povray: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)  
-prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes  
416.gamess: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)  
-prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-  
434.zeusmp: basepeak = yes

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## Peak Optimization Flags (Continued)

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)  
-prof-use(pass 2) -unroll4 -auto -inline-alloc  
-opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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