# SPEC® CINT2006 Result



## Fujitsu

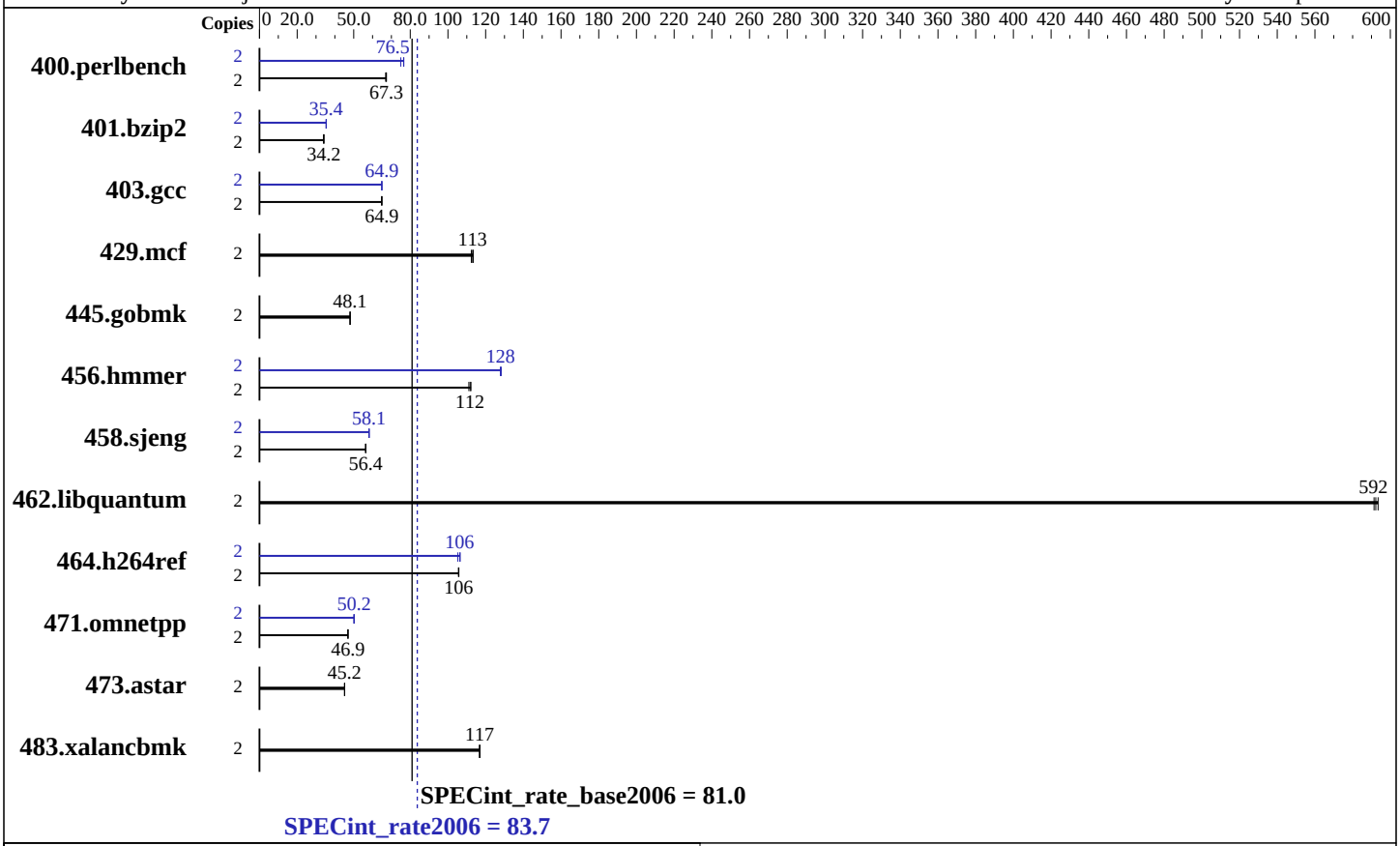
PRIMERGY RX1330 M2, Intel Celeron G3900, 2.80 GHz

SPECint®_rate2006 = 83.7

SPECint_rate_base2006 = 81.0

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Nov-2015
**Hardware Availability:** Feb-2016
**Software Availability:** Sep-2015




### Hardware

| | |
|---|---|
| CPU Name: | Intel Celeron G3900 |
| CPU Characteristics: | |
| CPU MHz: | 2800 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 2 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 64 GB (4 x 16 GB 2Rx8 PC4-2133P-E) |
| Disk Subsystem: | 1 x SATA, 500 GB, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 (x86_64) Kernel 3.12.48-52.27-default |
| Compiler: | C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.2 |

# SPEC CINT2006 Result



## Fujitsu

PRIMERGY RX1330 M2, Intel Celeron G3900, 2.80 GHz

SPECint_rate2006 = 83.7

SPECint_rate_base2006 = 81.0

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Nov-2015
**Hardware Availability:** Feb-2016
**Software Availability:** Sep-2015

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 2 | **290** | **67.3** | 292 | 66.9 | 290 | 67.4 | 2 | 255 | 76.6 | **255** | **76.5** | 261 | 75.0 |
| 401.bzip2 | 2 | **565** | **34.2** | 564 | 34.2 | 568 | 34.0 | 2 | 545 | 35.4 | **545** | **35.4** | 545 | 35.4 |
| 403.gcc | 2 | **248** | **64.9** | 249 | 64.8 | 248 | 65.0 | 2 | 248 | 65.0 | **248** | **64.9** | 248 | 64.9 |
| 429.mcf | 2 | **161** | **113** | 161 | 113 | 162 | 112 | 2 | **161** | **113** | 161 | 113 | 162 | 112 |
| 445.gobmk | 2 | **436** | **48.1** | 437 | 48.0 | 436 | 48.1 | 2 | **436** | **48.1** | 437 | 48.0 | 436 | 48.1 |
| 456.hmmer | 2 | 166 | 112 | **167** | **112** | 168 | 111 | 2 | 145 | 128 | 146 | 128 | **146** | **128** |
| 458.sjeng | 2 | 429 | 56.4 | 430 | 56.3 | **429** | **56.4** | 2 | 416 | 58.2 | **416** | **58.1** | 416 | 58.1 |
| 462.libquantum | 2 | 69.8 | 594 | **70.0** | **592** | 70.1 | 591 | 2 | 69.8 | 594 | **70.0** | **592** | 70.1 | 591 |
| 464.h264ref | 2 | **419** | **106** | 419 | 106 | 418 | 106 | 2 | 421 | 105 | **416** | **106** | 416 | 106 |
| 471.omnetpp | 2 | 266 | 47.0 | 267 | 46.9 | **266** | **46.9** | 2 | **249** | **50.2** | 249 | 50.1 | 249 | 50.2 |
| 473.astar | 2 | 311 | 45.1 | 311 | 45.2 | **311** | **45.2** | 2 | 311 | 45.1 | 311 | 45.2 | **311** | **45.2** |
| 483.xalancbmk | 2 | **118** | **117** | 118 | 117 | 118 | 117 | 2 | **118** | **117** | 118 | 117 | 118 | 117 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Sysinfo program /home/SPECcpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost Wed Nov 25 19:09:43 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Celeron(R) CPU G3900 @ 2.80GHz
      1 "physical id"s (chips)
      2 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 2
```

Continued on next page

## Platform Notes (Continued)

```
        siblings  : 2
        physical 0: cores 0 1
     cache size : 2048 KB

  From /proc/meminfo
     MemTotal:       65905868 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     SUSE Linux Enterprise Server 12

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 0
        # This file is deprecated and will be removed in a future service pack or
        release.
        # Please check /etc/os-release for details about this release.
     os-release:
        NAME="SLES"
        VERSION="12"
        VERSION_ID="12"
        PRETTY_NAME="SUSE Linux Enterprise Server 12"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12"

  uname -a:
     Linux localhost 3.12.48-52.27-default #1 SMP Mon Oct 5 10:08:10 UTC 2015
     (314f0e3) x86_64 x86_64 x86_64 GNU/Linux

  run-level 5 Nov 25 13:58

  SPEC is set to: /home/SPECcpu2006
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda4      xfs   424G  4.0G  420G   1% /home
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.0.0 for D3375-A1x
    10/27/2015
    Memory:
     4x SK Hynix HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

  (End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled

 For information about Fujitsu please visit: http://www.fujitsu.com
```

## Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

## Base Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-D_FILE_OFFSET_BITS=64`
403.gcc: `-D_FILE_OFFSET_BITS=64`
429.mcf: `-D_FILE_OFFSET_BITS=64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-D_FILE_OFFSET_BITS=64`
458.sjeng: `-D_FILE_OFFSET_BITS=64`
462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
464.h264ref: `-D_FILE_OFFSET_BITS=64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-D_FILE_OFFSET_BITS=64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

## Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
403.gcc: `-D_FILE_OFFSET_BITS=64`
429.mcf: `-D_FILE_OFFSET_BITS=64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
458.sjeng: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
464.h264ref: `-D_FILE_OFFSET_BITS=64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-D_FILE_OFFSET_BITS=64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen:threadsafe`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-par-num-threads=1`(pass 1)
`-prof-use`(pass 2) `-auto-ilp32`

Continued on next page

## Peak Optimization Flags (Continued)

401.bzip2: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jan 26 15:12:23 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 January 2016.