



SPEC® CINT2006 Result

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Supermicro

SuperServer 5019S-M2
(X11SSZ-QF, Intel Core i7-6700T)

SPECint®2006 = 69.6

SPECint_base2006 = 67.7

CPU2006 license: 001176

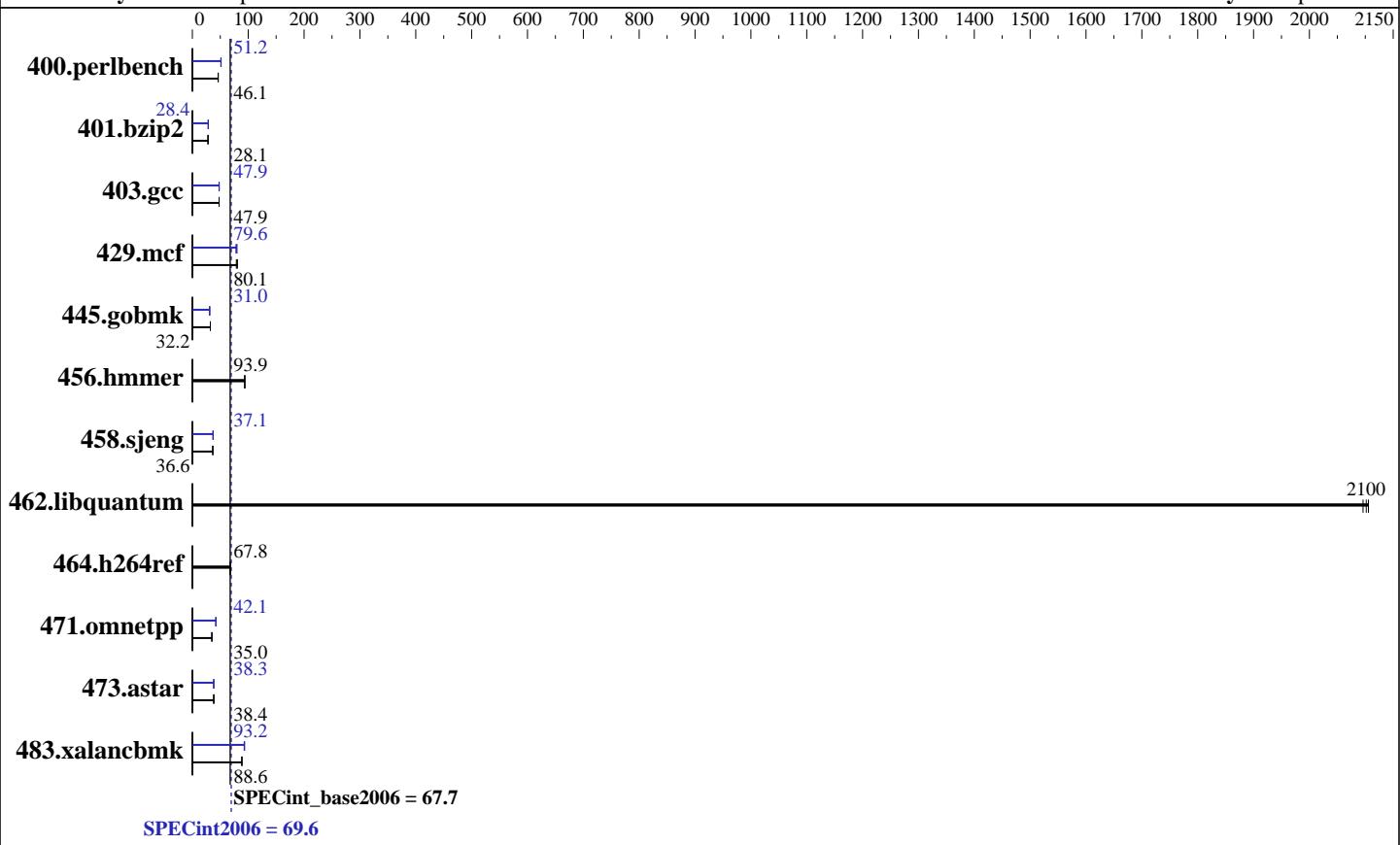
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2015



Hardware

CPU Name:	Intel Core i7-6700T
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (4 x 8 GB 2Rx8 PC4-2133P-U)
Disk Subsystem:	1 x 200 GB SATA III SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	210	46.4	212	46.0	212	46.1	191	51.0	191	51.2	190	51.5
401.bzip2	345	28.0	343	28.1	344	28.1	339	28.5	339	28.4	339	28.4
403.gcc	168	48.0	168	47.8	168	47.9	168	47.9	167	48.1	168	47.9
429.mcf	114	80.1	116	78.7	113	80.6	117	78.0	115	79.7	115	79.6
445.gobmk	326	32.2	325	32.2	326	32.2	339	31.0	339	31.0	339	31.0
456.hmmer	99.2	94.1	99.7	93.6	99.4	93.9	99.2	94.1	99.7	93.6	99.4	93.9
458.sjeng	331	36.6	331	36.6	331	36.6	326	37.1	326	37.1	326	37.1
462.libquantum	9.84	2110	9.86	2100	9.89	2100	9.84	2110	9.86	2100	9.89	2100
464.h264ref	327	67.7	325	68.0	326	67.8	327	67.7	325	68.0	326	67.8
471.omnetpp	178	35.0	179	35.0	179	34.9	148	42.2	149	42.1	148	42.1
473.astar	183	38.5	183	38.4	183	38.3	184	38.2	183	38.4	183	38.3
483.xalancbmk	77.9	88.6	78.0	88.5	77.6	88.9	74.0	93.2	73.8	93.5	74.4	92.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Hyper-threading = Disabled

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1
running on X11SSZ-01 Mon Dec 14 22:09:56 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Core(TM) i7-6700T CPU @ 2.80GHz

1 "physical id"s (chips)

4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4

siblings : 4

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Platform Notes (Continued)

```
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:      32763244 kB
HugePages_Total:      0
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.1 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.1"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

uname -a:
Linux X11SSZ-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 14 22:04

SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda2        xfs   183G   60G  123G  33% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0a 11/09/2015
Memory:
 4x Micron 16ATF1G64AZ-2G1A2 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP_NUM_THREADS = "4"

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General Notes (Continued)

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -D_FILE_OFFSET_BITS=64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch

-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div

-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc

-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel

-opt-prefetch -auto-p32

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Peak Optimization Flags (Continued)

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>



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