SPEC® CFP2006 Result

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ACTION S.A.

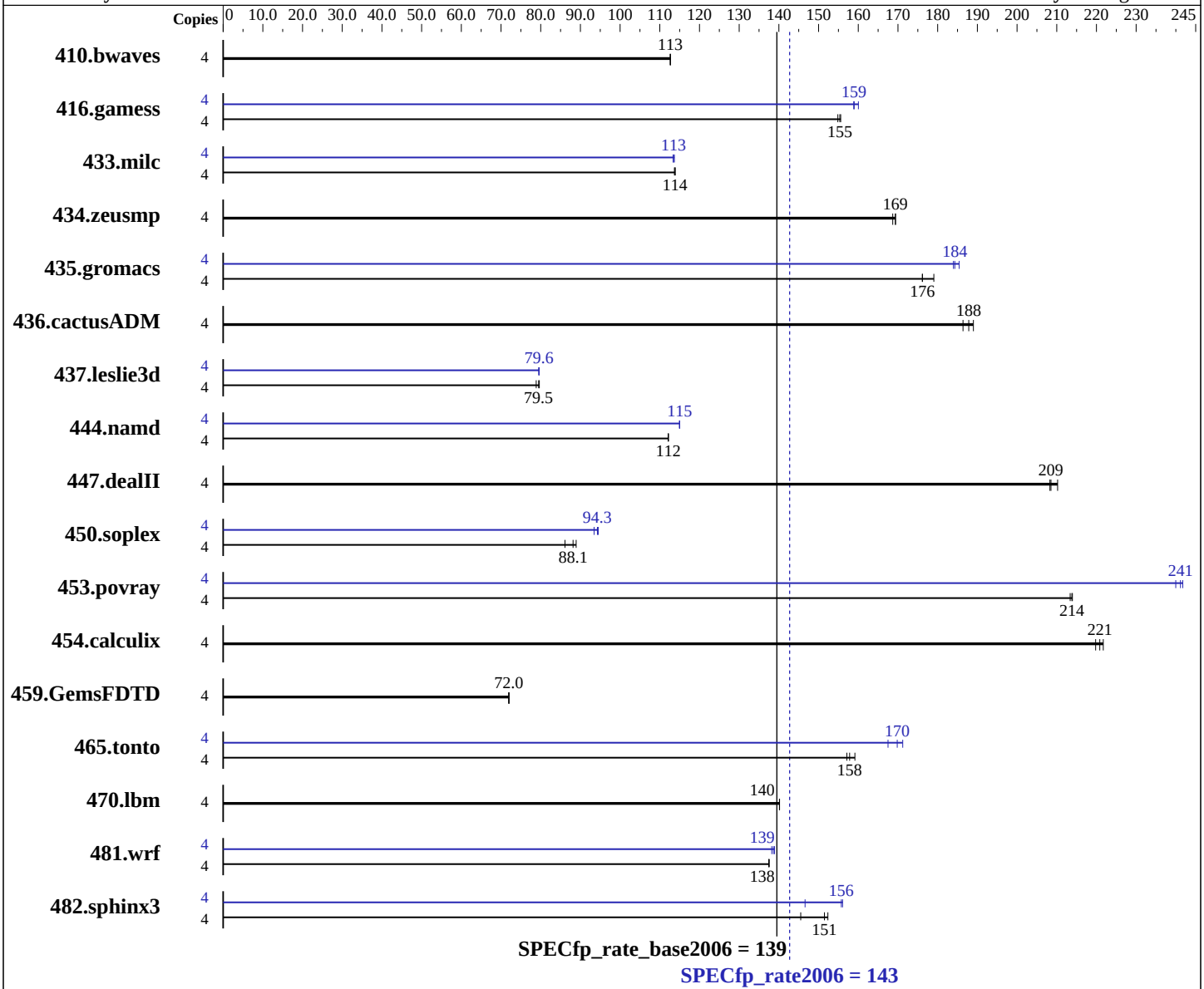
ACTINA SOLAR 110 S6 (Intel Xeon E3-1220 v3, 3.10 GHz)

SPECfp®_rate2006 = 143

SPECfp_rate_base2006 = 139

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Dec-2015
Hardware Availability: Sep-2014
Software Availability: Aug-2015



Hardware

CPU Name:	Intel Xeon E3-1220 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	3100
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.1 (Maipo) 3.10.0-229.11.1.el7.x86_64
Compiler:	C/C++: Version 16.0.0.047 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.047 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (4 x 8 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem: 1 x 240 GB SATA II SSD
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	**483**	**113**	483	113	482	113	4	**483**	**113**	483	113	482	113
416.gamess	4	503	156	506	155	**504**	**155**	4	**493**	**159**	489	160	493	159
433.milc	4	322	114	**323**	**114**	323	114	4	**324**	**113**	323	114	324	113
434.zeusmp	4	216	169	215	169	**215**	**169**	4	216	169	215	169	**215**	**169**
435.gromacs	4	162	176	160	179	**162**	**176**	4	154	185	155	184	**155**	**184**
436.cactusADM	4	**254**	**188**	253	189	256	186	4	**254**	**188**	253	189	256	186
437.leslie3d	4	477	78.9	**473**	**79.5**	472	79.7	4	472	79.6	**473**	**79.6**	473	79.5
444.namd	4	286	112	286	112	**286**	**112**	4	**279**	**115**	279	115	279	115
447.dealII	4	**219**	**209**	218	210	220	208	4	**219**	**209**	218	210	220	208
450.soplex	4	387	86.1	375	88.9	**378**	**88.1**	4	357	93.4	**354**	**94.3**	353	94.5
453.povray	4	99.5	214	**99.5**	**214**	99.7	213	4	88.7	240	88.0	242	**88.2**	**241**
454.calculix	4	149	222	**149**	**221**	150	220	4	149	222	**149**	**221**	150	220
459.GemsFDTD	4	**590**	**72.0**	589	72.0	590	71.9	4	**590**	**72.0**	589	72.0	590	71.9
465.tonto	4	250	157	247	159	**249**	**158**	4	230	171	235	167	**232**	**170**
470.lbm	4	**392**	**140**	392	140	392	140	4	**392**	**140**	392	140	392	140
481.wrf	4	325	138	**325**	**138**	325	137	4	323	138	**322**	**139**	322	139
482.sphinx3	4	512	152	536	146	**515**	**151**	4	**501**	**156**	532	147	500	156

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Bios Settings: Dafault
```

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Platform Notes (Continued)

```
BMC Setting
Fan Mode = Full Speed

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on SUT Mon Dec  7 16:53:51 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1220 v3 @ 3.10GHz
      1 "physical id"s (chips)
      4 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 4
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

From /proc/meminfo
   MemTotal:       32742600 kB
   HugePages_Total:       1
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   os-release.rpmnew:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.1 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.1"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

uname -a:
```

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Platform Notes (Continued)

```
      Linux SUT 3.10.0-229.11.1.el7.x86_64 #5 SMP Mon Sep 14 17:11:19 CEST 2015
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Dec 4 16:05

   SPEC is set to: /cpu2006.1.2
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda1      ext4  212G   41G  161G  21% /

   Additional information from dmidecode:
     BIOS American Megatrends Inc. 2.0 04/24/2014
     Memory:
      4x   8 GB
      4x 0793 W-MEM1600E38G 8 GB 1600 MHz 2 rank

   (End of data from sysinfo program)
   dmidecode does not properly detect memory modules
   4 modules of 8 GB were used to run the test (32 GB total)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Binaries compiled on a system with 2x Xeon E5-2650 v3 chips + 256 GB memory
 using RedHat EL 7.1
```

Base Compiler Invocation

C benchmarks:
```
     icc   -m64
```

C++ benchmarks:
```
     icpc  -m64
```

Fortran benchmarks:
```
     ifort -m64
```

Benchmarks using both Fortran and C:
```
     icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`

Continued on next page

Base Portability Flags (Continued)

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/lib/ia32

Fortran benchmarks:
ifort -m64

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevD-aug-2015-For-Supermicro-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevD-aug-2015-For-Supermicro-Platform.xml

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