



# SPEC<sup>®</sup> CFP2006 Result

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## Supermicro

Supermicro C7H170-M motherboard  
(C7H170-M , Intel Core i3-6100TE)

SPECfp<sup>®</sup>\_rate2006 = 115

SPECfp\_rate\_base2006 = 114

CPU2006 license: 001176

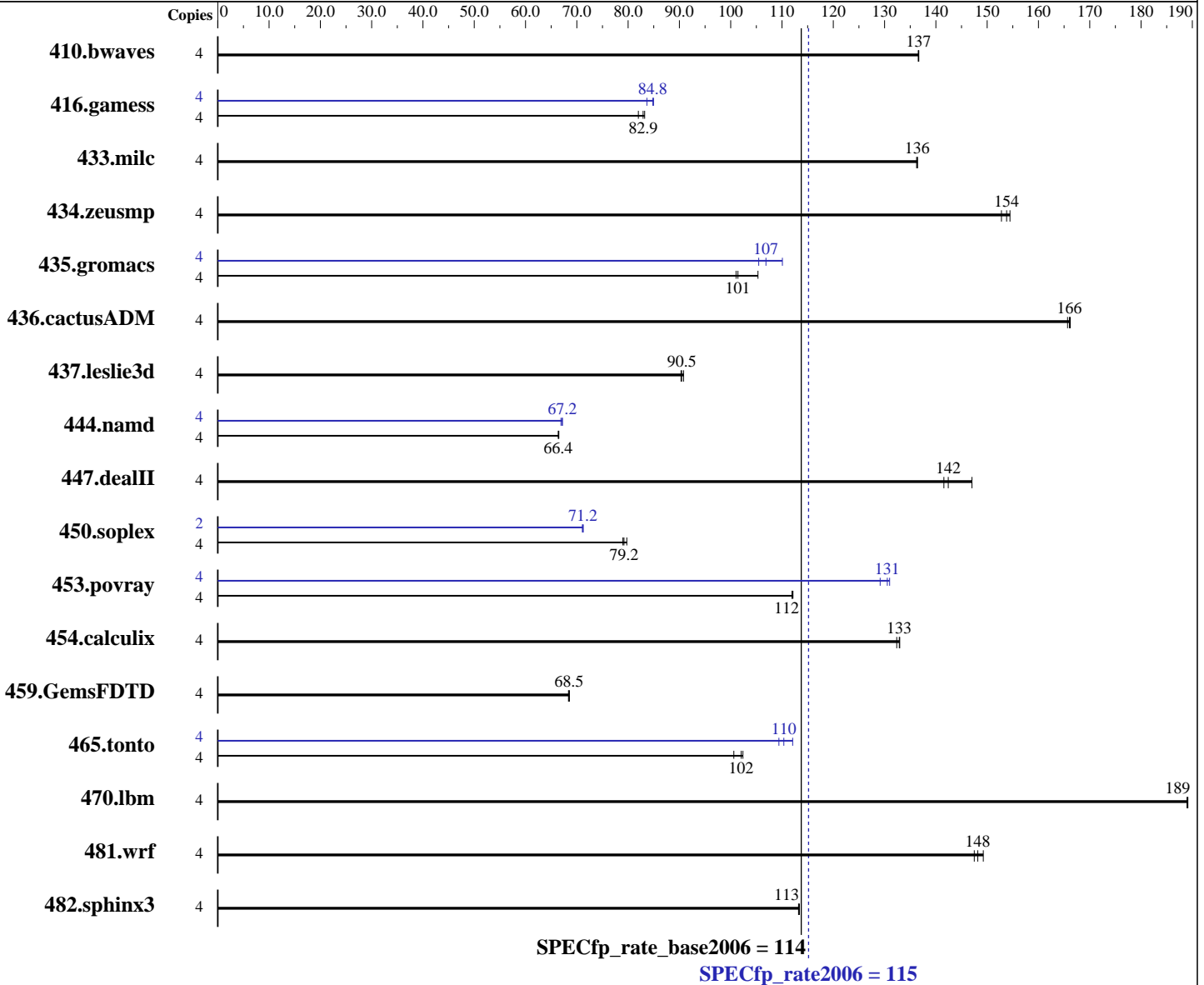
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2015

Hardware Availability: Oct-2015

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Core i3-6100TE  
 CPU Characteristics:  
 CPU MHz: 2700  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 4 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (4 x 4 GB 1Rx8 PC4-2666P-U, running at 2133 MHz)  
Disk Subsystem: 1 x 200 GB SATA III SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	398	137	398	137	<b>398</b>	<b>137</b>	4	398	137	398	137	<b>398</b>	<b>137</b>		
416.gamess	4	955	82.0	<b>944</b>	<b>82.9</b>	941	83.2	4	936	83.7	922	85.0	<b>924</b>	<b>84.8</b>		
433.milc	4	270	136	<b>269</b>	<b>136</b>	269	136	4	270	136	<b>269</b>	<b>136</b>	269	136		
434.zeusmp	4	<b>237</b>	<b>154</b>	238	153	236	154	4	<b>237</b>	<b>154</b>	238	153	236	154		
435.gromacs	4	283	101	<b>282</b>	<b>101</b>	271	105	4	271	105	259	110	<b>267</b>	<b>107</b>		
436.cactusADM	4	<b>288</b>	<b>166</b>	289	166	288	166	4	<b>288</b>	<b>166</b>	289	166	288	166		
437.leslie3d	4	414	90.8	416	90.3	<b>416</b>	<b>90.5</b>	4	414	90.8	416	90.3	<b>416</b>	<b>90.5</b>		
444.namd	4	<b>483</b>	<b>66.4</b>	482	66.5	483	66.4	4	478	67.2	<b>478</b>	<b>67.2</b>	479	66.9		
447.dealII	4	311	147	<b>321</b>	<b>142</b>	323	142	4	311	147	<b>321</b>	<b>142</b>	323	142		
450.soplex	4	<b>421</b>	<b>79.2</b>	422	79.0	418	79.7	2	234	71.2	<b>234</b>	<b>71.2</b>	235	71.1		
453.povray	4	190	112	<b>190</b>	<b>112</b>	190	112	4	165	129	<b>163</b>	<b>131</b>	162	131		
454.calculix	4	<b>248</b>	<b>133</b>	248	133	249	132	4	<b>248</b>	<b>133</b>	248	133	249	132		
459.GemsFDTD	4	<b>620</b>	<b>68.5</b>	619	68.6	621	68.4	4	<b>620</b>	<b>68.5</b>	619	68.6	621	68.4		
465.tonto	4	391	101	384	102	<b>386</b>	<b>102</b>	4	<b>357</b>	<b>110</b>	351	112	360	109		
470.lbm	4	291	189	<b>291</b>	<b>189</b>	291	189	4	291	189	<b>291</b>	<b>189</b>	291	189		
481.wrf	4	299	149	303	147	<b>302</b>	<b>148</b>	4	299	149	303	147	<b>302</b>	<b>148</b>		
482.sphinx3	4	<b>688</b>	<b>113</b>	689	113	687	113	4	<b>688</b>	<b>113</b>	689	113	687	113		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

As tested, the system used a Supermicro CSE-743TQ-865B-SQ chassis.

The chassis is configured with a PWS-865-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0103L4 rear fan and 2 FAN-0104L4 chassis fan.

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914

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**CPU2006 license:** 001176  
**Test sponsor:** Supermicro  
**Tested by:** Supermicro

**Test date:** Nov-2015  
**Hardware Availability:** Oct-2015  
**Software Availability:** Sep-2015

### Platform Notes (Continued)

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on C7H170-01 Sat Nov 21 12:01:14 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-6100TE CPU @ 2.70GHz
 1 "physical id"s (chips)
 4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 2
  siblings  : 4
  physical 0: cores 0 1
 cache size : 4096 KB
```

```
From /proc/meminfo
MemTotal:      16209832 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.1 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.1"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

```
uname -a:
Linux C7H170-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 21 02:22
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2       xfs       183G      36G  147G  20% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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### Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0c 11/11/2015

Memory:

4x 0420 F4-2666C15-4GRR 4 GB 1 rank 2133 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB  
memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

### Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main

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## Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
         -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
         -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
         -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
          -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
          -par-num-threads=1(pass 1) -prof-use(pass 2)
          -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
          -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
          -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
          -ansi-alias

```

### Fortran benchmarks:

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## Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Dec 16 10:39:50 2015 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 15 December 2015.