SPEC® CINT2006 Result
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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant ML30 Gen9
(3.60 GHz, Intel Xeon E3-1270 v5)

$SPECint®\_rate2006 = 267$

$SPECint\_rate\_base2006 = 256$

CPU2006 license:	3	**Test date:**	Nov-2015
Test sponsor:	HPE	**Hardware Availability:**	Dec-2015
Tested by:	HPE	**Software Availability:**	Aug-2015

SPECint_rate_base2006 = 256

SPECint_rate2006 = 267

Hardware

CPU Name:	Intel Xeon E3-1270 v5
CPU Characteristics:	Intel Turbo Boost Technology up to 4.00 GHz
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (4 x 8 GB 2Rx8 PC4-2133P-U)
Disk Subsystem:	1 x 1 TB SATA, RAID 0
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12
	Kernel 3.12.43-52.6-default
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	btrfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

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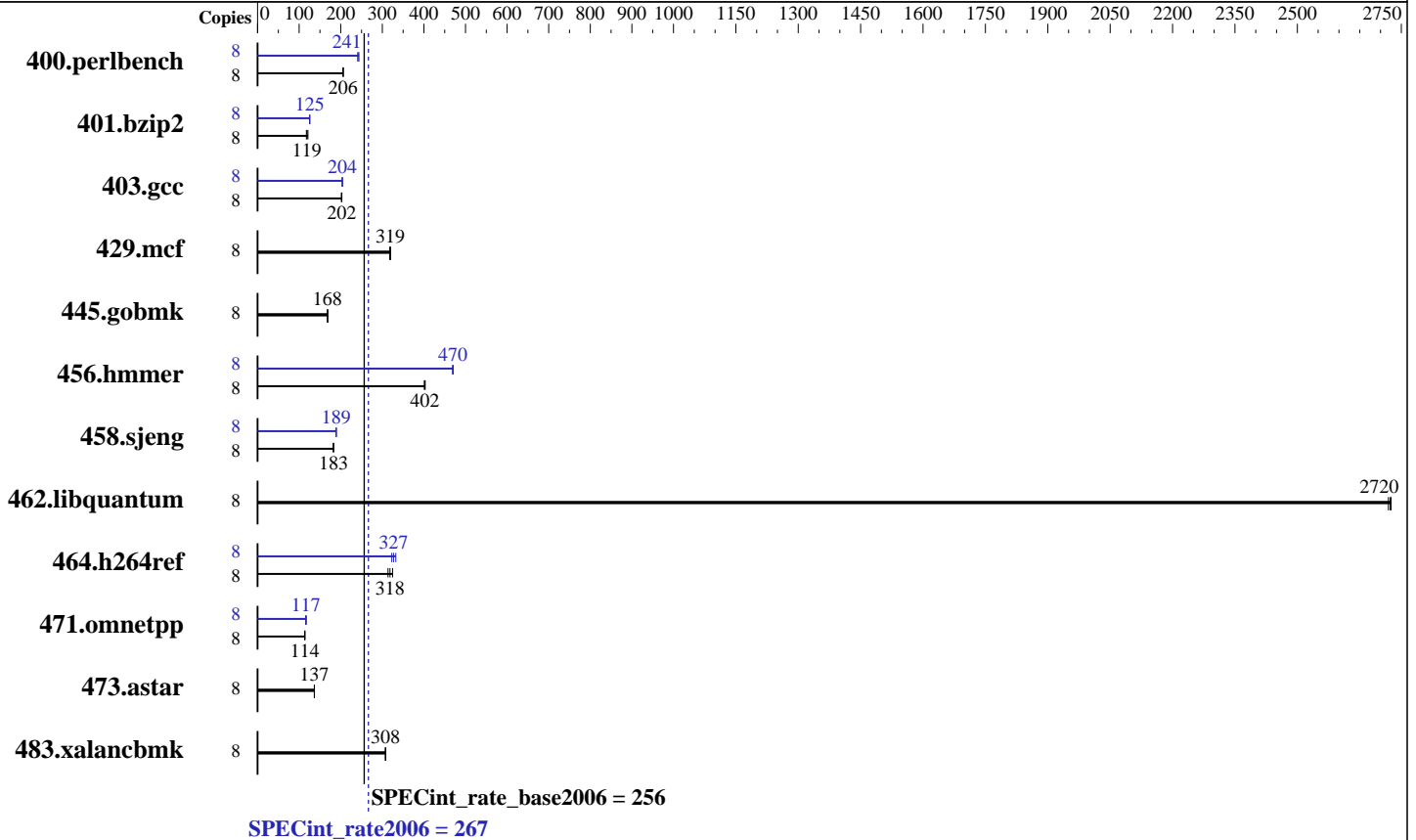
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	380	206	379	206	**380**	**206**	8	**324**	**241**	324	241	321	244
401.bzip2	8	**649**	**119**	658	117	637	121	8	615	126	618	125	**615**	**125**
403.gcc	8	**319**	**202**	318	203	319	202	8	317	203	**316**	**204**	316	204
429.mcf	8	**229**	**319**	230	318	228	320	8	**229**	**319**	230	318	228	320
445.gobmk	8	**498**	**168**	498	168	499	168	8	**498**	**168**	498	168	499	168
456.hmmer	8	186	401	185	403	**186**	**402**	8	159	469	159	470	**159**	**470**
458.sjeng	8	**530**	**183**	530	183	531	182	8	513	189	511	189	**512**	**189**
462.libquantum	8	**60.9**	**2720**	60.8	2730	61.0	2720	8	**60.9**	**2720**	60.8	2730	61.0	2720
464.h264ref	8	564	314	545	325	**556**	**318**	8	533	332	**541**	**327**	549	323
471.omnetpp	8	439	114	441	113	**440**	**114**	8	432	116	427	117	**429**	**117**
473.astar	8	**411**	**137**	412	136	411	137	8	**411**	**137**	412	136	411	137
483.xalancbmk	8	179	308	**179**	**308**	179	308	8	179	308	**179**	**308**	179	308

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
  HP Power Profile set to Custom
  HP Power Regulator to HP Static High Performance Mode
  Minimum Processor Idle Power Core C-State set to C6 State
  Minimum Processor Idle Power Package C-State set to No Package State
  Energy/Performance Bias set to Maximum Performance
  Collaborative Power Control set to Disabled
  Thermal Configuration set to Maximum Cooling
  Processor Power and Utilization Monitoring set to Disabled
  Memory Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-1e4g Fri Nov 6 15:46:42 2015

Continued on next page

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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1270 v5 @ 3.60GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

From /proc/meminfo
   MemTotal:       32807460 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 0
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12"
      VERSION_ID="12"
      PRETTY_NAME="SUSE Linux Enterprise Server 12"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
   Linux linux-1e4g 3.12.43-52.6-default #1 SMP Wed May 20 12:44:39 UTC 2015
   (fc0ceac) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 6 15:46

SPEC is set to: /cpu2006
   Filesystem      Type   Size  Used Avail Use% Mounted on
   /dev/sda3       btrfs  928G   13G  914G   2% /
Additional information from dmidecode:
```

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Platform Notes (Continued)

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

      BIOS HP U23 09/24/2015
      Memory:
      4x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2133 MHz

    (End of data from sysinfo program)

General Notes

    Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

     Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
     memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:
      icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
      icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

        400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
          401.bzip2: -D_FILE_OFFSET_BITS=64
           403.gcc: -D_FILE_OFFSET_BITS=64
           429.mcf: -D_FILE_OFFSET_BITS=64
         445.gobmk: -D_FILE_OFFSET_BITS=64
         456.hmmer: -D_FILE_OFFSET_BITS=64
          458.sjeng: -D_FILE_OFFSET_BITS=64
     462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
        464.h264ref: -D_FILE_OFFSET_BITS=64
        471.omnetpp: -D_FILE_OFFSET_BITS=64
          473.astar: -D_FILE_OFFSET_BITS=64
      483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Peak Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
  401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
    403.gcc: -D_FILE_OFFSET_BITS=64
    429.mcf: -D_FILE_OFFSET_BITS=64
  445.gobmk: -D_FILE_OFFSET_BITS=64
  456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
   458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
 464.h264ref: -D_FILE_OFFSET_BITS=64
  471.omnetpp: -D_FILE_OFFSET_BITS=64
    473.astar: -D_FILE_OFFSET_BITS=64
```

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Peak Portability Flags (Continued)

    483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`
             `-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)`
             `-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32`

      401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`
             `-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)`
             `-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch`
             `-auto-ilp32 -ansi-alias`

       403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

       429.mcf: `basepeak = yes`

     445.gobmk: `basepeak = yes`

     456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

      458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`
             `-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)`
             `-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4`
             `-auto-ilp32`

    462.libquantum: `basepeak = yes`

     464.h264ref: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`
             `-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)`
             `-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2`
             `-ansi-alias`

C++ benchmarks:

    471.omnetpp: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`
             `-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)`
             `-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias`
             `-opt-ra-region-strategy=block -Wl,-z,muldefs`
             `-L/sh -lsmartheap`

      473.astar: `basepeak = yes`

    483.xalancbmk: `basepeak = yes`

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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