



# SPEC® CFP2006 Result

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## Supermicro

SuperServer 5019S-M2  
(X11SSZ-QF, Intel Core i5-6400T)

**SPECfp®\_rate2006 = 154**

**SPECfp\_rate\_base2006 = 151**

CPU2006 license: 001176

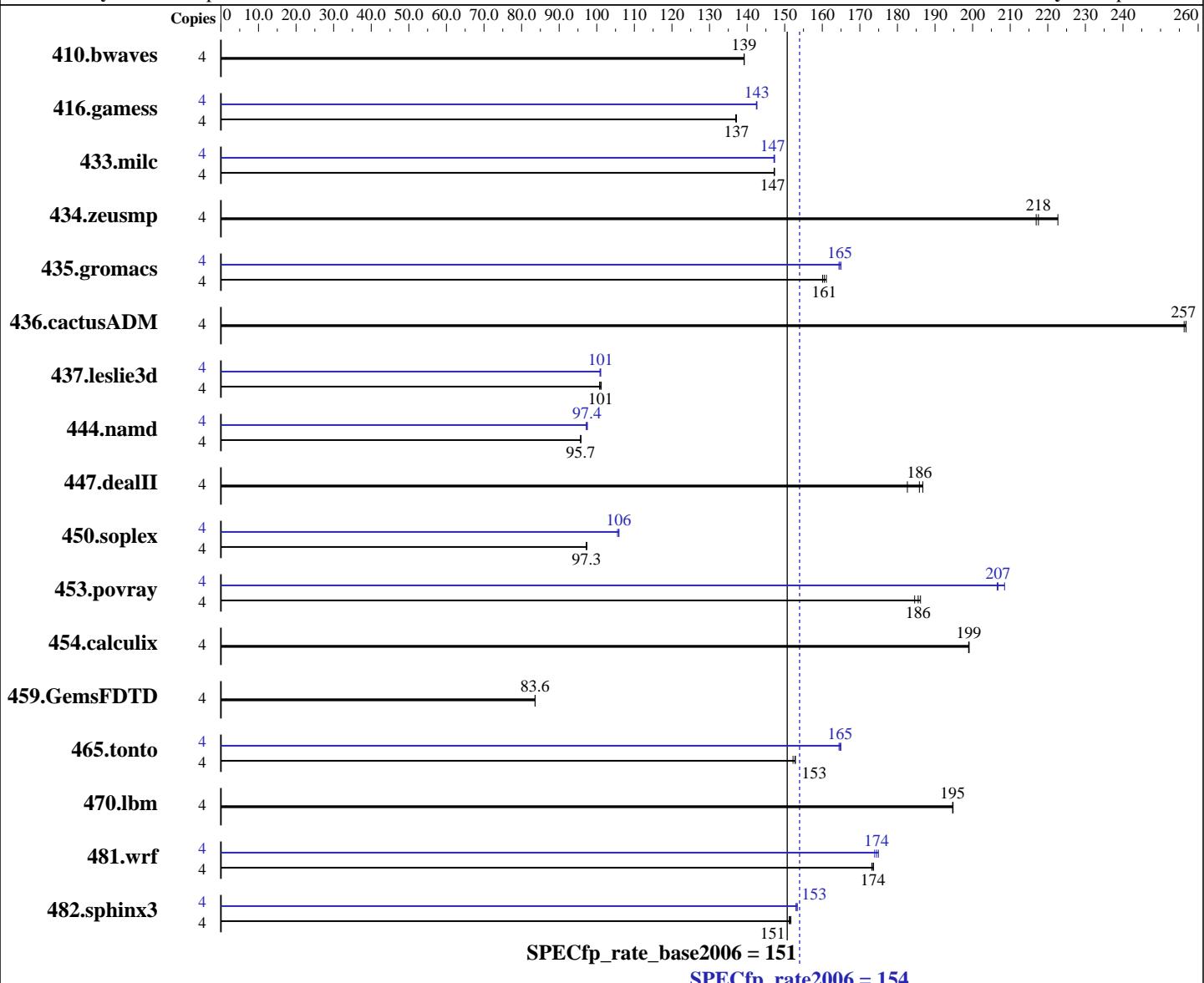
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Core i5-6400T  
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
CPU MHz: 2200  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

### Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86\_64  
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
Auto Parallel: No  
File System: xfs  
System State: Run level 3 (multi-user)

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L3 Cache: 6 MB I+D on chip per chip  
Other Cache: None  
Memory: 32 GB (4 x 8 GB 2Rx8 PC4-2133P-U)  
Disk Subsystem: 1 x 200 GB SATA III SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	391	139	<b>390</b>	<b>139</b>	390	139	4	391	139	<b>390</b>	<b>139</b>	390	139
416.gamess	4	572	137	<b>571</b>	<b>137</b>	571	137	4	<b>549</b>	<b>143</b>	550	142	549	143
433.milc	4	249	147	<b>249</b>	<b>147</b>	249	147	4	249	147	250	147	<b>249</b>	<b>147</b>
434.zeusmp	4	<b>167</b>	<b>218</b>	163	223	168	217	4	<b>167</b>	<b>218</b>	163	223	168	217
435.gromacs	4	177	161	178	160	<b>178</b>	<b>161</b>	4	<b>173</b>	<b>165</b>	173	165	174	164
436.cactusADM	4	186	257	187	256	<b>186</b>	<b>257</b>	4	186	257	187	256	<b>186</b>	<b>257</b>
437.leslie3d	4	372	101	<b>372</b>	<b>101</b>	373	101	4	372	101	<b>372</b>	<b>101</b>	373	101
444.namd	4	<b>335</b>	<b>95.7</b>	335	95.7	335	95.8	4	330	97.2	<b>329</b>	<b>97.4</b>	329	97.5
447.dealII	4	251	183	<b>246</b>	<b>186</b>	245	187	4	251	183	<b>246</b>	<b>186</b>	245	187
450.soplex	4	<b>343</b>	<b>97.3</b>	343	97.4	343	97.2	4	<b>315</b>	<b>106</b>	316	106	315	106
453.povray	4	114	186	<b>115</b>	<b>186</b>	115	185	4	102	208	103	207	<b>103</b>	<b>207</b>
454.calculix	4	166	199	166	199	<b>166</b>	<b>199</b>	4	166	199	166	199	<b>166</b>	<b>199</b>
459.GemsFDTD	4	<b>508</b>	<b>83.6</b>	508	83.6	508	83.6	4	<b>508</b>	<b>83.6</b>	508	83.6	508	83.6
465.tonto	4	258	153	259	152	<b>258</b>	<b>153</b>	4	<b>239</b>	<b>165</b>	239	164	239	165
470.lbm	4	282	195	282	195	<b>282</b>	<b>195</b>	4	282	195	282	195	<b>282</b>	<b>195</b>
481.wrf	4	<b>257</b>	<b>174</b>	258	173	257	174	4	<b>257</b>	<b>174</b>	<b>256</b>	<b>174</b>	255	175
482.sphinx3	4	514	152	<b>515</b>	<b>151</b>	516	151	4	<b>509</b>	<b>153</b>	508	153	510	153

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1  
running on X11SSZ-01 Sat Nov 14 16:52:31 2015

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Core(TM) i5-6400T CPU @ 2.20GHz
        1 "physical id"s (chips)
        4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 4
    siblings : 4
    physical 0: cores 0 1 2 3
    cache size : 6144 KB
```

```
From /proc/meminfo
MemTotal:      32888188 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.1 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.1"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

```
uname -a:
Linux X11SSZ-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 13 23:05
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        xfs   183G   25G  159G  14% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS American Megatrends Inc. 1.0a 10/30/2015

Memory:

4x Micron 16ATF1G64AZ-2G1A2 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64

481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

450.soplex: icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbmb: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -auto-ilp32

```

470.lbmb: basepeak = yes

```

482.sphinx3: -xCORE-AVX2 -prof-gen(pass 1) -ipo -O3 -no-prec-div
              -prof-use(pass 2) -unroll12

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
            -ansi-alias

```

Fortran benchmarks:

410.bwaves: basepeak = yes

```

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
            -inline-level=0 -scalar-rep-

```

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## Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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