



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

Supermicro C7Z170-SQ motherboard
(C7Z170-SQ , Intel Core i5-6500T)

SPECint®_rate2006 = 170

SPECint_rate_base2006 = 165

CPU2006 license: 001176

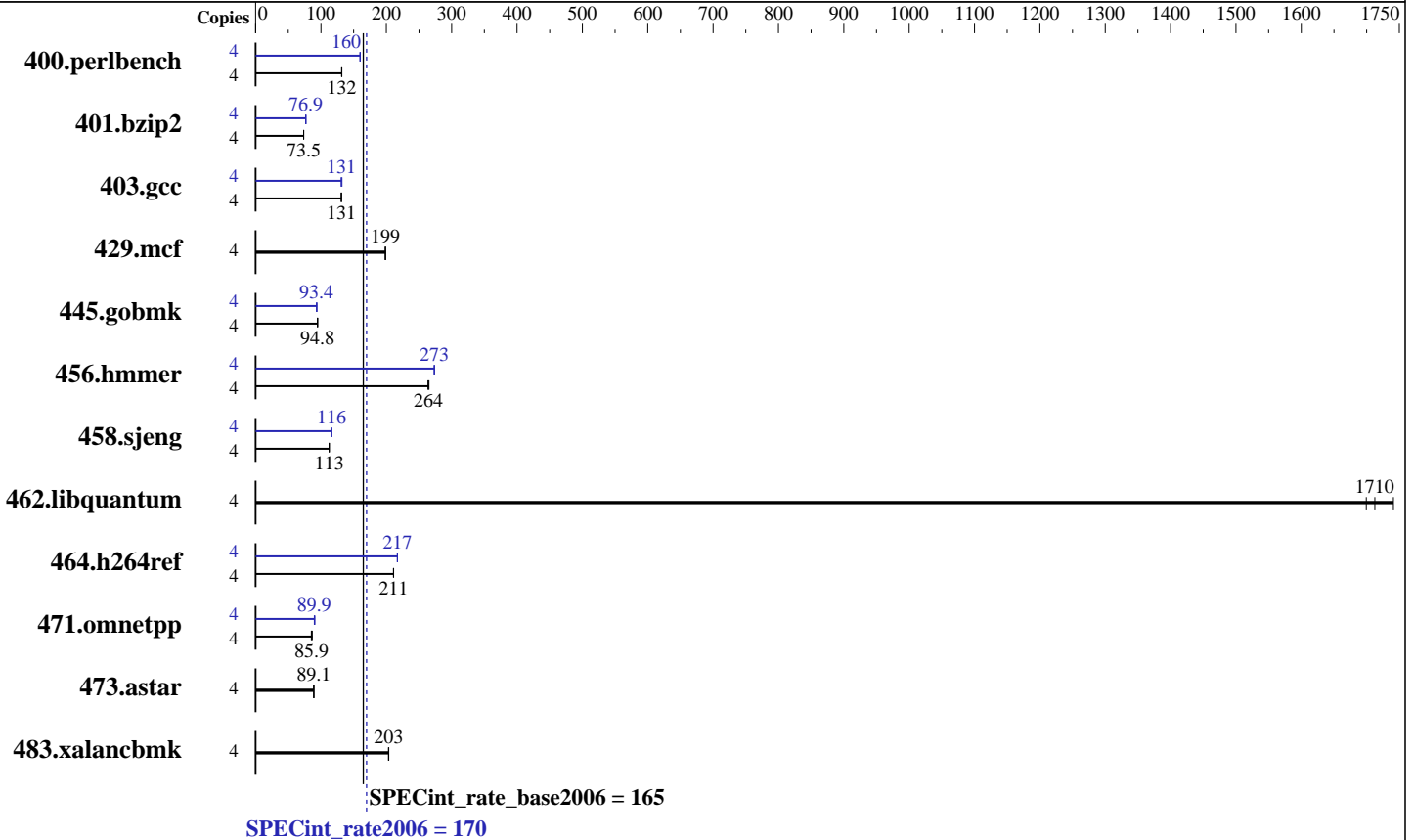
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2014



Hardware

CPU Name: Intel Core i5-6500T
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 6 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 1Rx8 PC4-2133P-U)
 Disk Subsystem: 1 x 400 GB SATA III SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.0, Kernel 3.10.0-123.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

Supermicro C7Z170-SQ motherboard
(C7Z170-SQ , Intel Core i5-6500T)

SPECint_rate2006 = 170

SPECint_rate_base2006 = 165

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2014

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	296	132	296	132	297	132	4	244	160	244	160	244	160
401.bzip2	4	525	73.5	524	73.7	525	73.5	4	502	77.0	502	76.9	503	76.7
403.gcc	4	245	131	246	131	245	131	4	246	131	244	132	246	131
429.mcf	4	184	198	184	199	184	199	4	184	198	184	199	184	199
445.gobmk	4	443	94.8	443	94.7	442	94.9	4	450	93.3	447	93.8	449	93.4
456.hammer	4	141	264	141	265	141	264	4	136	273	136	274	137	273
458.sjeng	4	430	113	430	113	430	113	4	417	116	416	116	416	116
462.libquantum	4	47.6	1740	48.4	1710	48.8	1700	4	47.6	1740	48.4	1710	48.8	1700
464.h264ref	4	419	211	419	211	419	211	4	408	217	408	217	408	217
471.omnetpp	4	288	86.7	291	85.9	293	85.3	4	278	89.9	278	89.9	275	90.8
473.astar	4	315	89.1	315	89.2	316	88.9	4	315	89.1	315	89.2	316	88.9
483.xalancbmk	4	136	203	135	204	136	203	4	136	203	135	204	136	203

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

As tested, the system used a Supermicro CSE-732G-903B chassis.
The chassis is configured with a PWS-903-PQ power supply, 1 SNK-P0051AP4 heatsink, as well as 1 FAN-0124L4 rear cooling fan.
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Mon Nov 9 05:58:12 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Core(TM) i5-6500T CPU @ 2.50GHz
1 "physical id"s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

Supermicro C7Z170-SQ motherboard
(C7Z170-SQ , Intel Core i5-6500T)

SPECint_rate2006 = 170

SPECint_rate_base2006 = 165

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2014

Platform Notes (Continued)

```
cpu cores : 4
siblings  : 4
physical 0: cores 0 1 2 3
cache size : 6144 KB
```

From /proc/meminfo

```
MemTotal:      16170460 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

From /etc/*release* /etc/*version*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

uname -a:

```
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 9 05:54

SPEC is set to: /home/cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs      318G  317G  1.2G 100% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. T20151015150001 10/15/2015

Memory:

4x Micron 8ATF51264AZ-2G1A2 4 GB 1 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

Supermicro C7Z170-SQ motherboard
(C7Z170-SQ , Intel Core i5-6500T)

SPECint_rate2006 = 170

SPECint_rate_base2006 = 165

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2014

General Notes (Continued)

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

400.perlbench: icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

Supermicro C7Z170-SQ motherboard
(C7Z170-SQ , Intel Core i5-6500T)

SPECint_rate2006 = 170

SPECint_rate_base2006 = 165

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2014

Peak Compiler Invocation (Continued)

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

Supermicro C7Z170-SQ motherboard
(C7Z170-SQ , Intel Core i5-6500T)

SPECint_rate2006 = 170

SPECint_rate_base2006 = 165

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2014

Peak Optimization Flags (Continued)

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Dec 1 17:41:21 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 1 December 2015.