



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint®_rate2006 = 2110

Huawei RH5885 V3 (Intel Xeon E7-8867 v3)

SPECint_rate_base2006 = 2030

CPU2006 license: 3175

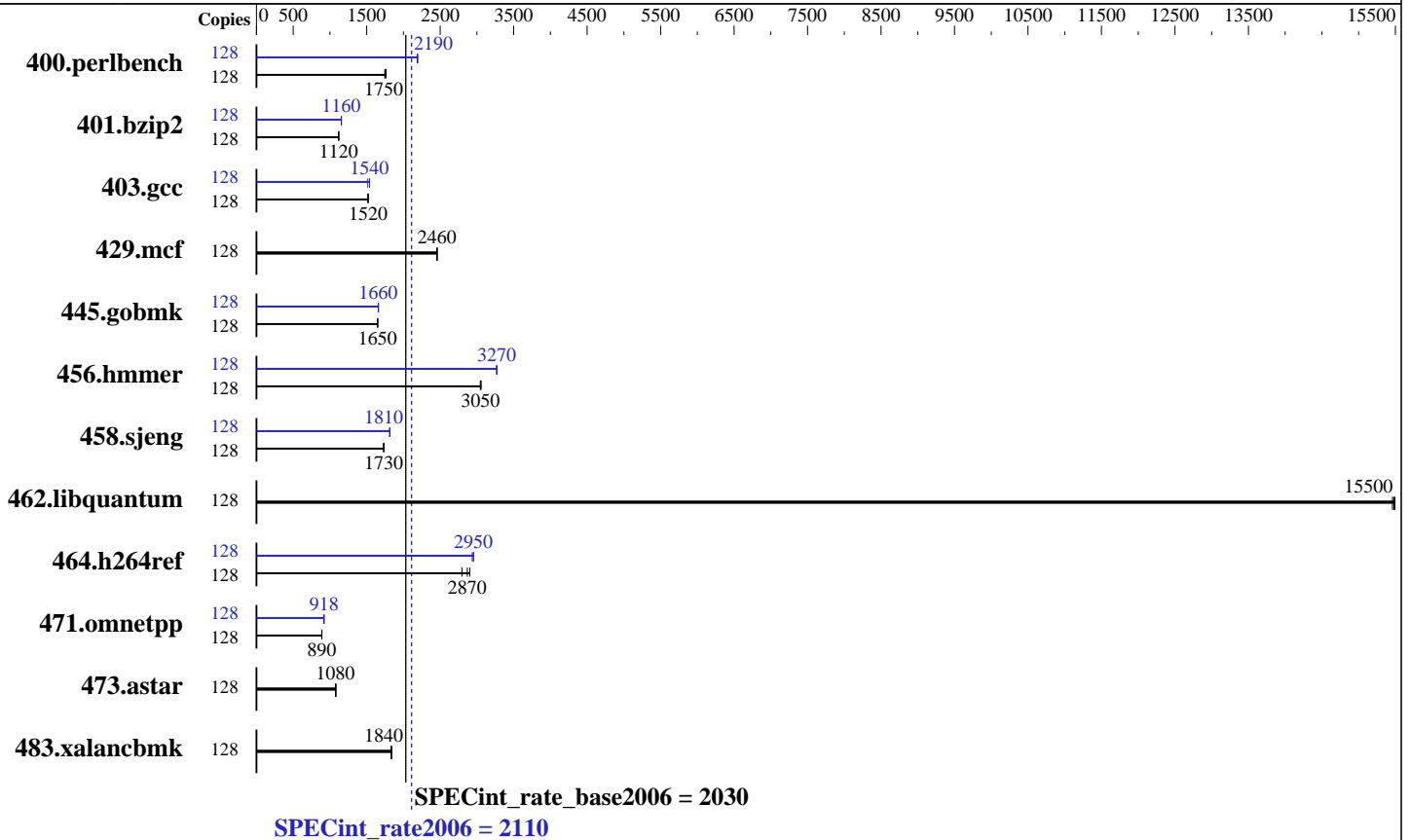
Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2015

Hardware Availability: May-2015

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E7-8867 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 64 cores, 4 chips, 16 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 45 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
 Disk Subsystem: 2 x 300 GB SAS, 10K RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) 3.12.28-4-default
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 2110

Huawei RH5885 V3 (Intel Xeon E7-8867 v3)

SPECint_rate_base2006 = 2030

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Nov-2015
Hardware Availability: May-2015
Software Availability: Oct-2014

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	128	<u>715</u>	<u>1750</u>	709	1760	715	1750	128	569	2200	<u>572</u>	<u>2190</u>	572	2190
401.bzip2	128	1101	1120	1105	1120	<u>1105</u>	<u>1120</u>	128	1070	1150	1068	1160	<u>1069</u>	<u>1160</u>
403.gcc	128	677	1520	680	1520	<u>678</u>	<u>1520</u>	128	<u>670</u>	<u>1540</u>	669	1540	680	1520
429.mcf	128	<u>475</u>	<u>2460</u>	475	2460	475	2460	128	<u>475</u>	<u>2460</u>	475	2460	475	2460
445.gobmk	128	814	1650	<u>814</u>	<u>1650</u>	815	1650	128	808	1660	811	1660	<u>809</u>	<u>1660</u>
456.hammer	128	<u>391</u>	<u>3050</u>	392	3050	391	3060	128	365	3270	<u>365</u>	<u>3270</u>	365	3270
458.sjeng	128	<u>894</u>	<u>1730</u>	894	1730	895	1730	128	854	1810	<u>854</u>	<u>1810</u>	853	1820
462.libquantum	128	171	15500	172	15500	<u>171</u>	<u>15500</u>	128	171	15500	172	15500	<u>171</u>	<u>15500</u>
464.h264ref	128	976	2900	1013	2800	<u>987</u>	<u>2870</u>	128	<u>960</u>	<u>2950</u>	965	2940	959	2950
471.omnetpp	128	<u>899</u>	<u>890</u>	898	891	900	889	128	875	915	868	922	<u>871</u>	<u>918</u>
473.astar	128	<u>835</u>	<u>1080</u>	829	1080	835	1080	128	<u>835</u>	<u>1080</u>	829	1080	835	1080
483.xalancbmk	128	481	1830	<u>481</u>	<u>1840</u>	480	1840	128	481	1830	<u>481</u>	<u>1840</u>	480	1840

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Set Memory Power Saving to disabled
Sysinfo program /zsn/spec1/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on RH5885V3 Sun Nov 1 18:08:19 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8867 v3 @ 2.50GHz
4 "physical id"s (chips)
128 "processors"

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 2110

Huawei RH5885 V3 (Intel Xeon E7-8867 v3)

SPECint_rate_base2006 = 2030

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2015

Hardware Availability: May-2015

Software Availability: Oct-2014

Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```

cpu cores : 16
siblings  : 32
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
cache size : 46080 KB

```

From /proc/meminfo

```

MemTotal:      529106988 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

From /etc/*release* /etc/*version*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
# release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"

```

uname -a:

```

Linux RH5885V3 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 1 18:07

SPEC is set to: /zsn/spec1

```

Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sdb1        ext4     823G     174G   607G   23% /zsn

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLISQ954 09/19/2015

Memory:

```

32x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz

```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 2110

Huawei RH5885 V3 (Intel Xeon E7-8867 v3)

SPECint_rate_base2006 = 2030

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Nov-2015
Hardware Availability: May-2015
Software Availability: Oct-2014

Platform Notes (Continued)

16x NO DIMM NO DIMM

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have two lines reading as:

32x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz

16x NO DIMM NO DIMM

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/zsn/spec1/libs/32:/zsn/spec1/libs/64:/zsn/spec1/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 2110

Huawei RH5885 V3 (Intel Xeon E7-8867 v3)

SPECint_rate_base2006 = 2030

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Nov-2015
Hardware Availability: May-2015
Software Availability: Oct-2014

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 2110

Huawei RH5885 V3 (Intel Xeon E7-8867 v3)

SPECint_rate_base2006 = 2030

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Nov-2015
Hardware Availability: May-2015
Software Availability: Oct-2014

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.xml>



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 2110

Huawei RH5885 V3 (Intel Xeon E7-8867 v3)

SPECint_rate_base2006 = 2030

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2015

Hardware Availability: May-2015

Software Availability: Oct-2014

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Dec 1 17:41:03 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 December 2015.