



SPEC® CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECfp®_rate2006 = 389

Huawei CH226 V3 (Intel Xeon E5-2623 v3)

SPECfp_rate_base2006 = 380

CPU2006 license: 3175

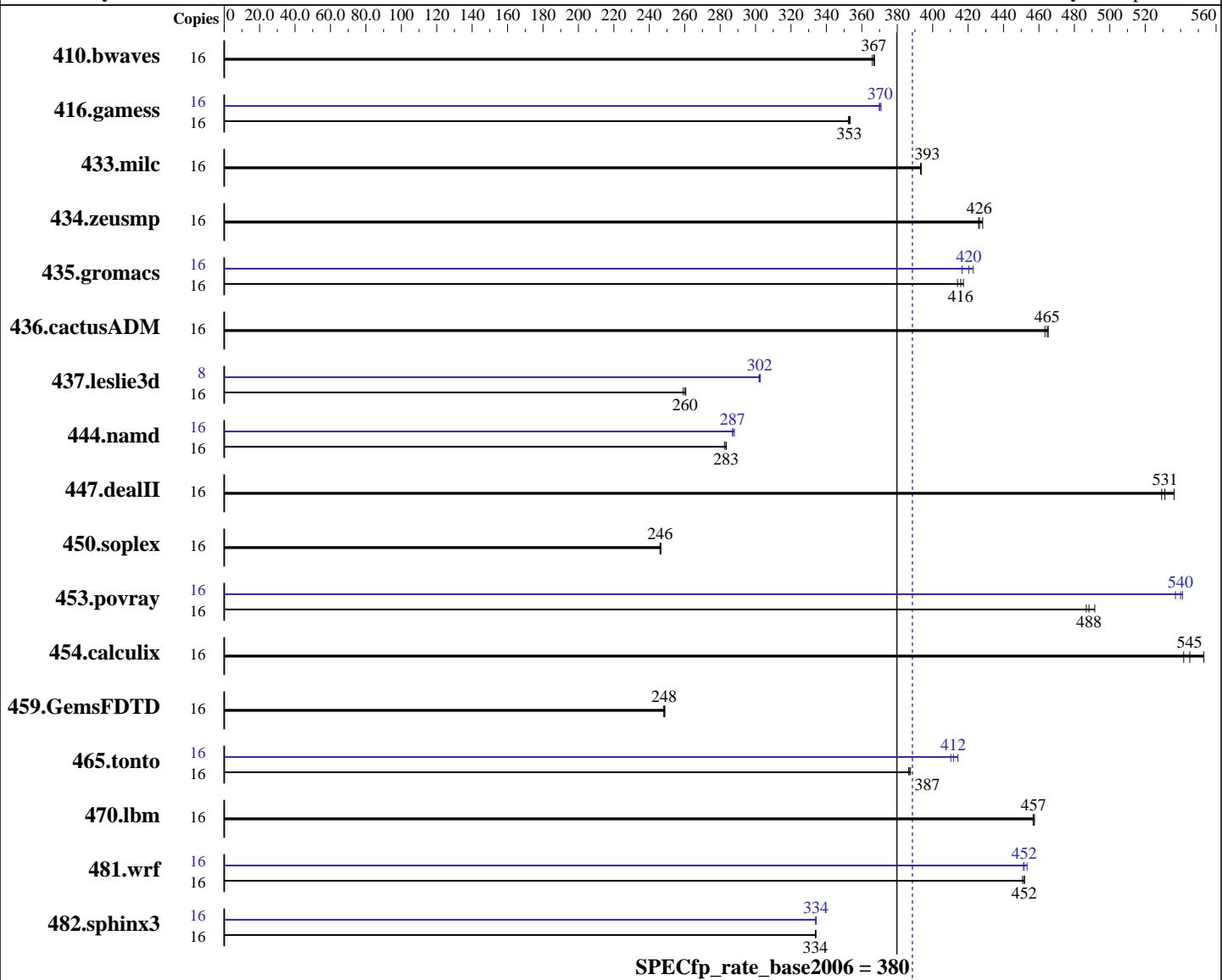
Test date: Aug-2015

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Sep-2014



SPECfp_rate_base2006 = 380

SPECfp_rate2006 = 389

Hardware

CPU Name: Intel Xeon E5-2623 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 Compiler: 3.10.0-123.el7.x86_64
 C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 389

Huawei CH226 V3 (Intel Xeon E5-2623 v3)

SPECfp_rate_base2006 = 380

CPU2006 license: 3175

Test date: Aug-2015

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Sep-2014

L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	593	367	592	367	594	366	16	593	367	592	367	594	366		
416.gamess	16	889	352	888	353	886	353	16	845	371	847	370	846	370		
433.milc	16	373	393	374	393	373	393	16	373	393	374	393	373	393		
434.zeusmp	16	342	426	341	426	340	428	16	342	426	341	426	340	428		
435.gromacs	16	274	417	276	414	275	416	16	270	423	272	420	274	417		
436.cactusADM	16	411	465	411	465	413	463	16	411	465	411	465	413	463		
437.leslie3d	16	578	260	577	261	580	259	8	249	302	249	302	248	303		
444.namd	16	453	283	453	283	454	282	16	447	287	445	288	447	287		
447.dealII	16	345	531	346	529	341	536	16	345	531	346	529	341	536		
450.soplex	16	542	246	542	246	541	247	16	542	246	542	246	541	247		
453.povray	16	173	492	175	487	174	488	16	157	541	158	537	158	540		
454.calculix	16	242	545	239	553	244	542	16	242	545	239	553	244	542		
459.GemsFDTD	16	682	249	683	248	684	248	16	682	249	683	248	684	248		
465.tonto	16	406	387	407	387	407	386	16	380	414	382	412	384	410		
470.lbm	16	481	457	481	457	481	457	16	481	457	481	457	481	457		
481.wrf	16	396	452	396	452	396	451	16	396	451	394	453	396	452		
482.sphinx3	16	934	334	934	334	933	334	16	934	334	933	334	933	334		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Performance

Set Snoop Mode to ES mode

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 389

Huawei CH226 V3 (Intel Xeon E5-2623 v3)

SPECfp_rate_base2006 = 380

CPU2006 license: 3175

Test date: Aug-2015

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Sep-2014

Platform Notes (Continued)

```
Set Patrol Scrub to Disable
Sysinfo program /spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date::: 2014-06-25 #$
running on localhost.localdomain Wed Aug 26 15:22:28 2015
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2623 v3 @ 3.00GHz
        2 "physical id"s (chips)
        16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 4
        siblings : 8
        physical 0: cores 0 1 2 3
        physical 1: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:      263579372 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.0 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.0"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Aug 26 06:00

```
SPEC is set to: /spec
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sdal      ext4   443G   22G   399G   6% /
Additional information from dmidecode:
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

Huawei CH226 V3 (Intel Xeon E5-2623 v3)

SPECfp_rate2006 = 389

SPECfp_rate_base2006 = 380

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 1.36 04/09/2015

Memory:

8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz, configured at 1867 MHz
8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1867 MHz
8x NO DIMM NO DIMM 3 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
`LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"`

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

`echo always > /sys/kernel/mm/transparent_hugepage/enabled`

Filesystem page cache cleared with:

`echo 1> /proc/sys/vm/drop_caches`

runspec command invoked through numactl i.e.:

`numactl --interleave=all runspec <etc>`

Base Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`

416.gamess: `-DSPEC_CPU_LP64`

433.milc: `-DSPEC_CPU_LP64`

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

Huawei CH226 V3 (Intel Xeon E5-2623 v3)

SPECfp_rate2006 = 389

CPU2006 license: 3175

Test date: Aug-2015

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Sep-2014

Base Portability Flags (Continued)

```
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

Huawei CH226 V3 (Intel Xeon E5-2623 v3)

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

SPECfp_rate2006 = 389

SPECfp_rate_base2006 = 380

Test date: Aug-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll12

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto -inline-calloc -opt-malloc-options=3

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

Huawei CH226 V3 (Intel Xeon E5-2623 v3)

SPECfp_rate2006 = 389

SPECfp_rate_base2006 = 380

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2)
              -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
              -opt-prefetch -auto-ilp32
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: basepeak = yes
```

```
481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Sep 23 11:04:15 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 22 September 2015.