SPEC® CINT2006 Result

ACTION S.A.

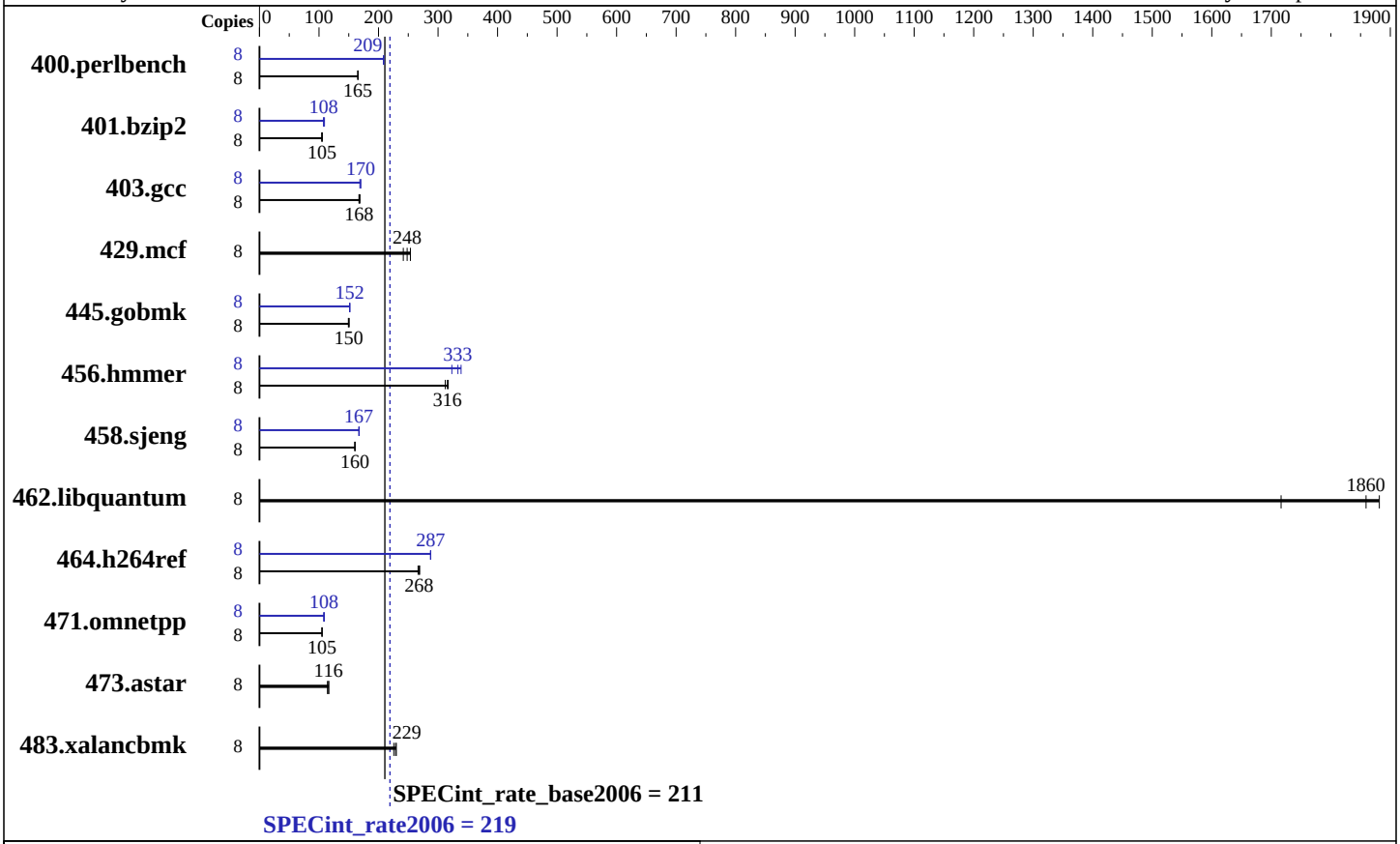
ACTINA SOLAR 110 X6 (Intel Xeon E3-1241 v3, 3.50 GHz)

SPECint®_rate2006 = 219

SPECint_rate_base2006 = 211

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Jul-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014



Hardware

CPU Name:	Intel Xeon E3-1241 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (4 x 8 GB 2Rx8 PC3-12800U-13)
Disk Subsystem:	1 x 240 GB SATA II SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.6 (Santiago) 2.6.32-504.8.1.el6.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**473**	**165**	473	165	472	166	8	375	209	375	208	**375**	**209**
401.bzip2	8	**735**	**105**	731	106	738	105	8	**716**	**108**	707	109	717	108
403.gcc	8	381	169	385	167	**384**	**168**	8	379	170	382	169	**379**	**170**
429.mcf	8	302	242	288	254	**294**	**248**	8	302	242	288	254	**294**	**248**
445.gobmk	8	**559**	**150**	562	149	557	151	8	554	152	**554**	**152**	553	152
456.hmmer	8	239	312	**236**	**316**	235	317	8	**224**	**333**	220	339	231	323
458.sjeng	8	602	161	604	160	**604**	**160**	8	577	168	580	167	**579**	**167**
462.libquantum	8	88.1	1880	96.6	1720	**89.2**	**1860**	8	88.1	1880	96.6	1720	**89.2**	**1860**
464.h264ref	8	665	266	**660**	**268**	658	269	8	615	288	616	287	**616**	**287**
471.omnetpp	8	476	105	475	105	**476**	**105**	8	463	108	**462**	**108**	459	109
473.astar	8	492	114	**484**	**116**	483	116	8	492	114	**484**	**116**	483	116
483.xalancbmk	8	**241**	**229**	240	230	245	226	8	**241**	**229**	240	230	245	226

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Bios Settings:
Set Fan Profile = Performance

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on SUT Wed Jul 29 14:56:30 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1241 v3 @ 3.50GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
```

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Platform Notes (Continued)

```
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 4
         siblings  : 8
         physical 0: cores 0 1 2 3
      cache size : 8192 KB

   From /proc/meminfo
      MemTotal:       32814316 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      Red Hat Enterprise Linux Server release 6.6 (Santiago)

   From /etc/*release* /etc/*version*
      redhat-release: Red Hat Enterprise Linux Server release 6.6 (Santiago)
      system-release: Red Hat Enterprise Linux Server release 6.6 (Santiago)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

   uname -a:
      Linux SUT 2.6.32-504.8.1.el6.x86_64 #1 SMP Wed Mar 11 12:12:13 CET 2015
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Jul 29 14:53

   SPEC is set to: /cpu2006.1.2
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda1      ext4  212G   47G  154G  24% /

   Additional information from dmidecode:
     BIOS Intel Corp. S1200RP.86B.03.01.0002.041520151123 04/15/2015
     Memory:
      4x   8 GB
      4x 0793 W-MEM1600E38G 8 GB 1600 MHz 2 rank

   (End of data from sysinfo program)
   dmidecode does not properly detect memory modules
   4 modules of 8 GB were used to run the test (32 GB total)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  Filesystem page cache cleared with:
  echo 1>       /proc/sys/vm/drop_caches
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

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General Notes (Continued)

```
Binaries compiled on a system with 2x Xeon E5-2650 v3 chips + 256 GB memory
using RedHat EL 6.6
```

Base Compiler Invocation

C benchmarks:
```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:
```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

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Peak Compiler Invocation (Continued)

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevA-mar-2015-For-Intel-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevA-mar-2015-For-Intel-Platform.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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