



SPEC® CINT2006 Result

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Supermicro

SuperServer 2048U-RTR4
(X10QRH+ , Intel Xeon E5-4660 v3)

SPECint®2006 = 54.5

SPECint_base2006 = 53.1

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2015

Hardware Availability: Jun-2015

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E5-4660 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 56 cores, 4 chips, 14 cores/chip
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 35 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 512 GB SATA III, SSD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12,
Kernel 3.12.28-4-default
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	296	33.0	292	33.5	<u>294</u>	<u>33.2</u>	256	38.2	258	37.9	<u>258</u>	<u>37.9</u>
401.bzip2	473	20.4	<u>472</u>	<u>20.4</u>	471	20.5	<u>477</u>	<u>20.2</u>	477	20.2	478	20.2
403.gcc	<u>280</u>	<u>28.8</u>	279	28.8	281	28.7	<u>262</u>	<u>30.7</u>	262	30.7	265	30.4
429.mcf	169	53.8	<u>170</u>	<u>53.5</u>	171	53.3	175	52.0	<u>175</u>	<u>52.1</u>	174	52.5
445.gobmk	<u>435</u>	<u>24.1</u>	435	24.1	435	24.1	438	23.9	<u>438</u>	<u>23.9</u>	439	23.9
456.hammer	163	57.3	<u>163</u>	<u>57.2</u>	163	57.2	169	55.3	170	54.8	<u>169</u>	<u>55.3</u>
458.sjeng	417	29.0	418	29.0	<u>418</u>	<u>29.0</u>	416	29.1	<u>416</u>	<u>29.1</u>	416	29.1
462.libquantum	3.45	6000	<u>3.49</u>	<u>5940</u>	3.52	5890	3.45	6000	<u>3.49</u>	<u>5940</u>	3.52	5890
464.h264ref	566	39.1	567	39.0	<u>567</u>	<u>39.1</u>	566	39.1	567	39.0	<u>567</u>	<u>39.1</u>
471.omnetpp	<u>187</u>	<u>33.4</u>	188	33.3	182	34.4	<u>148</u>	<u>42.3</u>	148	42.4	148	42.1
473.astar	<u>251</u>	<u>27.9</u>	251	28.0	253	27.7	260	27.0	<u>259</u>	<u>27.1</u>	259	27.1
483.xalancbmk	<u>123</u>	<u>55.9</u>	124	55.9	123	56.0	<u>127</u>	<u>54.5</u>	127	54.5	127	54.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Hyper-Threading (ALL) = Disable

COD Enable = Disable

Early Snoop = Disable

Enforce POR = Disabled

Sysinfo program /home/SPEC2K6/SPEC2006-V12/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1

running on linux-rrui Sun Jun 14 06:40:25 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-4660 v3 @ 2.10GHz

4 "physical id"s (chips)

56 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with

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info@spec.org

<http://www.spec.org/>

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Platform Notes (Continued)

```
caution.)
cpu cores : 14
siblings  : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

```
From /proc/meminfo
MemTotal:      529335176 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"
```

```
uname -a:
Linux linux-rrui 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 5 Jun 14 06:36
```

```
SPEC is set to: /home/SPEC2K6/SPEC2006-V12
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  458G  7.2G  450G   2% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.00 06/01/2015
Memory:

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Platform Notes (Continued)

16x NO DIMM NO DIMM
1x Samsung(data:13/44) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
1x Samsung(data:13/48) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
3x Samsung(data:13/51) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x Samsung(data:14/13) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
1x Samsung(data:14/16) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
5x Samsung(data:14/17) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
1x Samsung(data:14/25) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
14x Samsung(data:14/26) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
4x Samsung(data:14/47) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/SPEC2K6/SPEC2006-V12/libs/32:/home/SPEC2K6/SPEC2006-V12/libs/64:/home/SPEC2K6/SPEC2006-V12/sh"

OMP_NUM_THREADS = "56"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmarheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmarheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmarheap



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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