



# SPEC® CINT2006 Result

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## Huawei

SPECint®\_rate2006 = 2550

Huawei RH5885H V3 (Intel Xeon E7-8867 v3)

SPECint\_rate\_base2006 = 2460

CPU2006 license: 3175

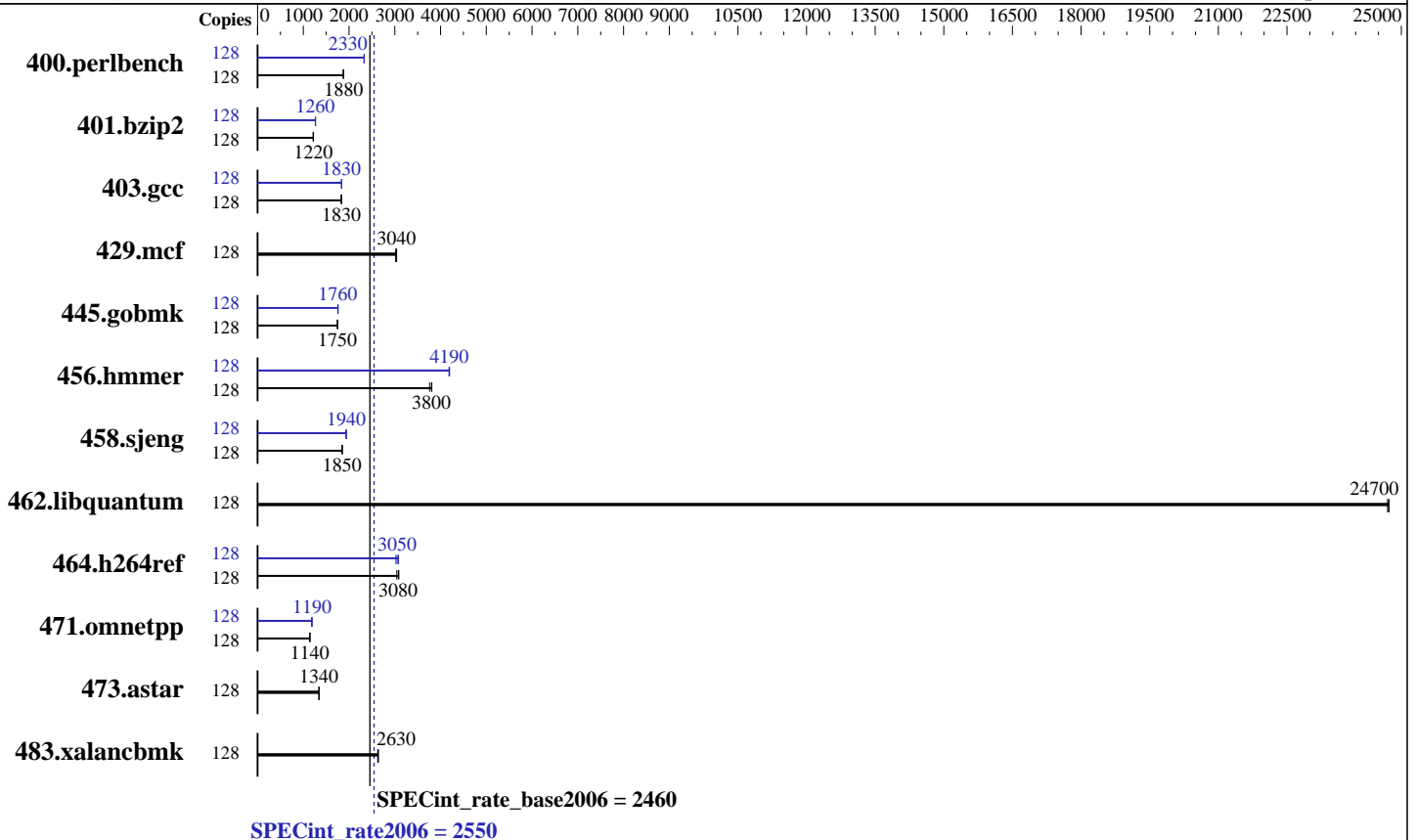
Test date: May-2015

Test sponsor: Huawei

Hardware Availability: May-2015

Tested by: Huawei

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E7-8867 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 64 cores, 4 chips, 16 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 45 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
 Disk Subsystem: 2 x 300 GB SAS, 10K RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	128	664	1880	669	1870	<b>665</b>	<b>1880</b>	128	537	2330	536	2330	<b>537</b>	<b>2330</b>
401.bzip2	128	1010	1220	<b>1014</b>	<b>1220</b>	1016	1220	128	979	1260	974	1270	<b>977</b>	<b>1260</b>
403.gcc	128	561	1840	564	1830	<b>562</b>	<b>1830</b>	128	<b>562</b>	<b>1830</b>	562	1830	560	1840
429.mcf	128	384	3040	387	3020	<b>384</b>	<b>3040</b>	128	384	3040	387	3020	<b>384</b>	<b>3040</b>
445.gobmk	128	767	1750	<b>769</b>	<b>1750</b>	770	1740	128	764	1760	763	1760	<b>764</b>	<b>1760</b>
456.hammer	128	314	3810	318	3760	<b>314</b>	<b>3800</b>	128	<b>285</b>	<b>4190</b>	285	4180	284	4200
458.sjeng	128	<b>838</b>	<b>1850</b>	838	1850	837	1850	128	799	1940	<b>799</b>	<b>1940</b>	800	1940
462.libquantum	128	107	24700	<b>107</b>	<b>24700</b>	107	24700	128	107	24700	<b>107</b>	<b>24700</b>	107	24700
464.h264ref	128	931	3040	<b>921</b>	<b>3080</b>	917	3090	128	<b>928</b>	<b>3050</b>	920	3080	937	3020
471.omnetpp	128	701	1140	<b>700</b>	<b>1140</b>	700	1140	128	671	1190	<b>673</b>	<b>1190</b>	678	1180
473.astar	128	671	1340	<b>669</b>	<b>1340</b>	667	1350	128	671	1340	<b>669</b>	<b>1340</b>	667	1350
483.xalancbmk	128	336	2630	<b>336</b>	<b>2630</b>	334	2640	128	336	2630	<b>336</b>	<b>2630</b>	334	2640

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Set Lock\_step to disabled  
Baseboard Management Controller used to adjust the fan speed to 100%  
Set DRAM Maintenance to Manual  
Set DRAM Maintenance Mode to pTRR  
Set Patrol Scrub to Enabled  
Sysinfo program /spec/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on rh5885hv3 Tue May 26 06:20:20 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E7-8867 v3 @ 2.50GHz  
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## Platform Notes (Continued)

```

4 "physical id"s (chips)
128 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
cache size : 46080 KB

```

```

From /proc/meminfo
MemTotal: 1056464084 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

```

uname -a:
Linux rh5885hv3 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 May 26 06:14

```

SPEC is set to: /spec
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs 342G 18G 324G 6% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLISV705 03/30/2015

Memory:

32x NO DIMM NO DIMM

64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

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## Platform Notes (Continued)

(End of data from sysinfo program)  
Regarding the sysinfo display about the memory installed, the correct amount of memory is 1 TB and the dmidecode description should have two lines reading as:  
32x NO DIMM NO DIMM  
64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32  
C++ benchmarks:  
icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

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## Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-HSW-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-HSW-RevG.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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