



# SPEC<sup>®</sup> CFP2006 Result

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## Hewlett-Packard Company

SPECfp<sup>®</sup>\_rate2006 = 1770

ProLiant BL660c Gen9  
(2.10 GHz, Intel Xeon E5-4669 v3)

SPECfp\_rate\_base2006 = 1720

CPU2006 license: 3

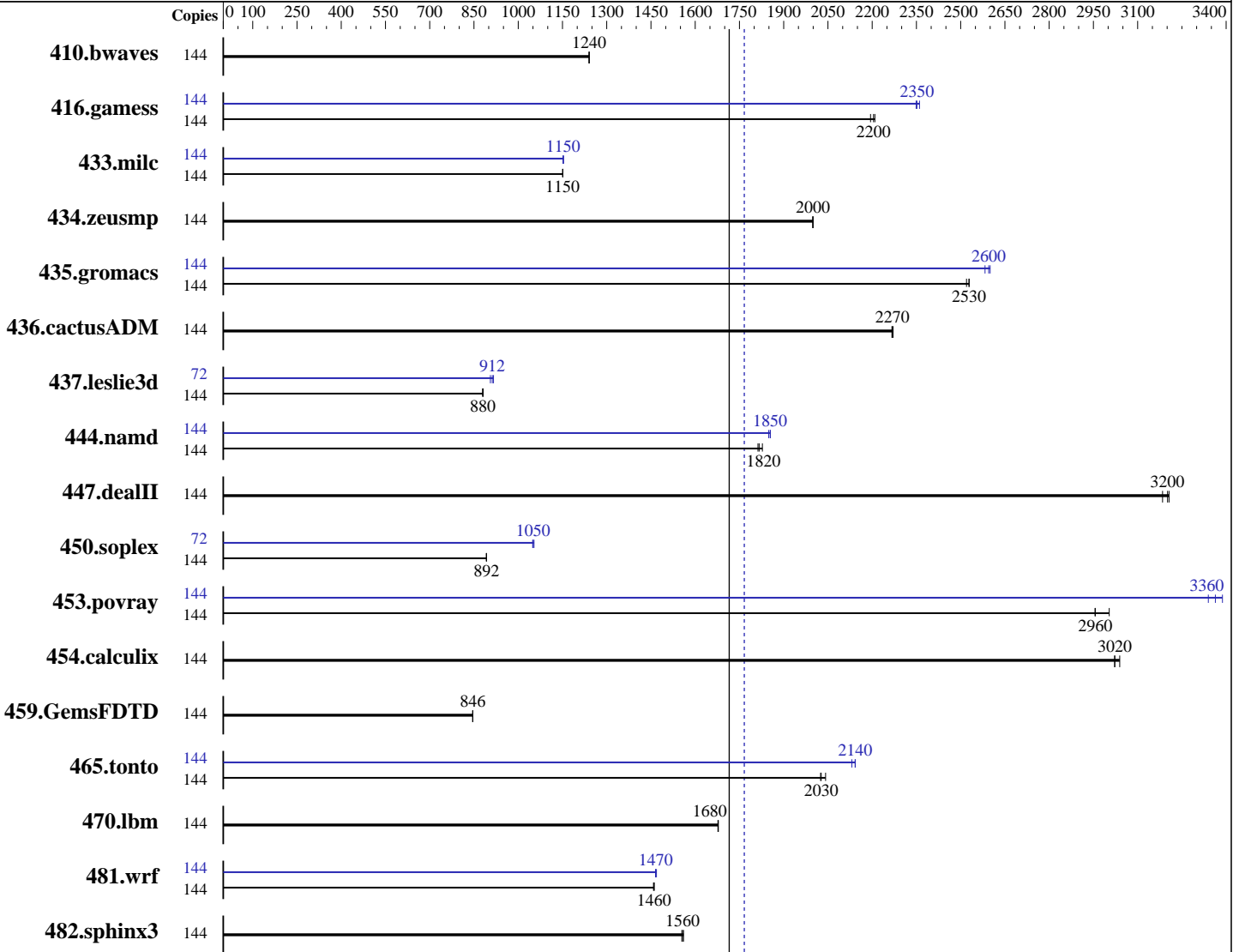
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Apr-2015

Hardware Availability: Jun-2015

Software Availability: Oct-2014



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### Hardware

CPU Name: Intel Xeon E5-4669 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 72 cores, 4 chips, 18 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64)  
 Kernel 3.12.28-4-default  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 45 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	144	1577	1240	1578	1240	<b>1578</b>	<b>1240</b>	144	1577	1240	1578	1240	<b>1578</b>	<b>1240</b>
416.gamess	144	1285	2190	1276	2210	<b>1279</b>	<b>2200</b>	144	1200	2350	1195	2360	<b>1199</b>	<b>2350</b>
433.milc	144	1149	1150	<b>1149</b>	<b>1150</b>	1148	1150	144	1147	1150	1146	1150	<b>1147</b>	<b>1150</b>
434.zeusmp	144	655	2000	<b>656</b>	<b>2000</b>	656	2000	144	655	2000	<b>656</b>	<b>2000</b>	656	2000
435.gromacs	144	<b>407</b>	<b>2530</b>	407	2530	408	2520	144	398	2580	<b>396</b>	<b>2600</b>	395	2600
436.cactusADM	144	<b>759</b>	<b>2270</b>	758	2270	759	2270	144	<b>759</b>	<b>2270</b>	758	2270	759	2270
437.leslie3d	144	1539	879	1536	881	<b>1539</b>	<b>880</b>	72	<b>742</b>	<b>912</b>	739	916	747	906
444.namd	144	632	1830	637	1810	<b>636</b>	<b>1820</b>	144	625	1850	623	1850	<b>623</b>	<b>1850</b>
447.dealII	144	<b>515</b>	<b>3200</b>	517	3180	514	3210	144	<b>515</b>	<b>3200</b>	517	3180	514	3210
450.soplex	144	1346	893	1346	892	<b>1346</b>	<b>892</b>	72	570	1050	572	1050	<b>572</b>	<b>1050</b>
453.povray	144	259	2960	255	3000	<b>259</b>	<b>2960</b>	144	<b>228</b>	<b>3360</b>	226	3390	229	3340
454.calculix	144	391	3040	<b>393</b>	<b>3020</b>	393	3020	144	391	3040	<b>393</b>	<b>3020</b>	393	3020
459.GemsFDTD	144	<b>1806</b>	<b>846</b>	1807	845	1805	846	144	<b>1806</b>	<b>846</b>	1807	845	1805	846
465.tonto	144	700	2030	694	2040	<b>699</b>	<b>2030</b>	144	<b>661</b>	<b>2140</b>	665	2130	661	2140
470.lbm	144	1180	1680	1179	1680	<b>1180</b>	<b>1680</b>	144	1180	1680	1179	1680	<b>1180</b>	<b>1680</b>
481.wrf	144	1103	1460	1101	1460	<b>1101</b>	<b>1460</b>	144	<b>1097</b>	<b>1470</b>	1095	1470	1098	1470
482.sphinx3	144	<b>1802</b>	<b>1560</b>	1799	1560	1805	1550	144	<b>1802</b>	<b>1560</b>	1799	1560	1805	1550

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```



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**Tested by:** Hewlett-Packard Company

**Test date:** Apr-2015  
**Hardware Availability:** Jun-2015  
**Software Availability:** Oct-2014

### Platform Notes

#### BIOS Configuration:

HP Power Profile set to Custom  
HP Power Regulator to HP Static High Performance Mode  
Minimum Processor Idle Power Core State set to C6 State  
Minimum Processor Idle Power Package State set to No Package State  
Energy/Performance Bias set to Maximum Performance  
Collaborative Power Control set to Disabled  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh  
Sysinfo program /home/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on bl660cgen9sles12cpu Sun Apr 12 03:53:29 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

#### From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-4669 v3 @ 2.10GHz
 4 "physical id"s (chips)
 144 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 18
  siblings  : 36
 physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
 physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
 physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
 physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB
```

#### From /proc/meminfo

```
MemTotal:      529300328 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

#### From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
```

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## Platform Notes (Continued)

```
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
Linux bl660cgen9sles12cpu 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC
2014 (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 10 09:27

SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   331G  4.0G  327G   2% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I38 03/05/2015
Memory:
32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB  
memory using RedHat EL 7.0

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64



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## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

```

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## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.lelie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
 -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias  
 -auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealIII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14  
-auto -inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>  
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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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