



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®\_rate2006 = 276**

PRIMERGY BX2560 M1, Intel Xeon E5-2603 v3, 1.6 GHz

**SPECint\_rate\_base2006 = 266**

CPU2006 license: 19

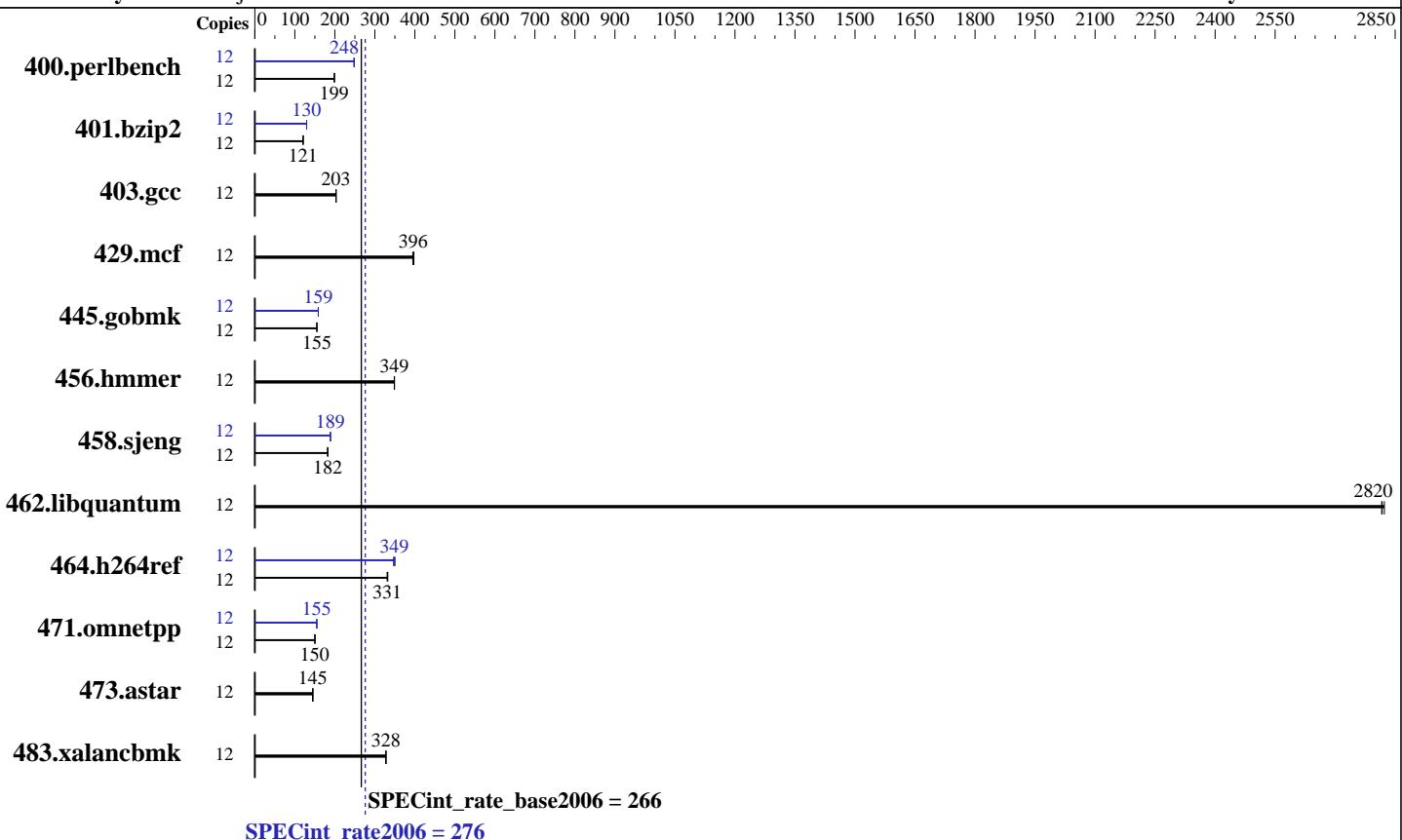
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2015

Hardware Availability: Sep-2014

Software Availability: Nov-2013



### Hardware

CPU Name:	Intel Xeon E5-2603 v3
CPU Characteristics:	
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	15 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

### Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.23.3.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	589	199	590	199	<b>590</b>	<b>199</b>	12	472	248	<b>472</b>	<b>248</b>	472	248
401.bzip2	12	959	121	<b>959</b>	<b>121</b>	961	121	12	<b>894</b>	<b>130</b>	894	129	894	130
403.gcc	12	477	203	<b>476</b>	<b>203</b>	475	203	12	477	203	<b>476</b>	<b>203</b>	475	203
429.mcf	12	275	398	277	395	<b>276</b>	<b>396</b>	12	275	398	277	395	<b>276</b>	<b>396</b>
445.gobmk	12	<b>811</b>	<b>155</b>	811	155	812	155	12	794	159	794	159	<b>794</b>	<b>159</b>
456.hmmer	12	321	348	<b>321</b>	<b>349</b>	321	349	12	321	348	<b>321</b>	<b>349</b>	321	349
458.sjeng	12	<b>797</b>	<b>182</b>	798	182	797	182	12	<b>768</b>	<b>189</b>	768	189	768	189
462.libquantum	12	88.3	2820	88.1	2820	<b>88.2</b>	<b>2820</b>	12	88.3	2820	88.1	2820	<b>88.2</b>	<b>2820</b>
464.h264ref	12	802	331	799	332	<b>802</b>	<b>331</b>	12	758	350	766	347	<b>762</b>	<b>349</b>
471.omnetpp	12	<b>500</b>	<b>150</b>	501	150	498	150	12	485	155	484	155	<b>485</b>	<b>155</b>
473.astar	12	581	145	<b>582</b>	<b>145</b>	582	145	12	581	145	<b>582</b>	<b>145</b>	582	145
483.xalancbmk	12	252	328	<b>253</b>	<b>328</b>	253	327	12	252	328	<b>253</b>	<b>328</b>	253	327

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Energy Performance = Performance

Utilization Profile = Unbalanced

QPI snoop mode: Early Snoop

COD Enable = Disabled, Early Snoop = Enabled

CPU C1E Support = Disabled

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

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## General Notes (Continued)

Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll12 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/sh -lsmartheap
```

```
473.astar: basepeak = yes
```

```
483.xalancbmk: basepeak = yes
```

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=__alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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