



# SPEC® CFP2006 Result

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## Fujitsu

### SPECfp®\_rate2006 = 768

PRIMERGY BX2560 M1, Intel Xeon E5-2680 v3, 2.5 GHz

### SPECfp\_rate\_base2006 = 746

CPU2006 license: 19

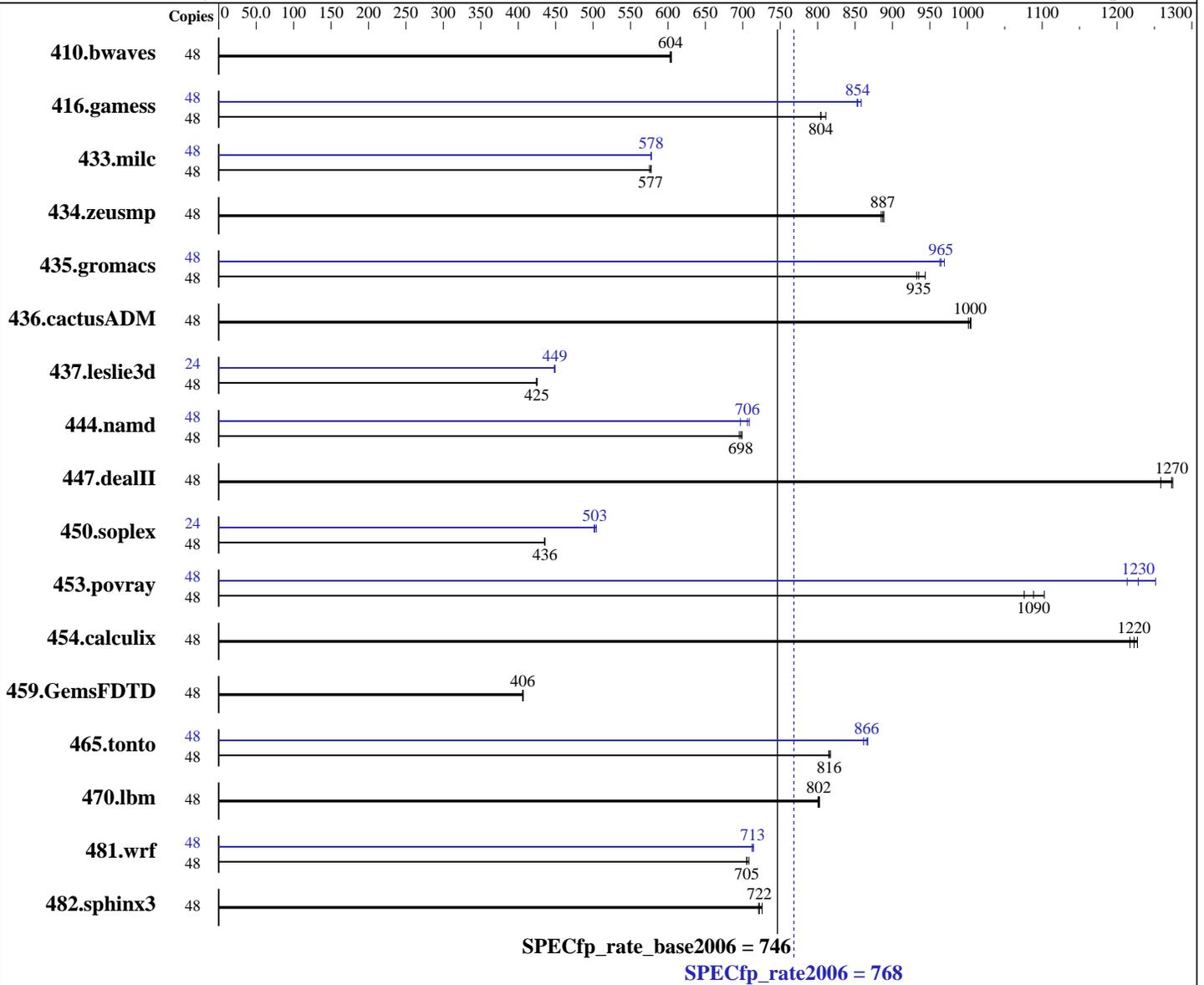
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2680 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 Kernel 3.10.0-123.8.1.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	1082	603	<b><u>1080</u></b>	<b><u>604</u></b>	1079	605	48	1082	603	<b><u>1080</u></b>	<b><u>604</u></b>	1079	605
416.gamess	48	1159	811	<b><u>1168</u></b>	<b><u>804</u></b>	1169	804	48	<b><u>1101</u></b>	<b><u>854</u></b>	1102	853	1095	858
433.milc	48	<b><u>764</u></b>	<b><u>577</u></b>	763	578	766	575	48	763	577	762	578	<b><u>763</u></b>	<b><u>578</u></b>
434.zeusmp	48	493	885	<b><u>492</u></b>	<b><u>887</u></b>	491	889	48	493	885	<b><u>492</u></b>	<b><u>887</u></b>	491	889
435.gromacs	48	363	944	<b><u>366</u></b>	<b><u>935</u></b>	368	932	48	<b><u>355</u></b>	<b><u>965</u></b>	356	963	354	969
436.cactusADM	48	573	1000	571	1000	<b><u>571</u></b>	<b><u>1000</u></b>	48	573	1000	571	1000	<b><u>571</u></b>	<b><u>1000</u></b>
437.leslie3d	48	1063	424	<b><u>1062</u></b>	<b><u>425</u></b>	1061	425	24	502	449	503	448	<b><u>502</u></b>	<b><u>449</u></b>
444.namd	48	554	695	<b><u>552</u></b>	<b><u>698</u></b>	551	699	48	543	709	<b><u>545</u></b>	<b><u>706</u></b>	552	697
447.dealII	48	431	1270	<b><u>431</u></b>	<b><u>1270</u></b>	436	1260	48	431	1270	<b><u>431</u></b>	<b><u>1270</u></b>	436	1260
450.soplex	48	918	436	920	435	<b><u>919</u></b>	<b><u>436</u></b>	24	397	505	399	501	<b><u>398</u></b>	<b><u>503</u></b>
453.povray	48	<b><u>235</u></b>	<b><u>1090</u></b>	237	1080	232	1100	48	204	1250	<b><u>208</u></b>	<b><u>1230</u></b>	210	1210
454.calculix	48	<b><u>324</u></b>	<b><u>1220</u></b>	325	1220	323	1230	48	<b><u>324</u></b>	<b><u>1220</u></b>	325	1220	323	1230
459.GemsFDTD	48	<b><u>1254</u></b>	<b><u>406</u></b>	1255	406	1252	407	48	<b><u>1254</u></b>	<b><u>406</u></b>	1255	406	1252	407
465.tonto	48	<b><u>579</u></b>	<b><u>816</u></b>	578	817	580	815	48	548	861	545	867	<b><u>546</u></b>	<b><u>866</u></b>
470.lbm	48	824	801	822	803	<b><u>823</u></b>	<b><u>802</u></b>	48	824	801	822	803	<b><u>823</u></b>	<b><u>802</u></b>
481.wrf	48	<b><u>760</u></b>	<b><u>705</u></b>	757	708	760	705	48	750	714	753	712	<b><u>752</u></b>	<b><u>713</u></b>
482.sphinx3	48	<b><u>1295</u></b>	<b><u>722</u></b>	1297	721	1289	726	48	<b><u>1295</u></b>	<b><u>722</u></b>	1297	721	1289	726

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Energy Performance = Performance  
Utilization Profile = Unbalanced

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## Platform Notes (Continued)

QPI snoop mode: Cluster on Die  
COD Enable = Enabled, Early Snoop = Disabled  
CPU C1E Support = Disabled

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m64  
C++ benchmarks:  
icpc -m64  
Fortran benchmarks:  
ifort -m64  
Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main

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## Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
 -ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
 -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
 -ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64

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Software Availability: Sep-2014

## Peak Portability Flags (Continued)

433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

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## Peak Optimization Flags (Continued)

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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