



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®\_rate2006 = **690**

PRIMERGY RX2530 M1, Intel Xeon E5-2643 v3, 3.4 GHz

SPECint\_rate\_base2006 = **667**

CPU2006 license: 19

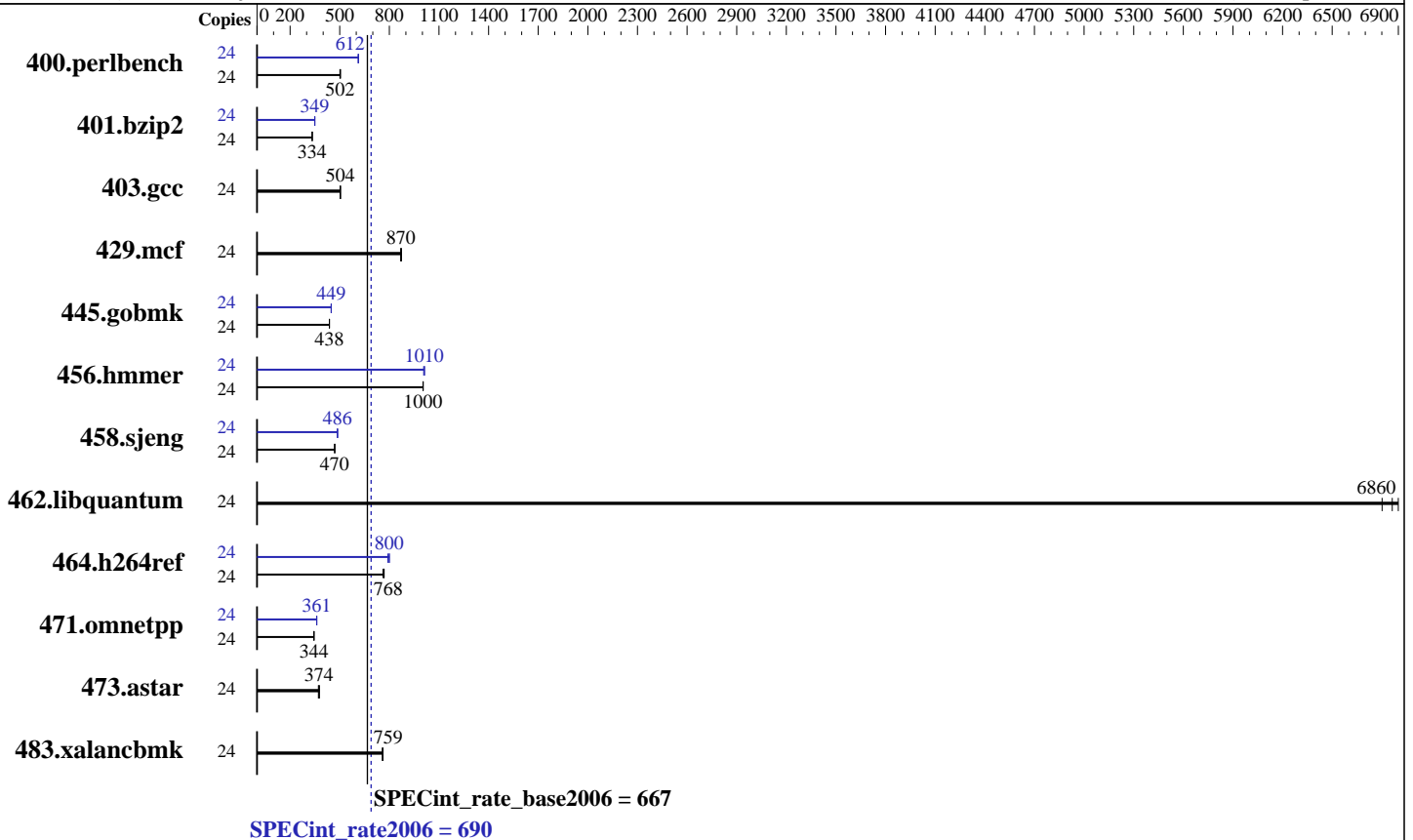
Test date: Mar-2015

Test sponsor: Fujitsu

Hardware Availability: Feb-2015

Tested by: Fujitsu

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2643 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	468	501	<b>467</b>	<b>502</b>	464	505	24	382	614	<b>383</b>	<b>612</b>	383	612
401.bzip2	24	695	333	694	334	<b>694</b>	<b>334</b>	24	663	349	663	349	<b>663</b>	<b>349</b>
403.gcc	24	382	505	<b>384</b>	<b>504</b>	384	503	24	382	505	<b>384</b>	<b>504</b>	384	503
429.mcf	24	250	874	<b>252</b>	<b>870</b>	252	869	24	250	874	<b>252</b>	<b>870</b>	252	869
445.gobmk	24	575	438	575	438	<b>575</b>	<b>438</b>	24	561	448	561	449	<b>561</b>	<b>449</b>
456.hammer	24	223	1000	223	1010	<b>223</b>	<b>1000</b>	24	<b>221</b>	<b>1010</b>	222	1010	221	1010
458.sjeng	24	617	470	<b>618</b>	<b>470</b>	619	469	24	597	486	596	487	<b>597</b>	<b>486</b>
462.libquantum	24	<b>72.5</b>	<b>6860</b>	73.1	6800	72.1	6900	24	<b>72.5</b>	<b>6860</b>	73.1	6800	72.1	6900
464.h264ref	24	697	762	<b>691</b>	<b>768</b>	691	768	24	<b>664</b>	<b>800</b>	671	791	664	800
471.omnetpp	24	<b>436</b>	<b>344</b>	437	344	435	345	24	414	362	416	360	<b>416</b>	<b>361</b>
473.astar	24	446	378	<b>451</b>	<b>374</b>	454	371	24	446	378	<b>451</b>	<b>374</b>	454	371
483.xalancbmk	24	218	758	217	762	<b>218</b>	<b>759</b>	24	218	758	217	762	<b>218</b>	<b>759</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Energy performance = Performance  
QPI snoop mode: COD = disable, Early snoop = enable  
CPU C1E Support = Disabled

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/ICC14.0-SPECcpu2006/libs/32:/ICC14.0-SPECcpu2006/libs/64:/ICC14.0-SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches

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## General Notes (Continued)

runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

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## Peak Compiler Invocation (Continued)

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

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## Peak Optimization Flags (Continued)

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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