



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®\_rate2006 = 424

PRIMERGY RX2530 M1, Intel Xeon E5-2623 v3, 3.0 GHz

SPECint\_rate\_base2006 = 410

CPU2006 license: 19

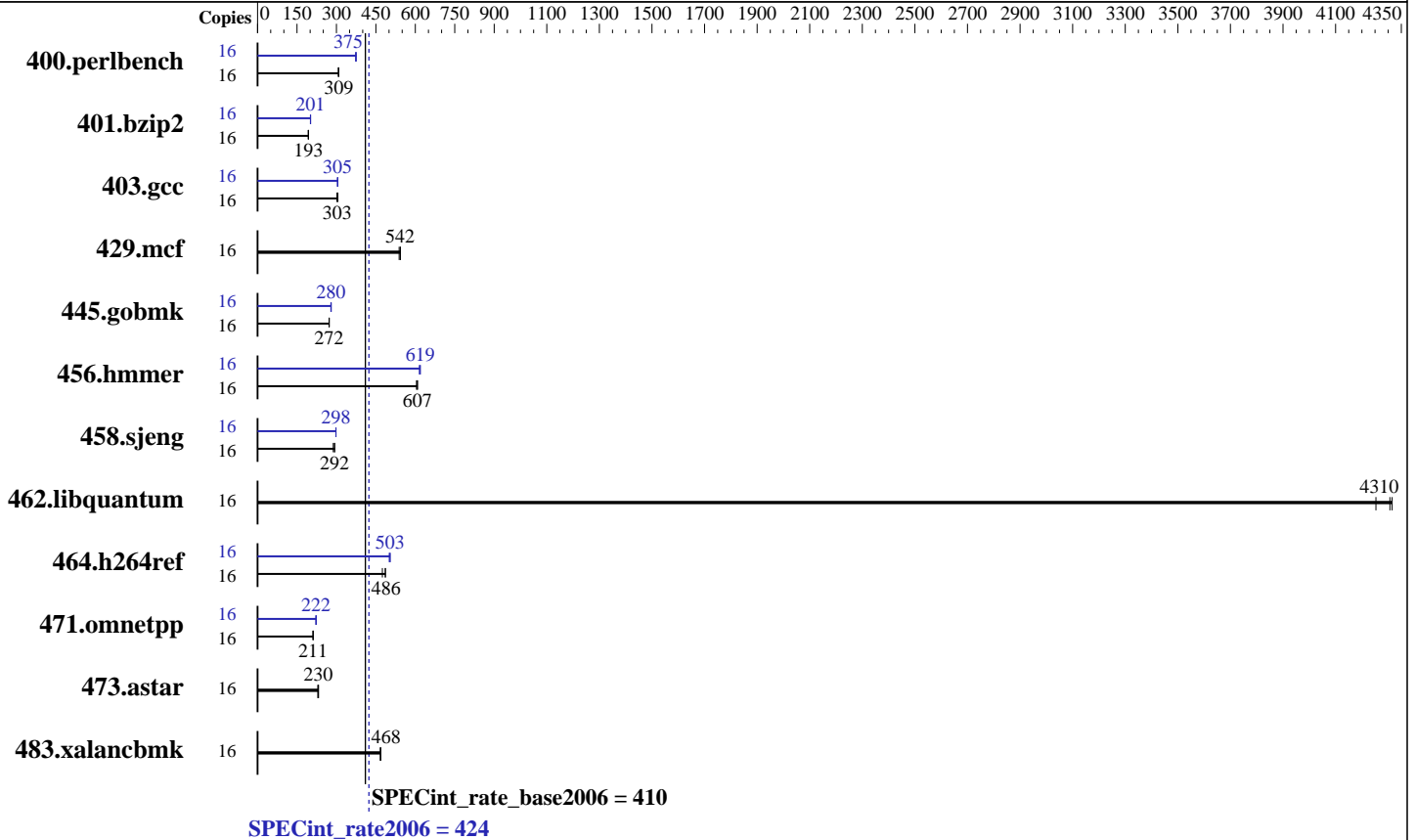
Test date: Mar-2015

Test sponsor: Fujitsu

Hardware Availability: Feb-2015

Tested by: Fujitsu

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2623 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	506	309	509	307	<u>507</u>	<u>309</u>	16	417	375	419	373	<u>417</u>	<u>375</u>
401.bzip2	16	800	193	<u>799</u>	<u>193</u>	798	194	16	<u>767</u>	<u>201</u>	768	201	765	202
403.gcc	16	422	306	426	302	<u>425</u>	<u>303</u>	16	<u>423</u>	<u>305</u>	422	305	424	304
429.mcf	16	<u>269</u>	<u>542</u>	269	543	271	538	16	<u>269</u>	<u>542</u>	269	543	271	538
445.gobmk	16	616	272	617	272	<u>617</u>	<u>272</u>	16	<u>600</u>	<u>280</u>	602	279	599	280
456.hammer	16	247	604	245	608	<u>246</u>	<u>607</u>	16	243	615	241	619	<u>241</u>	<u>619</u>
458.sjeng	16	<u>662</u>	<u>292</u>	660	293	672	288	16	650	298	<u>649</u>	<u>298</u>	649	298
462.libquantum	16	76.8	4310	<u>77.0</u>	<u>4310</u>	77.9	4250	16	76.8	4310	<u>77.0</u>	<u>4310</u>	77.9	4250
464.h264ref	16	<u>729</u>	<u>486</u>	728	486	746	475	16	<u>704</u>	<u>503</u>	707	501	702	505
471.omnetpp	16	<u>473</u>	<u>211</u>	473	211	473	211	16	<u>450</u>	<u>222</u>	448	223	450	222
473.astar	16	<u>487</u>	<u>230</u>	484	232	490	229	16	<u>487</u>	<u>230</u>	484	232	490	229
483.xalancbmk	16	236	469	<u>236</u>	<u>468</u>	236	467	16	236	469	<u>236</u>	<u>468</u>	236	467

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Energy performance = Performance  
QPI snoop mode: COD = disable, Early snoop = enable  
CPU C1E Support = Disabled

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/ICC14.0-SPECcpu2006/libs/32:/ICC14.0-SPECcpu2006/libs/64:/ICC14.0-SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches

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## General Notes (Continued)

runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

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## Peak Compiler Invocation (Continued)

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

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## Peak Optimization Flags (Continued)

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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