



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 2620

PowerEdge R930 (Intel Xeon E7-8870 v3, 2.10 GHz)

SPECint_rate_base2006 = 2530

CPU2006 license: 55

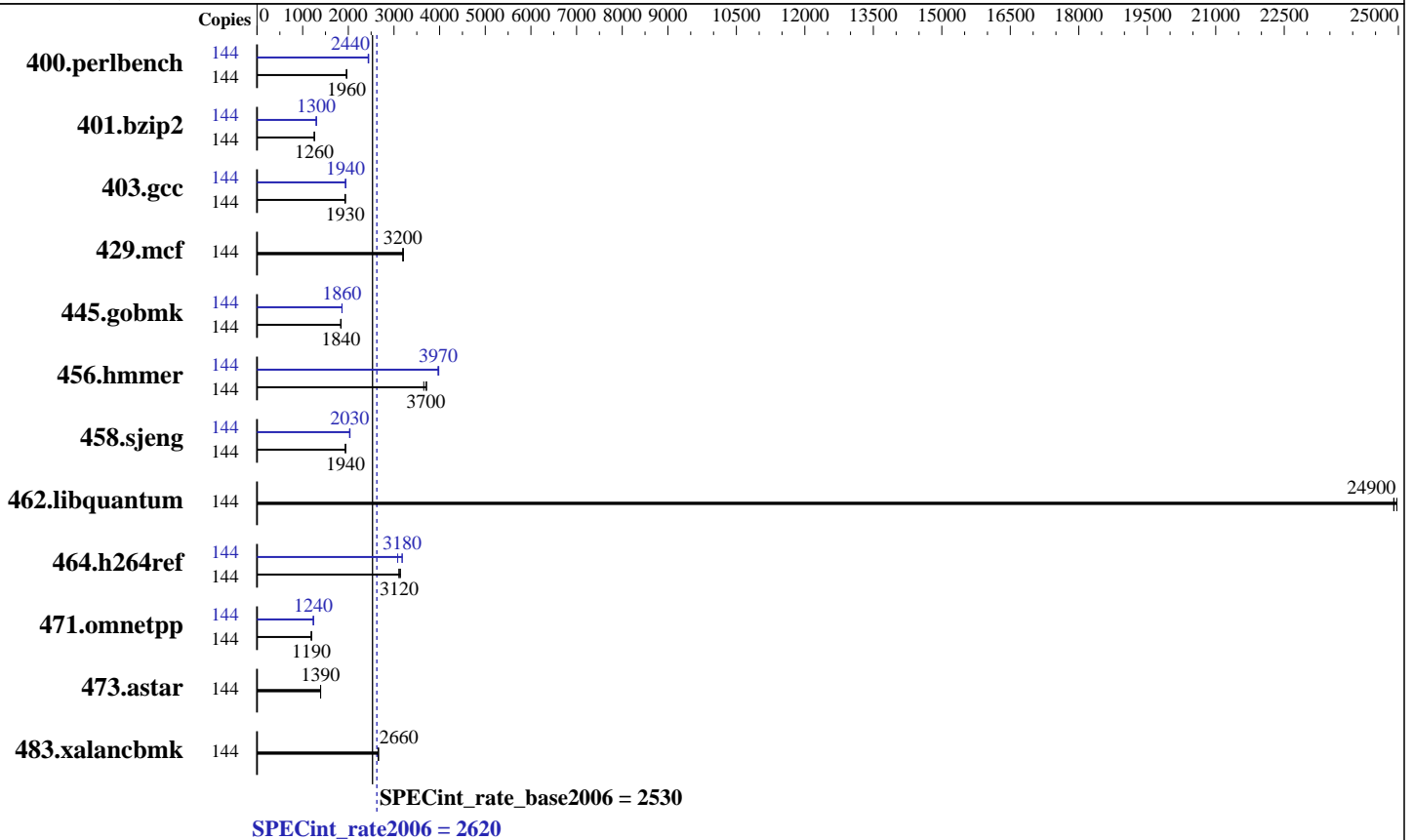
Test date: Apr-2015

Test sponsor: Dell Inc.

Hardware Availability: Jun-2015

Tested by: Dell Inc.

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E7-8870 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 72 cores, 4 chips, 18 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 45 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
 Disk Subsystem: 2 x 300 GB 15000 RPM SAS6, RAID0
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12
 3.12.28-4-default
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 5 (multi-user with display manager)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	144	<u>718</u>	<u>1960</u>	718	1960	715	1970	144	575	2450	<u>577</u>	<u>2440</u>	578	2430
401.bzip2	144	1107	1260	1106	1260	<u>1106</u>	<u>1260</u>	144	1072	1300	1067	1300	<u>1070</u>	<u>1300</u>
403.gcc	144	602	1930	598	1940	<u>601</u>	<u>1930</u>	144	598	1940	<u>597</u>	<u>1940</u>	596	1940
429.mcf	144	<u>411</u>	<u>3200</u>	411	3190	410	3210	144	<u>411</u>	<u>3200</u>	411	3190	410	3210
445.gobmk	144	<u>820</u>	<u>1840</u>	820	1840	825	1830	144	<u>812</u>	<u>1860</u>	811	1860	812	1860
456.hammer	144	362	3710	368	3650	<u>363</u>	<u>3700</u>	144	337	3980	<u>338</u>	<u>3970</u>	339	3970
458.sjeng	144	900	1940	899	1940	<u>899</u>	<u>1940</u>	144	855	2040	860	2030	<u>857</u>	<u>2030</u>
462.libquantum	144	<u>120</u>	<u>24900</u>	120	24900	120	25000	144	<u>120</u>	<u>24900</u>	120	24900	120	25000
464.h264ref	144	1026	3110	<u>1022</u>	<u>3120</u>	1014	3140	144	1034	3080	1002	3180	<u>1003</u>	<u>3180</u>
471.omnetpp	144	<u>755</u>	<u>1190</u>	758	1190	753	1190	144	731	1230	<u>728</u>	<u>1240</u>	727	1240
473.astar	144	726	1390	<u>726</u>	<u>1390</u>	726	1390	144	726	1390	<u>726</u>	<u>1390</u>	726	1390
483.xalancbmk	144	373	2660	375	2650	<u>374</u>	<u>2660</u>	144	373	2660	375	2650	<u>374</u>	<u>2660</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
 Virtualization Technology disabled
 System Profile set to Custom
 CPU Power Management set to Maximum Performance
 Memory Frequency set to Maximum Performance
 Turbo Boost enabled
 Energy Efficient Turbo disabled
 C1E disabled
 C States disabled
 Collaborative CPU Performance Control disabled
 Memory Patrol Scrub disabled
 Memory Refresh Rate set to 1x
 Uncore Frequency set to Maximum
 Energy Efficient Policy set to Performance
 Monitor/MWait enabled

Sysinfo program

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Platform Notes (Continued)

```
/root/Desktop/Performance/ic15.0_Aug29_2014/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-wq6y Fri Apr 10 21:42:54 2015
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8870 v3 @ 2.10GHz
 4 "physical id"s (chips)
144 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 18
siblings : 36
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB
```

```
From /proc/meminfo
MemTotal: 529169424 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsc_release -d
SUSE Linux Enterprise Server 12
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"
```

```
uname -a:
Linux linux-wq6y 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux
```

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Platform Notes (Continued)

run-level 5 Apr 10 21:35

SPEC is set to: /root/Desktop/Performance/ic15.0_Aug29_2014

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdb2	ext4	366G	21G	344G	6%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.1 [MRC_096] 03/27/2015

Memory:

32x 00AD00B300AD Not Specified 16 GB 2 rank 2133 MHz, configured at 1600 MHz
64x Not Specified Not Specified

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = */root/Desktop/Performance/ic15.0_Aug29_2014/libs/32:/root/Desktop/Performance/ic15.0_Aug29_2014/libs/64:/root/Desktop/Performance/ic15.0_Aug29_2014/sh*

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.20150421.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.20150421.xml>

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For other inquiries, please contact webmaster@spec.org.

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