



# SPEC® CINT2006 Result

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Dell Inc.

**SPECint\_rate2006 = 2840**

PowerEdge R930 (Intel Xeon E7-8890 v3, 2.50 GHz)

**SPECint\_rate\_base2006 = 2740**

CPU2006 license: 55

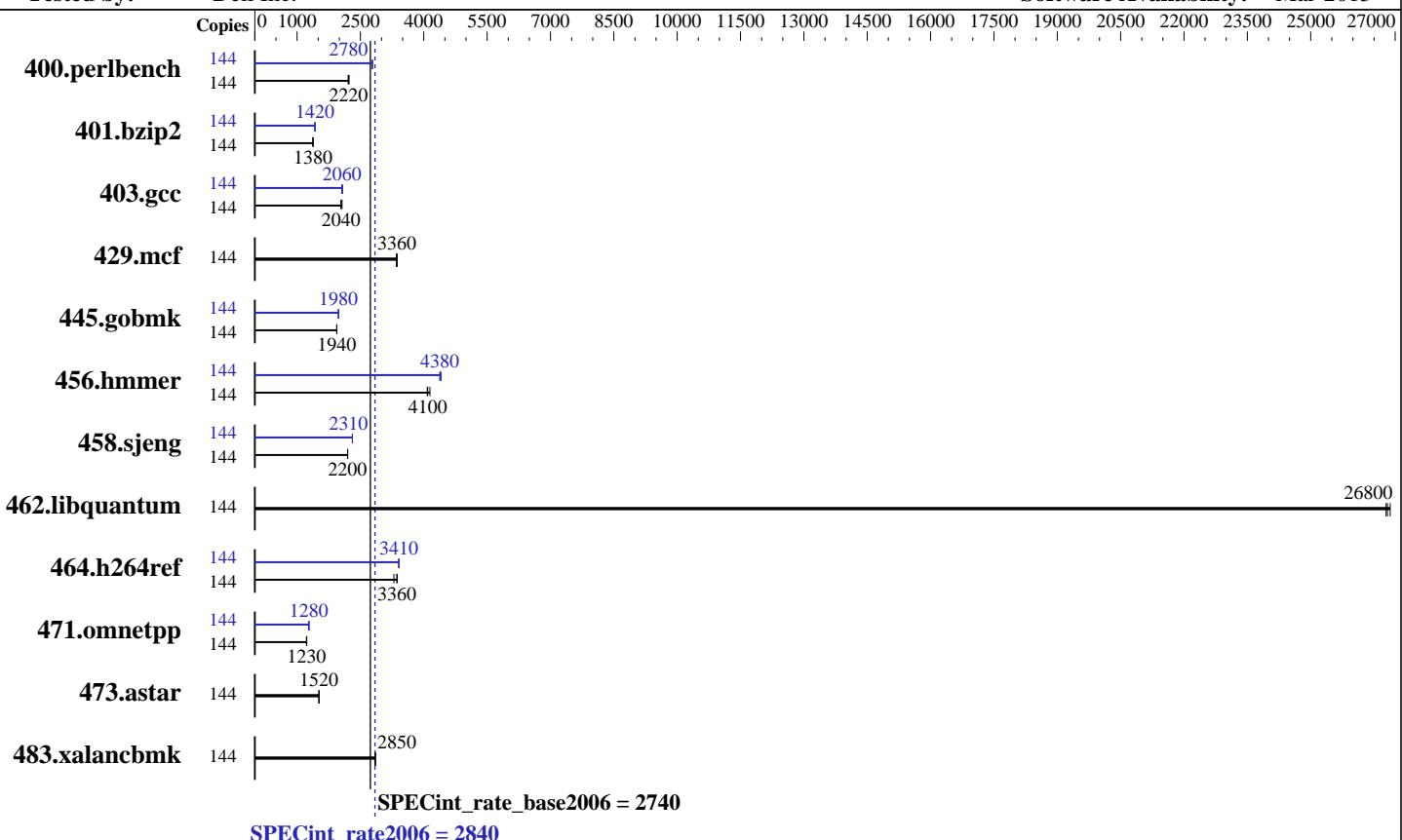
**Test date:** Apr-2015

**Test sponsor:** Dell Inc.

**Hardware Availability:** Jun-2015

**Tested by:** Dell Inc.

**Software Availability:** Mar-2015



## Hardware

CPU Name: Intel Xeon E7-8890 v3  
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
CPU MHz: 2500  
FPU: Integrated  
CPU(s) enabled: 72 cores, 4 chips, 18 cores/chip, 2 threads/core  
CPU(s) orderable: 2,4 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 45 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
Disk Subsystem: 2 x 300 GB 15000 RPM RAID0  
Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12 3.12.28-4-default  
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
Auto Parallel: No  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 32-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	144	637	2210	630	2230	<b>634</b>	<b>2220</b>	144	<b>506</b>	<b>2780</b>	505	2790	507	2780
401.bzip2	144	1007	1380	1010	1380	<b>1009</b>	<b>1380</b>	144	978	1420	974	1430	<b>976</b>	<b>1420</b>
403.gcc	144	<b>568</b>	<b>2040</b>	568	2040	561	2060	144	<b>561</b>	<b>2060</b>	558	2080	562	2060
429.mcf	144	<b>391</b>	<b>3360</b>	392	3350	390	3370	144	<b>391</b>	<b>3360</b>	392	3350	390	3370
445.gobmk	144	778	1940	779	1940	<b>779</b>	<b>1940</b>	144	<b>763</b>	<b>1980</b>	763	1980	763	1980
456.hammer	144	329	4080	324	4150	<b>328</b>	<b>4100</b>	144	305	4410	307	4380	<b>306</b>	<b>4380</b>
458.sjeng	144	793	2200	<b>793</b>	<b>2200</b>	793	2200	144	754	2310	<b>754</b>	<b>2310</b>	754	2310
462.libquantum	144	111	26900	111	26800	<b>111</b>	<b>26800</b>	144	111	26900	111	26800	<b>111</b>	<b>26800</b>
464.h264ref	144	944	3370	968	3290	<b>948</b>	<b>3360</b>	144	935	3410	934	3410	<b>935</b>	<b>3410</b>
471.omnetpp	144	732	1230	<b>734</b>	<b>1230</b>	736	1220	144	702	1280	704	1280	<b>703</b>	<b>1280</b>
473.astar	144	665	1520	<b>665</b>	<b>1520</b>	666	1520	144	665	1520	<b>665</b>	<b>1520</b>	666	1520
483.xalancbmk	144	<b>348</b>	<b>2850</b>	348	2860	349	2840	144	<b>348</b>	<b>2850</b>	348	2860	349	2840

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Virtualization Technology disabled

System Profile set to Custom

CPU Power Management set to Maximum Performance

Memory Frequency set to Maximum Performance

Turbo Boost enabled

Energy Efficient Turbo disabled

C1E disabled

C States disabled

Collaborative CPU Performance Control disabled

Memory Patrol Scrub disabled

Memory Refresh Rate set to 1x

Uncore Frequency set to Maximum

Energy Efficient Policy set to Performance

Monitor/MWait enabled

Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1

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## Platform Notes (Continued)

running on linux-8jiq Sat Apr 4 16:25:51 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E7-8890 v3 @ 2.50GHz
        4 "physical id"s (chips)
        144 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 18
        siblings : 36
        physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
    cache size : 46080 KB
```

```
From /proc/meminfo
    MemTotal:      529203048 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 0
    # This file is deprecated and will be removed in a future service pack or
    release.
    # Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12"
    VERSION_ID="12"
    PRETTY_NAME="SUSE Linux Enterprise Server 12"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12"
```

```
uname -a:
Linux linux-8jiq 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 4 16:22
```

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## Platform Notes (Continued)

SPEC is set to: /root/cpu2006-1.2

Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda2 ext4 547G 8.7G 538G 2% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.1 [MRC\_096] 03/27/2015

Memory:

2x 00AD063200AD Not Specified 16 GB 2 rank 2133 MHz, configured at 1600 MHz  
30x 00CE00B300CE Not Specified 16 GB 2 rank 2133 MHz, configured at 1600 MHz  
64x Not Specified Not Specified

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmr: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmr: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.20150421.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.20150421.xml>

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