



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint®2006 = **62.7**

Express5800/T120f (Intel Xeon E5-2680 v3)

SPECint_base2006 = **60.2**

CPU2006 license: 9006

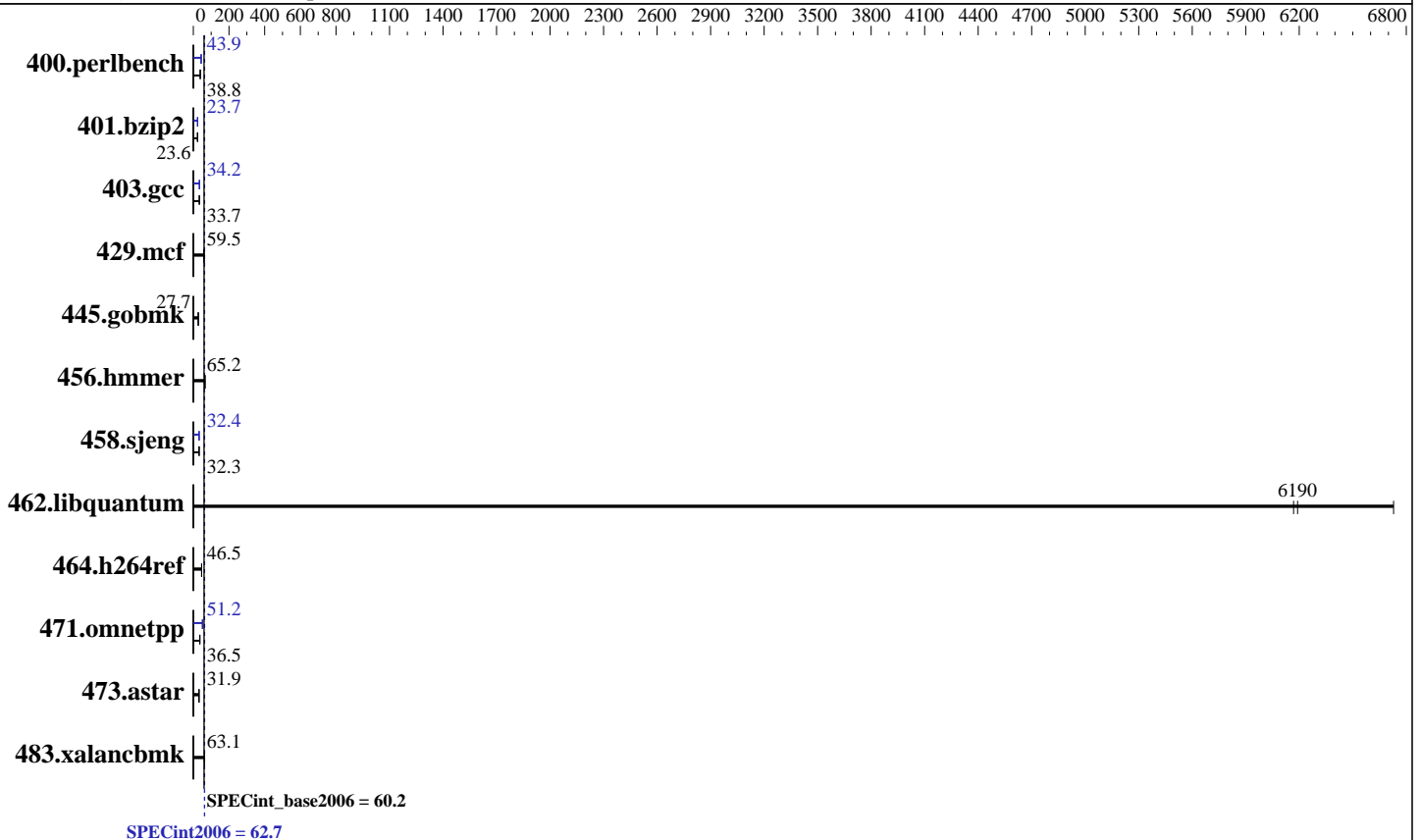
Test date: Feb-2015

Test sponsor: NEC Corporation

Hardware Availability: Jan-2015

Tested by: NEC Corporation

Software Availability: Jul-2014



Hardware

CPU Name: Intel Xeon E5-2680 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Kernel 2.6.32-431.20.3.el6.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = 62.7

Express5800/T120f (Intel Xeon E5-2680 v3)

SPECint_base2006 = 60.2

CPU2006 license: 9006

Test date: Feb-2015

Test sponsor: NEC Corporation

Hardware Availability: Jan-2015

Tested by: NEC Corporation

Software Availability: Jul-2014

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	252	38.8	251	38.9	<u>252</u>	<u>38.8</u>	223	43.9	222	43.9	<u>223</u>	<u>43.9</u>
401.bzip2	408	23.6	<u>409</u>	<u>23.6</u>	410	23.5	407	23.7	<u>407</u>	<u>23.7</u>	405	23.8
403.gcc	<u>239</u>	<u>33.7</u>	239	33.7	238	33.8	236	34.1	<u>235</u>	<u>34.2</u>	235	34.2
429.mcf	153	59.5	154	59.4	<u>153</u>	<u>59.5</u>	153	59.5	154	59.4	<u>153</u>	<u>59.5</u>
445.gobmk	378	27.7	<u>379</u>	<u>27.7</u>	379	27.7	378	27.7	<u>379</u>	<u>27.7</u>	379	27.7
456.hammer	143	65.1	<u>143</u>	<u>65.2</u>	143	65.3	143	65.1	<u>143</u>	<u>65.2</u>	143	65.3
458.sjeng	374	32.3	374	32.3	<u>374</u>	<u>32.3</u>	373	32.5	373	32.4	<u>373</u>	<u>32.4</u>
462.libquantum	3.08	6730	3.36	6170	<u>3.35</u>	<u>6190</u>	3.08	6730	3.36	6170	<u>3.35</u>	<u>6190</u>
464.h264ref	<u>476</u>	<u>46.5</u>	474	46.6	477	46.4	<u>476</u>	<u>46.5</u>	474	46.6	477	46.4
471.omnetpp	171	36.5	<u>171</u>	<u>36.5</u>	172	36.4	<u>122</u>	<u>51.2</u>	123	51.0	122	51.3
473.astar	222	31.7	<u>220</u>	<u>31.9</u>	220	31.9	222	31.7	<u>220</u>	<u>31.9</u>	220	31.9
483.xalancbmk	<u>109</u>	<u>63.1</u>	110	62.9	109	63.2	<u>109</u>	<u>63.1</u>	110	62.9	109	63.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Power Management Policy: Custom
Energy Performance: Performance
Hyper-Threading: Disabled

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "24"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation	SPECint2006 =	62.7
Express5800/T120f (Intel Xeon E5-2680 v3)	SPECint_base2006 =	60.2
CPU2006 license: 9006	Test date:	Feb-2015
Test sponsor: NEC Corporation	Hardware Availability:	Jan-2015
Tested by: NEC Corporation	Software Availability:	Jul-2014

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmarthheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks (except as noted below):
icpc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation	SPECint2006 =	62.7
Express5800/T120f (Intel Xeon E5-2680 v3)	SPECint_base2006 =	60.2
CPU2006 license: 9006	Test date:	Feb-2015
Test sponsor: NEC Corporation	Hardware Availability:	Jan-2015
Tested by: NEC Corporation	Software Availability:	Jul-2014

Peak Compiler Invocation (Continued)

471.omnetpp: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
 -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = 62.7

Express5800/T120f (Intel Xeon E5-2680 v3)

SPECint_base2006 = 60.2

CPU2006 license: 9006

Test date: Feb-2015

Test sponsor: NEC Corporation

Hardware Availability: Jan-2015

Tested by: NEC Corporation

Software Availability: Jul-2014

Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Mar 10 16:02:25 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 March 2015.