



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

SPECint®\_rate2006 = 776

ProLiant DL180 Gen9  
(1.80 GHz, Intel Xeon E5-2650L v3)

SPECint\_rate\_base2006 = 743

CPU2006 license: 3

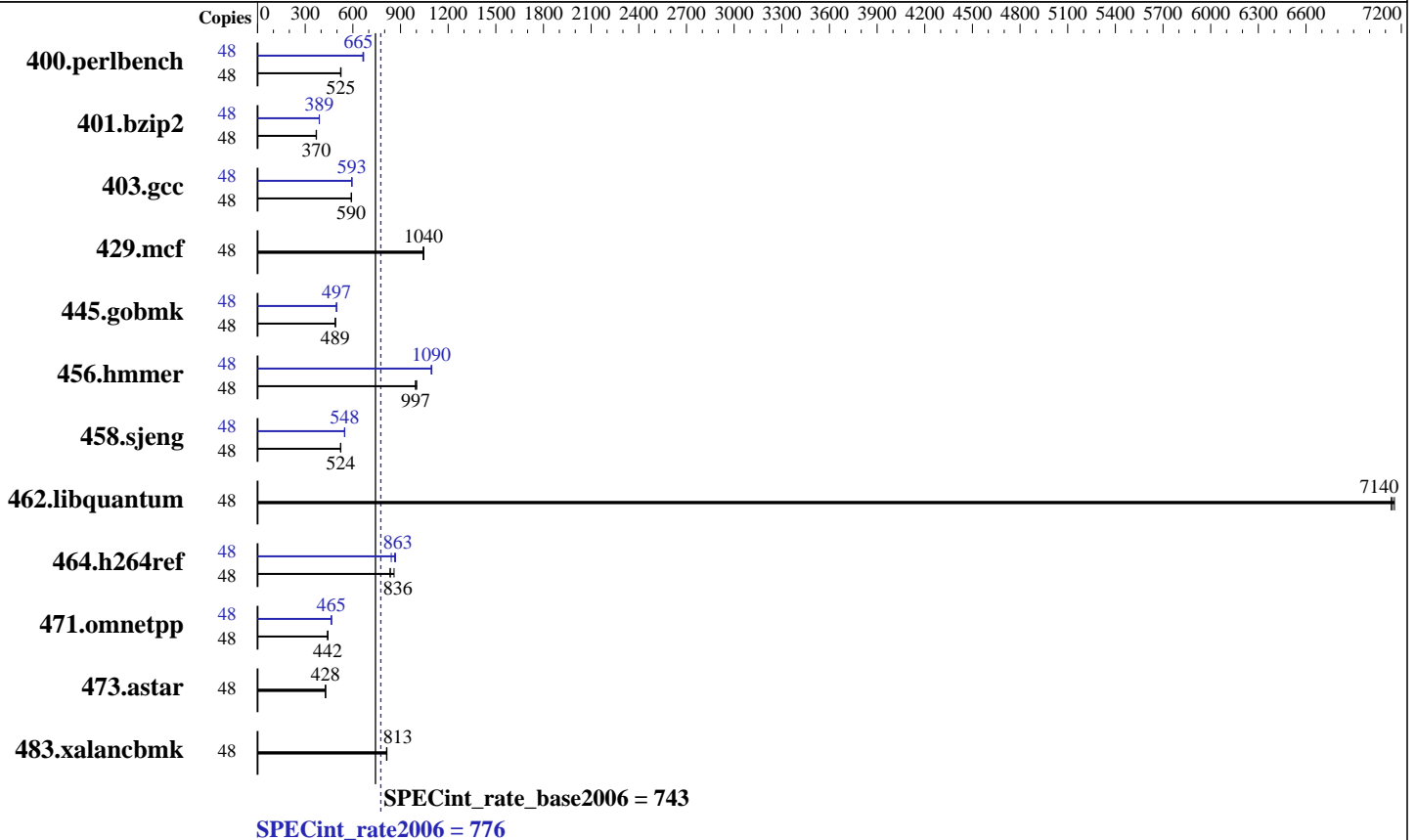
Test date: Jan-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2650L v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 Kernel 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	48	893	525	898	522	<b>893</b>	<b>525</b>	48	702	668	707	664	<b>706</b>	<b>665</b>		
401.bzip2	48	<b>1251</b>	<b>370</b>	1249	371	1256	369	48	1193	388	1191	389	<b>1191</b>	<b>389</b>		
403.gcc	48	<b>655</b>	<b>590</b>	655	590	653	592	48	651	593	<b>651</b>	<b>593</b>	651	594		
429.mcf	48	420	1040	418	1050	<b>419</b>	<b>1040</b>	48	420	1040	418	1050	<b>419</b>	<b>1040</b>		
445.gobmk	48	1026	491	1029	489	<b>1029</b>	<b>489</b>	48	1010	499	1015	496	<b>1013</b>	<b>497</b>		
456.hammer	48	447	1000	451	994	<b>449</b>	<b>997</b>	48	408	1100	<b>409</b>	<b>1090</b>	410	1090		
458.sjeng	48	1107	524	<b>1109</b>	<b>524</b>	1113	522	48	1058	549	1061	548	<b>1060</b>	<b>548</b>		
462.libquantum	48	139	7160	<b>139</b>	<b>7140</b>	139	7140	48	139	7160	<b>139</b>	<b>7140</b>	139	7140		
464.h264ref	48	1236	859	1273	834	<b>1271</b>	<b>836</b>	48	1262	841	1223	869	<b>1231</b>	<b>863</b>		
471.omnetpp	48	677	443	<b>679</b>	<b>442</b>	681	440	48	644	466	646	464	<b>645</b>	<b>465</b>		
473.astar	48	789	427	<b>788</b>	<b>428</b>	787	428	48	789	427	<b>788</b>	<b>428</b>	787	428		
483.xalancbmk	48	408	812	407	814	<b>408</b>	<b>813</b>	48	408	812	407	814	<b>408</b>	<b>813</b>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Platform Notes

BIOS Configuration:  
HP Power Profile set to Maximum Performance  
Collaborative Power Control set to Disabled  
QPI Snoop Configuration set to Cluster on Die  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh  
Sysinfo program /home/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on kokomotop Fri Jan 16 13:46:43 2015

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### Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2650L v3 @ 1.80GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores      : 6
siblings       : 12
physical 0:    cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1:    cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size     : 15360 KB

```

```

From /proc/meminfo
MemTotal:      263711272 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

```

uname -a:
Linux kokomotop 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jan 15 12:21

```

SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   318G  6.1G  312G   2% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS HP U20 12/10/2014

Memory:

16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB  
memory using RedHat EL 7.0

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

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## Base Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

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## Peak Optimization Flags (Continued)

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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