



SPEC® CINT2006 Result

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Lenovo Group Limited

Lenovo ThinkServer RD550 (Intel Xeon E5-2620 v3, 2.40 GHz)

SPECint®2006 = 55.6

SPECint_base2006 = 52.6

CPU2006 license: 9017

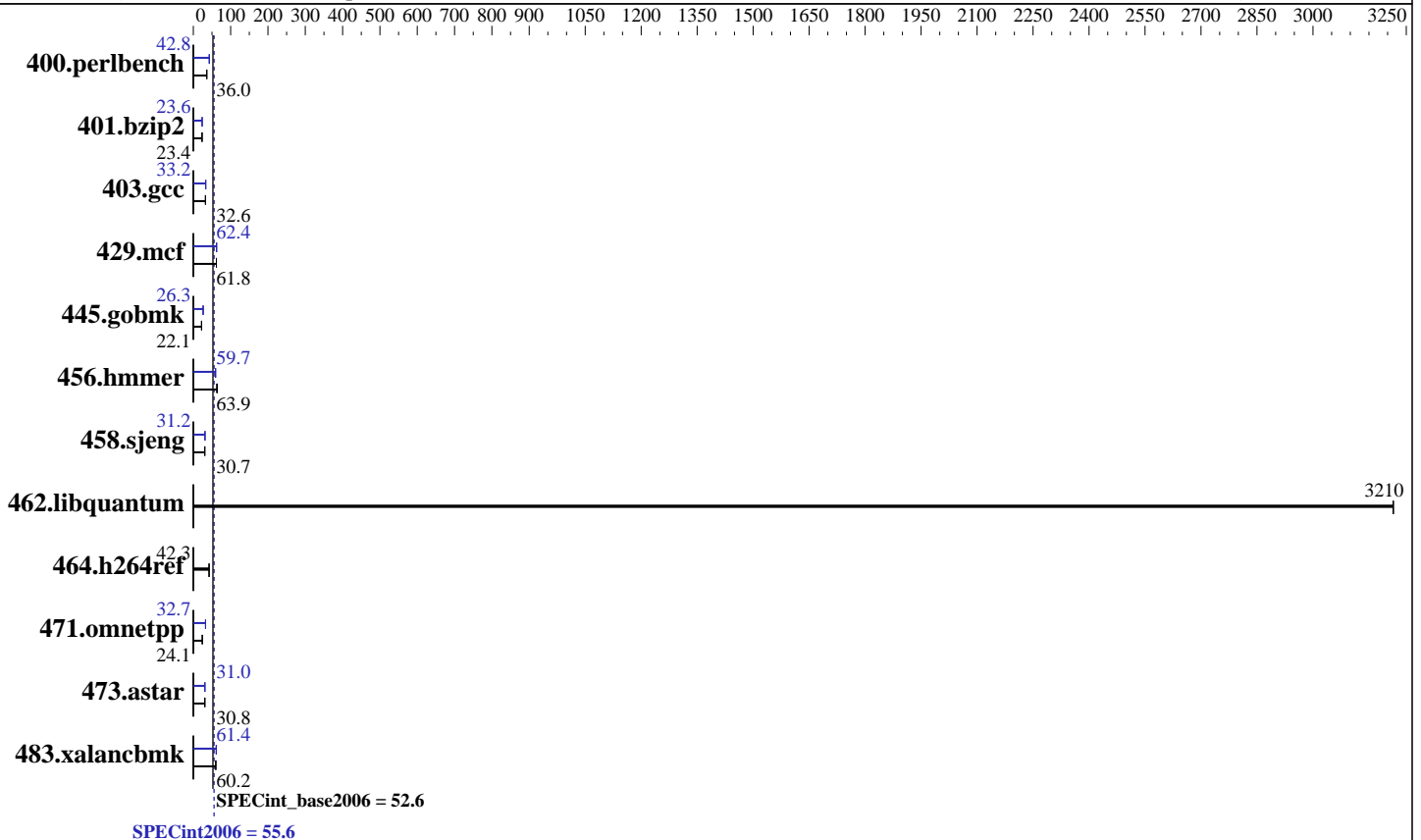
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Jan-2014



Hardware

CPU Name: Intel Xeon E5-2620 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x 800 GB SATA SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	271	36.0	<u>272</u>	<u>36.0</u>	272	36.0	229	42.7	228	42.9	<u>228</u>	<u>42.8</u>
401.bzip2	<u>413</u>	<u>23.4</u>	413	23.4	413	23.4	409	23.6	<u>409</u>	<u>23.6</u>	409	23.6
403.gcc	247	32.6	247	32.6	<u>247</u>	<u>32.6</u>	242	33.3	243	33.2	<u>242</u>	<u>33.2</u>
429.mcf	147	61.8	148	61.8	<u>148</u>	<u>61.8</u>	147	61.9	<u>146</u>	<u>62.4</u>	146	62.5
445.gobmk	<u>475</u>	<u>22.1</u>	475	22.1	474	22.1	<u>399</u>	<u>26.3</u>	399	26.3	399	26.3
456.hammer	149	62.5	146	64.1	<u>146</u>	<u>63.9</u>	157	59.5	156	59.7	<u>156</u>	<u>59.7</u>
458.sjeng	394	30.7	394	30.7	<u>394</u>	<u>30.7</u>	388	31.2	387	31.2	<u>388</u>	<u>31.2</u>
462.libquantum	6.45	3210	6.44	3220	<u>6.45</u>	<u>3210</u>	6.45	3210	6.44	3220	<u>6.45</u>	<u>3210</u>
464.h264ref	523	42.3	525	42.1	<u>523</u>	<u>42.3</u>	523	42.3	525	42.1	<u>523</u>	<u>42.3</u>
471.omnetpp	261	23.9	<u>260</u>	<u>24.1</u>	259	24.1	<u>191</u>	<u>32.7</u>	191	32.8	191	32.7
473.astar	<u>228</u>	<u>30.8</u>	229	30.7	227	30.9	226	31.0	228	30.8	<u>226</u>	<u>31.0</u>
483.xalancbmk	<u>115</u>	<u>60.2</u>	115	60.2	114	60.3	113	61.2	<u>112</u>	<u>61.4</u>	112	61.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Hyper-Threading set to Disabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled
Sysinfo program /usr/cpu2006/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on RD550 Fri Nov 14 00:29:20 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2620 v3 @ 2.40GHz
2 "physical id"s (chips)

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Platform Notes (Continued)

12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 6
siblings  : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

From /proc/meminfo

```
MemTotal:      264416148 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb_release -d

```
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux RD550 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 14 00:28

SPEC is set to: /usr/cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  730G   65G  628G  10% /
```

Additional information from dmidecode:

```
BIOS LENOVO PB1TS110 10/06/2014
```

Memory:

```
16x 16 GB
1x Hynix Semiconductor HMA42GR7MFR4N-TF 16 GB 1866 MHz 2 rank
15x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 1866 MHz 2 rank
8x NO DIMM NO DIMM
```

(End of data from sysinfo program)

RD550 support 4 channels and 12 DIMMs per processor, total 8 channels and 24 DIMMs. 16 DIMM slots installed with 16 GB DIMM for this run.

General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
```

```
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
```

```
OMP_NUM_THREADS = "12"
```

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General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmarthheap64

Base Other Flags

C benchmarks:

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Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-HSW-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-HSW-revA.xml>



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