



# SPEC® CINT2006 Result

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## Lenovo Group Limited

SPECint®\_rate2006 = 838

Lenovo ThinkServer RD650 (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint\_rate\_base2006 = 810

CPU2006 license: 9017

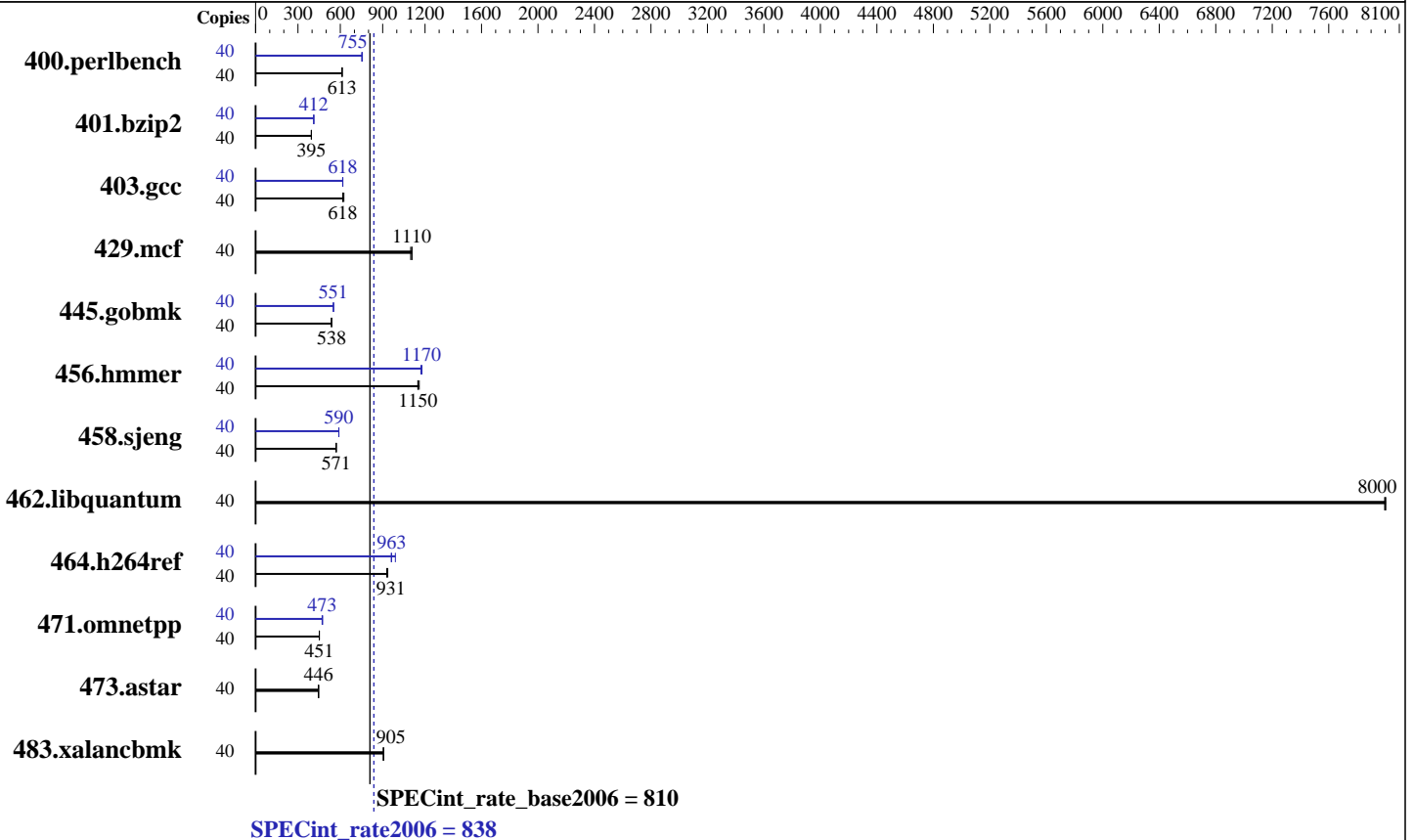
Test date: Nov-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Sep-2014

Tested by: Lenovo Group Limited

Software Availability: Jan-2014



### Hardware

CPU Name: Intel Xeon E5-2650 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 240 GB SATA SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

| Benchmark      | Base   |            |            |            |             |            |             | Peak   |            |            |            |             |            |             |
|----------------|--------|------------|------------|------------|-------------|------------|-------------|--------|------------|------------|------------|-------------|------------|-------------|
|                | Copies | Seconds    | Ratio      | Seconds    | Ratio       | Seconds    | Ratio       | Copies | Seconds    | Ratio      | Seconds    | Ratio       | Seconds    | Ratio       |
| 400.perlbench  | 40     | 638        | 612        | <b>638</b> | <b>613</b>  | 638        | 613         | 40     | 520        | 752        | 517        | 756         | <b>518</b> | <b>755</b>  |
| 401.bzip2      | 40     | 976        | 395        | 978        | 395         | <b>977</b> | <b>395</b>  | 40     | <b>937</b> | <b>412</b> | 937        | 412         | 937        | 412         |
| 403.gcc        | 40     | <b>521</b> | <b>618</b> | 515        | 625         | 521        | 618         | 40     | 522        | 617        | <b>521</b> | <b>618</b>  | 521        | 618         |
| 429.mcf        | 40     | 329        | 1110       | <b>330</b> | <b>1110</b> | 332        | 1100        | 40     | 329        | 1110       | <b>330</b> | <b>1110</b> | 332        | 1100        |
| 445.gobmk      | 40     | 779        | 539        | <b>780</b> | <b>538</b>  | 780        | 538         | 40     | 762        | 551        | <b>761</b> | <b>551</b>  | 760        | 552         |
| 456.hammer     | 40     | 322        | 1160       | <b>324</b> | <b>1150</b> | 325        | 1150        | 40     | 319        | 1170       | <b>318</b> | <b>1170</b> | 317        | 1180        |
| 458.sjeng      | 40     | 847        | 572        | <b>848</b> | <b>571</b>  | 849        | 570         | 40     | <b>821</b> | <b>590</b> | 820        | 590         | 821        | 589         |
| 462.libquantum | 40     | 104        | 8000       | 104        | 8000        | <b>104</b> | <b>8000</b> | 40     | 104        | 8000       | 104        | 8000        | <b>104</b> | <b>8000</b> |
| 464.h264ref    | 40     | 953        | 929        | 946        | 936         | <b>951</b> | <b>931</b>  | 40     | 894        | 990        | <b>919</b> | <b>963</b>  | 921        | 961         |
| 471.omnetpp    | 40     | 553        | 452        | <b>554</b> | <b>451</b>  | 554        | 451         | 40     | 531        | 471        | <b>529</b> | <b>473</b>  | 527        | 475         |
| 473.astar      | 40     | <b>630</b> | <b>446</b> | 629        | 446         | 631        | 445         | 40     | <b>630</b> | <b>446</b> | 629        | 446         | 631        | 445         |
| 483.xalancbmk  | 40     | 305        | 906        | 305        | 905         | <b>305</b> | <b>905</b>  | 40     | 305        | 906        | 305        | 905         | <b>305</b> | <b>905</b>  |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```

BIOS configuration:
Cluster On Die set to Enabled
Early Snoop set to Disabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled
Sysinfo program /usr/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on RD650 Thu Nov 6 01:50:58 2014

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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### Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.30GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 12800 KB

From /proc/meminfo
MemTotal:      264412016 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux RD650 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux

run-level 3 Nov 6 01:49

SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  217G  13G  193G   7% /

Additional information from dmidecode:
BIOS LENOVO PB2TS110 10/06/2014
Memory:
 16x 16 GB
 8x NO DIMM NO DIMM
 16x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank

(End of data from sysinfo program)
RD650 support 4 channels and 12 DIMMs per processor, total 8 channels and
24 DIMMs. 16 DIMM slots installed with 16 GB DIMM for this run.

```



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-RD650-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-RD650-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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