



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant BL460c Gen9
(3.20 GHz, Intel Xeon E5-2667 v3)

SPECint®2006 =

SPECint_base2006

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Nov-2014
Hardware Availability: Sep-2014
Software Availability: Sep-2014

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- 400.perlbench
- 401.bzip2
- 403.gcc
- 429.mcf
- 445.gobmk
- 456.hmmer
- 458.sjeng
- 462.libquantum
- 464.h264ref
- 471.omnetpp
- 473.astar
- 483

Non-Compliant

Hardware

CPU Name: Intel Xeon E5-2667 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 3200
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Kernel 3.10.0-123.el7.x86_64
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)

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Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 2 x 400 GB SAS SSD, RAID 1
Other Hardware: None

Base Processors: 32/64-bit
Link Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0

Result Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
401.bzip2	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
403.gcc	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
429.mcf	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
445.gobmk	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
456.hammer	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
458.sjeng	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
462.libquantum	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
464.h264ref	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
471.omnetpp	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
473.astar	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
483.xsbench	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



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Platform Notes

BIOS Configuration:

```

Intel Hyperthreading Options set to Disabled
HP Power Profile set to Custom
HP Power Regulator set to HP Static High Performance Mode
Minimum Processor Idle Power Core State set to C6 State
Minimum Processor Idle Power Package State set to No Package State
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
QPI Snoop Configuration set to Early Snoop
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to Refresh
Sysinfo program /cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on Y-BL460cGen9-VP Thu Nov 20 15:38:47 2014

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2667 v3 @ 3.20GHz
 2 "physical id"s (chips)
 6 "processor"s
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores      : 8
siblings       : 8
physical 0:    cores 0 1 2 3 4 5 6 7
physical 1:    cores 0 1 2 3 4 5 6 7
cache size     : 20480 KB

```

```

From /proc/meminfo
MemTotal:      263846220 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:

```

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Platform Notes (Continued)

```

NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux Y-BL460cGen9-VP2 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT
2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 2 15:36

SPEC is set to: /cpu2006
Filesystem            Type      Size      Used Avail Use% Mounted on
/dev/mapper/rhel-root ext4      310G     116G   179G   40% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 08/26/2014
Memory:
16x 752369-081 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

```



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General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter" **NON-COMPLIANT**
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/lib/64:/cpu2006/sh"
OMP_NUM_THREADS = "16"

Binaries compiled on a system with Intel Xeon E5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbenc: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
450.mmmcl: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.o.netpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lmartheap64`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64
400.perlbench: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
445.gobmk: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

C++ benchmarks (except as noted below):

`icpc -m64
471.cnetpp: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32`

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Peak Portability Flags (Continued)

```

401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
            -opt-prefetch -ansi-alias
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32
429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
          -opt-prefetch -auto-p32
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
            -ansi-alias
456.hmmer: basepeak = yes
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -unroll4

```

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-integer=block -ansi-alias
-WL,-z,muldefs -L/sh -lsmarheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto- -WL,-z,muldefs -L/sh -lsmarheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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