SPEC® CINT2006 Result

Unisys Corporation

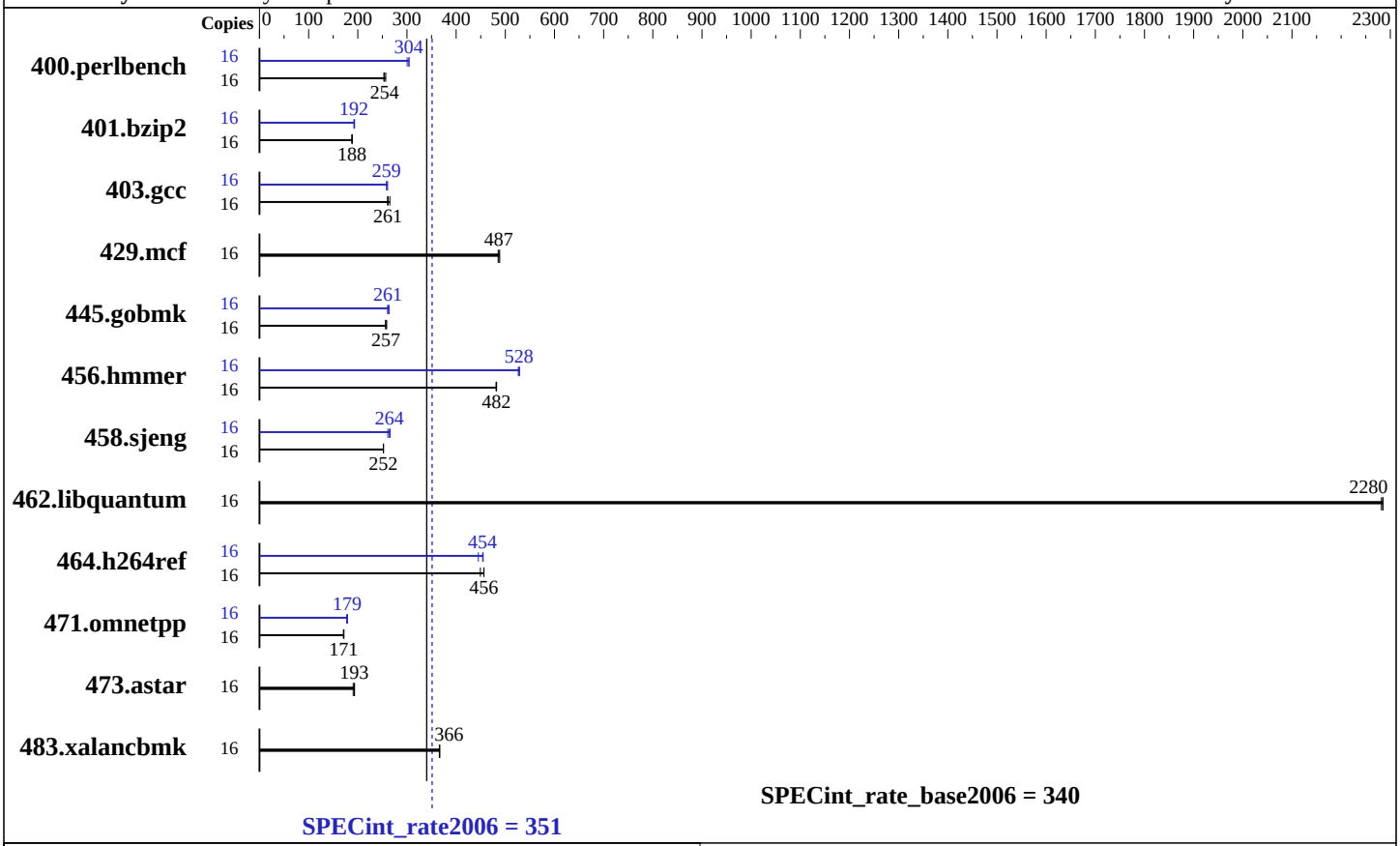
Forward! 2100 (8-core partition)

SPECint®_rate2006 = 351

SPECint_rate_base2006 = 340

CPU2006 license: 15
Test sponsor: Unisys Corporation
Tested by: Unisys Corporation

Test date: May-2014
Hardware Availability: Jun-2014
Software Availability: Jun-2014



Hardware

CPU Name:	Intel Xeon E5-2690 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 1 chip, 10 cores/chip, 2 threads/core
CPU(s) orderable:	2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (8 x 16 GB 2Rx4 PC3-12800R-11, ECC) 32 GB allocated to partition
Disk Subsystem:	8 x 600 GB 15K SAS RAID-DP
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) 3.0.76-0.11-default
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

SPEC CINT2006 Result

Unisys Corporation
Forward! 2100 (8-core partition)

SPECint_rate2006 = 351

SPECint_rate_base2006 = 340

CPU2006 license: 15
Test sponsor: Unisys Corporation
Tested by: Unisys Corporation

Test date: May-2014
Hardware Availability: Jun-2014
Software Availability: Jun-2014

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	609	257	618	253	**614**	**254**	16	**514**	**304**	514	304	520	300
401.bzip2	16	817	189	**821**	**188**	822	188	16	798	193	802	192	**802**	**192**
403.gcc	16	486	265	495	260	**493**	**261**	16	494	261	**498**	**259**	498	259
429.mcf	16	299	489	301	485	**300**	**487**	16	299	489	301	485	**300**	**487**
445.gobmk	16	655	256	649	259	**653**	**257**	16	**643**	**261**	643	261	636	264
456.hmmer	16	309	482	310	481	**310**	**482**	16	282	529	**283**	**528**	284	526
458.sjeng	16	768	252	**768**	**252**	767	252	16	742	261	**733**	**264**	729	266
462.libquantum	16	145	2290	**145**	**2280**	145	2280	16	145	2290	**145**	**2280**	145	2280
464.h264ref	16	775	457	788	449	**776**	**456**	16	778	455	**780**	**454**	795	445
471.omnetpp	16	582	172	586	171	**584**	**171**	16	**559**	**179**	559	179	564	177
473.astar	16	581	193	**582**	**193**	589	191	16	581	193	**582**	**193**	589	191
483.xalancbmk	16	302	366	**302**	**366**	301	367	16	302	366	**302**	**366**	301	367

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Sysinfo program /opt/cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ f4f716b9827353cbfded47e832667cd7
running on SPEC-AL Thu May 22 06:26:18 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2690 v2 @ 3.00GHz
      1 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
```

Continued on next page

Platform Notes (Continued)

```
        physical 0: cores 0 1 2 3 4 5 6 7
     cache size : 25600 KB

  From /proc/meminfo
     MemTotal:       32794896 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     SUSE Linux Enterprise Server 11 (x86_64)

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 11 (x86_64)
        VERSION = 11
        PATCHLEVEL = 3

  uname -a:
     Linux SPEC-AL 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
     (ccab990) x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 May 22 06:17 last=S

  SPEC is set to: /opt/cpu2006.1.2
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda1      ext3  296G   77G  219G  26% /opt/cpu2006.1.2

  (End of data from sysinfo program)

  Reporting on a 8-core, 32GB partition using
  Unisys' Secure Partitioning - s-Par(R) from a 20-core, 128GB system
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/opt/cpu2006.1.2/libs/32:/opt/cpu2006.1.2/libs/64:/opt/cpu2006.1.2/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`
`-Wl,-z,muldefs -L/sh -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`
`http://www.spec.org/cpu2006/flags/Unisys-Platform-Settings-V1.2-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`
`http://www.spec.org/cpu2006/flags/Unisys-Platform-Settings-V1.2-revA.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 22:46:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 June 2014.