SPEC® CFP2006 Result
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IBM Corporation

IBM System x3630 M4
(Intel Xeon E5-2407 v2, 2.40 GHz)

$SPECfp®\_rate2006 =$ 239

$SPECfp\_rate\_base2006 =$ 234
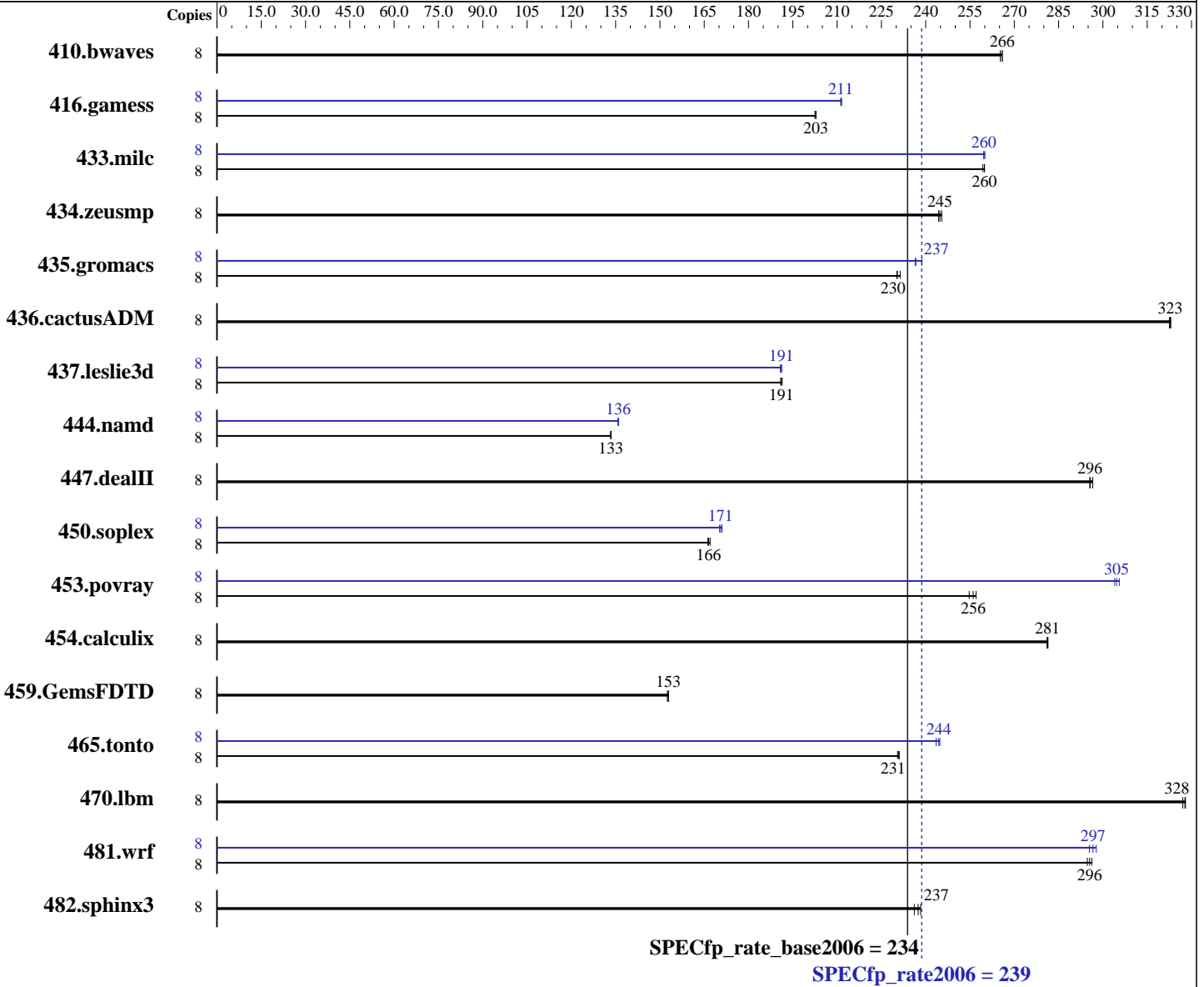
CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: May-2014
Hardware Availability: Mar-2014
Software Availability: Nov-2013

Benchmark results chart (Copies = 8 for each):

- 410.bwaves: 266
- 416.gamess: 211 / 203
- 433.milc: 260 / 260
- 434.zeusmp: 245
- 435.gromacs: 237 / 230
- 436.cactusADM: 323
- 437.leslie3d: 191 / 191
- 444.namd: 136 / 133
- 447.dealII: 296
- 450.soplex: 171 / 166
- 453.povray: 305 / 256
- 454.calculix: 281
- 459.GemsFDTD: 153
- 465.tonto: 244 / 231
- 470.lbm: 328
- 481.wrf: 297 / 296
- 482.sphinx3: 237

SPECfp_rate_base2006 = 234
SPECfp_rate2006 = 239

Hardware

CPU Name: Intel Xeon E5-2407 v2
CPU Characteristics:
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
2.6.32-431.el6.x86_64
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

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L3 Cache:	10 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	192 GB (12 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)	Peak Pointers:	32/64-bit
		Other Software:	None
Disk Subsystem:	1 x 2 TB SATA, 7200 RPM		
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	410	265	409	266	**409**	**266**	8	410	265	409	266	**409**	**266**
416.gamess	8	772	203	774	202	**773**	**203**	8	741	211	**741**	**211**	740	212
433.milc	8	283	259	283	260	**283**	**260**	8	282	260	283	260	**283**	**260**
434.zeusmp	8	297	245	**297**	**245**	298	244	8	297	245	**297**	**245**	298	244
435.gromacs	8	248	230	**248**	**230**	247	231	8	**241**	**237**	242	236	239	239
436.cactusADM	8	296	323	296	323	**296**	**323**	8	296	323	296	323	**296**	**323**
437.leslie3d	8	394	191	**393**	**191**	393	191	8	393	191	394	191	**393**	**191**
444.namd	8	**481**	**133**	481	133	481	133	8	472	136	**472**	**136**	472	136
447.dealII	8	310	296	309	297	**310**	**296**	8	310	296	309	297	**310**	**296**
450.soplex	8	401	166	**401**	**166**	400	167	8	390	171	**391**	**171**	392	170
453.povray	8	167	255	**166**	**256**	166	257	8	140	304	**140**	**305**	139	306
454.calculix	8	**235**	**281**	235	281	235	281	8	**235**	**281**	235	281	235	281
459.GemsFDTD	8	**555**	**153**	557	153	555	153	8	**555**	**153**	557	153	555	153
465.tonto	8	**341**	**231**	342	230	341	231	8	323	244	322	245	**322**	**244**
470.lbm	8	**335**	**328**	336	327	335	328	8	**335**	**328**	336	327	335	328
481.wrf	8	**302**	**296**	303	295	302	296	8	300	298	**301**	**297**	302	295
482.sphinx3	8	654	238	**657**	**237**	660	236	8	654	238	**657**	**237**	660	236

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode

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Platform Notes

```
BIOS setting:
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-20140116-ic14.0/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on x3630M4 Sat May 17 02:53:33 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2407 v2 @ 2.40GHz
      2 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 4
      physical 0: cores 0 1 2 3
      physical 1: cores 0 1 2 3
   cache size : 10240 KB

From /proc/meminfo
   MemTotal:       198464976 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux x3630M4 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 15 11:24

SPEC is set to: /home/SPECcpu-20140116-ic14.0
   Filesystem                    Type  Size  Used Avail Use% Mounted on
   /dev/mapper/vg_x3630m4-lv_home ext4  1.8T   11G  1.7T   1% /home

Additional information from dmidecode:
  BIOS IBM -[BEE135VUS-1.60]- 01/14/2014
  Memory:
   12x Samsung M393B2G70QH0-YK0 16 GB 1333 MHz 2 rank

(End of data from sysinfo program)
```

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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu-20140116-ic14.0/libs/32:/home/SPECcpu-20140116-ic14.0/libs/64:/home/SPECcpu-20140116-ic14.0/sh"

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>        /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
      icc   -m64

C++ benchmarks:
      icpc   -m64

Fortran benchmarks:
      ifort -m64

Benchmarks using both Fortran and C:
      icc   -m64 ifort -m64

Base Portability Flags

       410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
         433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
      435.gromacs: -DSPEC_CPU_LP64 -nofor_main
   436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
      437.leslie3d: -DSPEC_CPU_LP64
         444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
       450.soplex: -DSPEC_CPU_LP64
       453.povray: -DSPEC_CPU_LP64
      454.calculix: -DSPEC_CPU_LP64 -nofor_main
   459.GemsFDTD: -DSPEC_CPU_LP64
        465.tonto: -DSPEC_CPU_LP64
         470.lbm: -DSPEC_CPU_LP64
          481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
      482.sphinx3: -DSPEC_CPU_LP64

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Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```
```
  450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
  465.tonto: -DSPEC_CPU_LP64
   470.lbm: -DSPEC_CPU_LP64
   481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
  433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -auto-ilp32
```

```
   470.lbm: basepeak = yes
```

```
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
  444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -fno-alias -auto-ilp32
```

```
 447.dealII: basepeak = yes
```

```
 450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -opt-malloc-options=3
```

```
 453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
 410.bwaves: basepeak = yes
```

```
 416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-
```

```
 434.zeusmp: basepeak = yes
```

```
 437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

```
 459.GemsFDTD: basepeak = yes
```

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Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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