SPEC® CINT2006 Result

Hewlett-Packard Company

ProLiant DL560 Gen8
(2.60 GHz, Intel Xeon E5-4607 v2)

$SPECint^{®}2006 =$ 43.9

$SPECint\_base2006 =$ 41.5

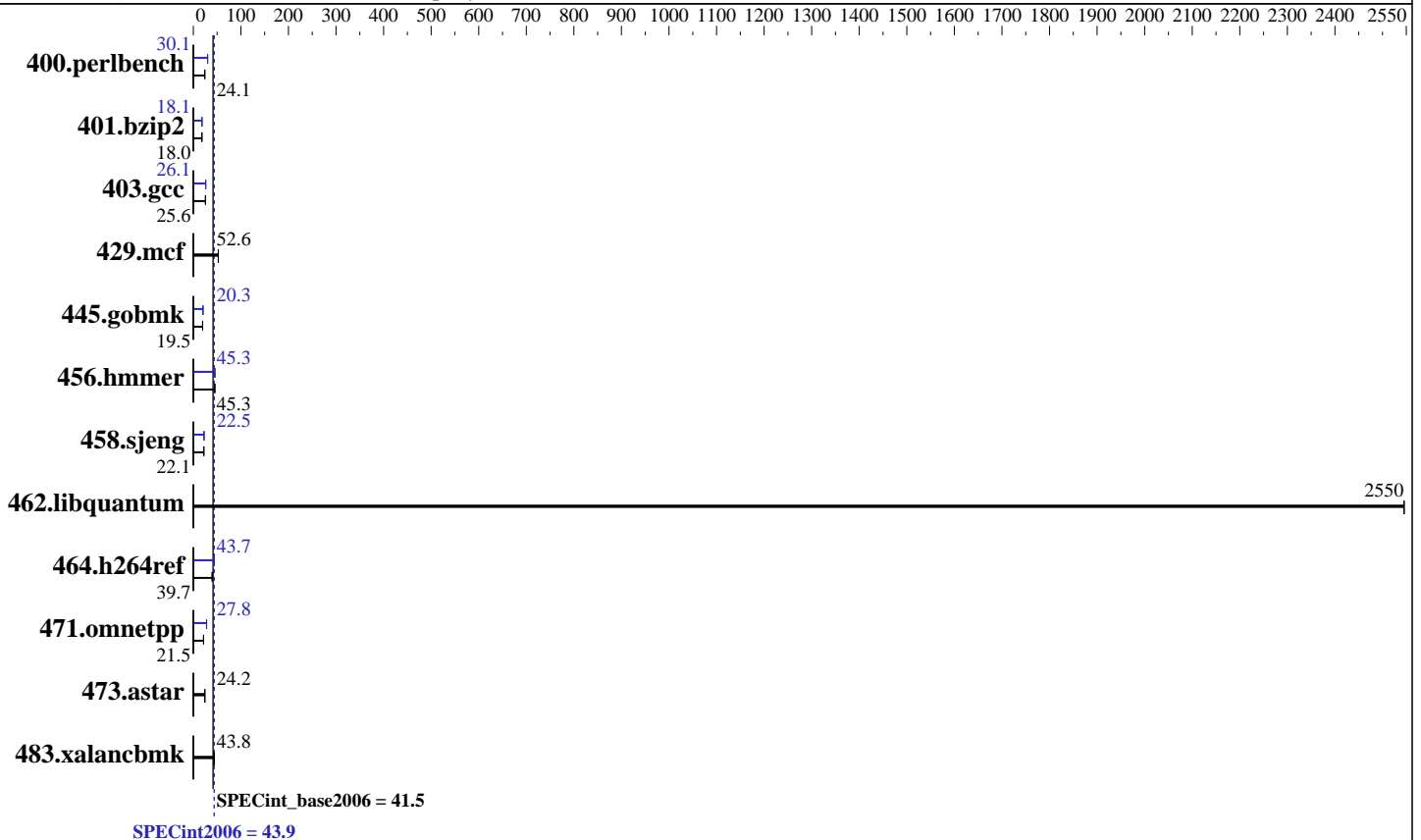|	
CPU2006 license: 3	**Test date:** Apr-2014
Test sponsor: Hewlett-Packard Company	**Hardware Availability:** Mar-2014
Tested by: Hewlett-Packard Company	**Software Availability:** Nov-2013

Benchmark results (scale 0 to 2550):

- 400.perlbench: 30.1 / 24.1
- 401.bzip2: 18.1 / 18.0
- 403.gcc: 26.1 / 25.6
- 429.mcf: 52.6
- 445.gobmk: 20.3 / 19.5
- 456.hmmer: 45.3 / 45.3
- 458.sjeng: 22.5 / 22.1
- 462.libquantum: 2550
- 464.h264ref: 43.7 / 39.7
- 471.omnetpp: 27.8 / 21.5
- 473.astar: 24.2
- 483.xalancbmk: 43.8

SPECint_base2006 = 41.5

SPECint2006 = 43.9

Hardware

CPU Name:	Intel Xeon E5-4607 v2
CPU Characteristics:	
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	15 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (32 x 8 GB 2Rx4 PC3-14900R-13, ECC, running at 1333 MHz and CL9)
Disk Subsystem:	1 x 900 GB 10 K SAS, RAID 0
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) Kernel 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**406**	**24.1**	406	24.1	405	24.1	**325**	**30.1**	325	30.1	324	30.1
401.bzip2	536	18.0	**537**	**18.0**	537	18.0	**532**	**18.1**	532	18.1	532	18.1
403.gcc	315	25.6	315	25.5	**315**	**25.6**	**309**	**26.1**	309	26.0	309	26.1
429.mcf	173	52.7	**174**	**52.6**	174	52.4	173	52.7	**174**	**52.6**	174	52.4
445.gobmk	**538**	**19.5**	538	19.5	538	19.5	517	20.3	**517**	**20.3**	517	20.3
456.hmmer	206	45.3	209	44.7	**206**	**45.3**	**206**	**45.3**	206	45.2	206	45.3
458.sjeng	548	22.1	548	22.1	**548**	**22.1**	**539**	**22.5**	539	22.5	539	22.5
462.libquantum	8.14	2550	8.14	2550	**8.14**	**2550**	8.14	2550	8.14	2550	**8.14**	**2550**
464.h264ref	558	39.7	**558**	**39.7**	558	39.7	507	43.6	**507**	**43.7**	506	43.8
471.omnetpp	**290**	**21.5**	290	21.5	290	21.6	**225**	**27.8**	222	28.1	225	27.8
473.astar	**291**	**24.2**	292	24.1	290	24.2	**291**	**24.2**	292	24.1	290	24.2
483.xalancbmk	**158**	**43.8**	157	44.0	158	43.8	**158**	**43.8**	157	44.0	158	43.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 cho always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
 cho 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 umactl --interleave=all runspec <etc>
Disabled unused Linux services through "stop_services.sh" before running.
```

Platform Notes

```
BIOS Configuration:
 HP Power Profile set to Maximum Performance
 Intel Hyperthreading Options set to Disabled
 Minimum Processor Idle Power Core State set to C1e State to Enabled
 Minimum Processor Idle Power Packages State set to Package C6 (non-retention) State
 Memory Power Savings Mode set to Maximum Performance
 Collaborative Power Control set to Disabled
 Dynamic Power Capping Functionality set to Disabled
 Thermal Configuration set to Maximum Cooling
 Processor Power and Utilization Monitoring set to Disabled
 Memory Refresh Rate set to 1x

 Sysinfo program /cpu2006/config/sysinfo.rev6818
 $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
 running on RHEL65-SR Tue Apr  8 16:15:38 2014
```

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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4607 v2 @ 2.60GHz
      4 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 6
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
      physical 2: cores 0 1 2 3 4 5
      physical 3: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       264633432 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux RHEL65-SR 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 8 16:06

SPEC is set to: /cpu2006
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda3       ext4  824G   13G  769G   2% /

Additional information from dmidecode:
  BIOS HP P77 02/04/2014
  Memory:
   32x HP 712382-071 8 GB 1333 MHz 2 rank
   16x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 256 GB and the dmidecode description should have one line reading as:
   32x HP 712382-071 8 GB 1333 MHz 2 rank
```

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General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
OMP_NUM_THREADS = "24"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
```

Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

```
  400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
      401.bzip2: -DSPEC_CPU_LP64
        403.gcc: -DSPEC_CPU_LP64
        429.mcf: -DSPEC_CPU_LP64
      445.gobmk: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
     464.h264ref: -DSPEC_CPU_LP64
     471.omnetpp: -DSPEC_CPU_LP64
       473.astar: -DSPEC_CPU_LP64
   483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

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Base Other Flags

C benchmarks:

> 403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
   icc -m64

   400.perlbench: icc -m32

   445.gobmk: icc -m32

   464.h264ref: icc -m32

C++ benchmarks (except as noted below):
   icpc -m64

   471.omnetpp: icpc -m32

Peak Portability Flags

   400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   403.gcc: -DSPEC_CPU_LP64
   429.mcf: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
   483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

   400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias

   401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
               -opt-prefetch -ansi-alias

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Peak Optimization Flags (Continued)

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block   -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.xml

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