



# SPEC® CINT2006 Result

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## Huawei Huawei CH222

SPECint®\_rate2006 = 177

SPECint\_rate\_base2006 = 171

CPU2006 license: 3175

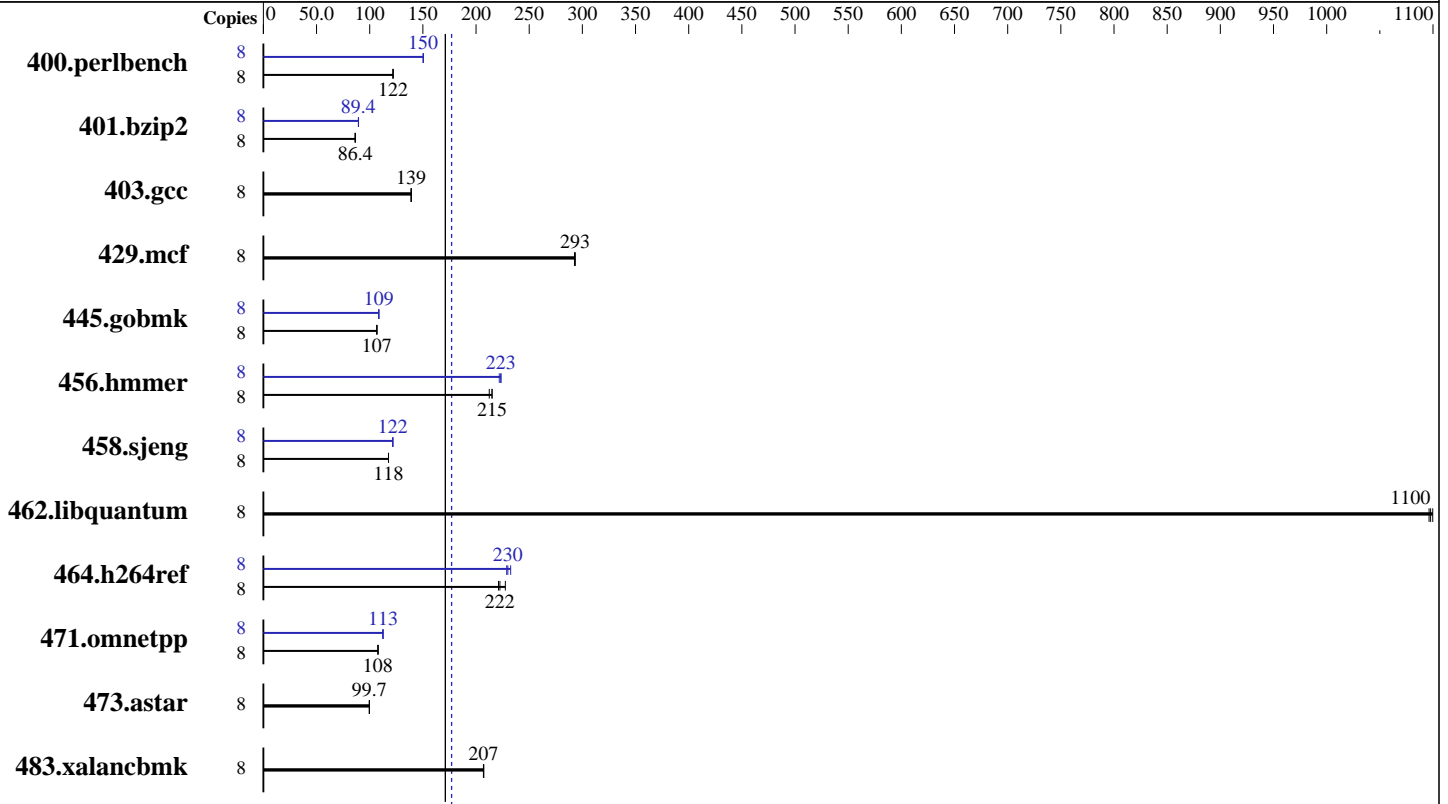
Test sponsor: Huawei

Tested by: Huawei

Test date: Apr-2014

Hardware Availability: Jan-2012

Software Availability: Nov-2013



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### Hardware

CPU Name: Intel Xeon E5-2603  
 CPU Characteristics:  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-10600R-09, ECC)  
 Disk Subsystem: 1 X 300 GB SAS 7200RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>641</b>	<b>122</b>	641	122	640	122	8	519	151	520	150	<b>520</b>	<b>150</b>
401.bzip2	8	895	86.3	<b>893</b>	<b>86.4</b>	893	86.5	8	864	89.4	863	89.5	<b>864</b>	<b>89.4</b>
403.gcc	8	<b>463</b>	<b>139</b>	463	139	464	139	8	<b>463</b>	<b>139</b>	463	139	464	139
429.mcf	8	<b>249</b>	<b>293</b>	249	293	249	293	8	<b>249</b>	<b>293</b>	249	293	249	293
445.gobmk	8	787	107	<b>786</b>	<b>107</b>	786	107	8	773	109	773	109	<b>773</b>	<b>109</b>
456.hammer	8	<b>347</b>	<b>215</b>	351	212	347	215	8	336	222	<b>334</b>	<b>223</b>	334	224
458.sjeng	8	822	118	823	118	<b>823</b>	<b>118</b>	8	<b>795</b>	<b>122</b>	795	122	795	122
462.libquantum	8	<b>151</b>	<b>1100</b>	151	1100	151	1100	8	<b>151</b>	<b>1100</b>	151	1100	151	1100
464.h264ref	8	<b>796</b>	<b>222</b>	778	228	801	221	8	<b>770</b>	<b>230</b>	761	233	774	229
471.omnetpp	8	463	108	464	108	<b>464</b>	<b>108</b>	8	<b>444</b>	<b>113</b>	445	112	444	113
473.astar	8	<b>563</b>	<b>99.7</b>	564	99.7	563	99.7	8	<b>563</b>	<b>99.7</b>	564	99.7	563	99.7
483.xalancbmk	8	266	207	267	207	<b>267</b>	<b>207</b>	8	266	207	267	207	<b>267</b>	<b>207</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Sysinfo program /spec/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on huawei Fri Apr 11 08:48:59 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2603 0 @ 1.80GHz  
2 "physical id"s (chips)  
8 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

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## Platform Notes (Continued)

```
cpu cores : 4
siblings  : 4
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:      132103760 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux huawei 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 11 08:44
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        ext4  193G   73G  110G  40% /
```

```
Additional information from dmidecode:
BIOS Insyde Corp. RMIBV372 12/21/2013
Memory:
8x NO DIMM NO DIMM
11x Samsung M393B1K70CH0-CH9 8 GB 1066 MHz 2 rank
5x Samsung M393B1K70DH0-CH9 8 GB 1066 MHz 2 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

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## General Notes (Continued)

The Huawei CH221 and the Huawei CH222 models are electronically equivalent. The results have been measured on a Huawei CH221 model

## Base Compiler Invocation

C benchmarks:  
icc -m32  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64

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## Peak Compiler Invocation (Continued)

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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